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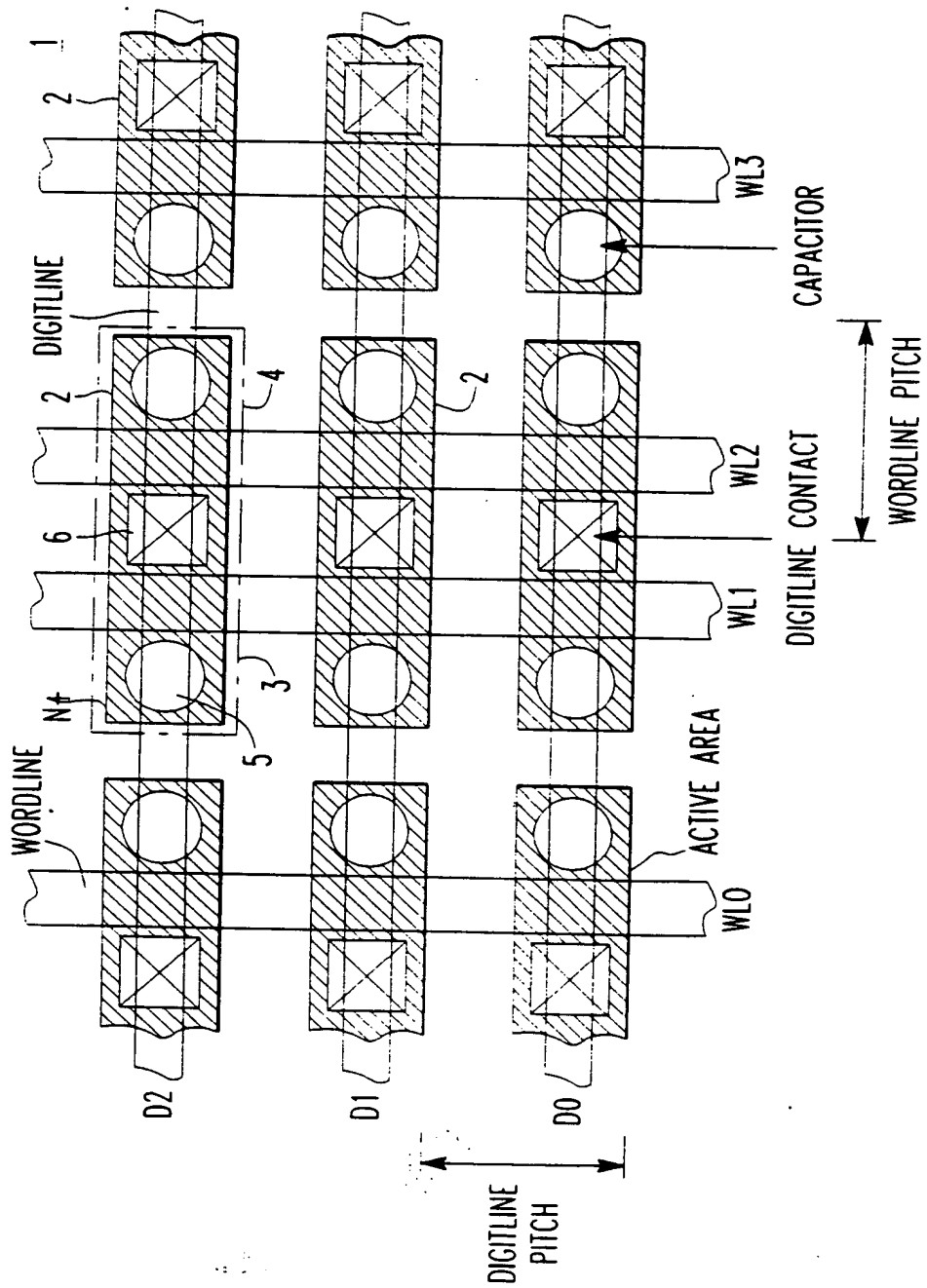


FIG. 1

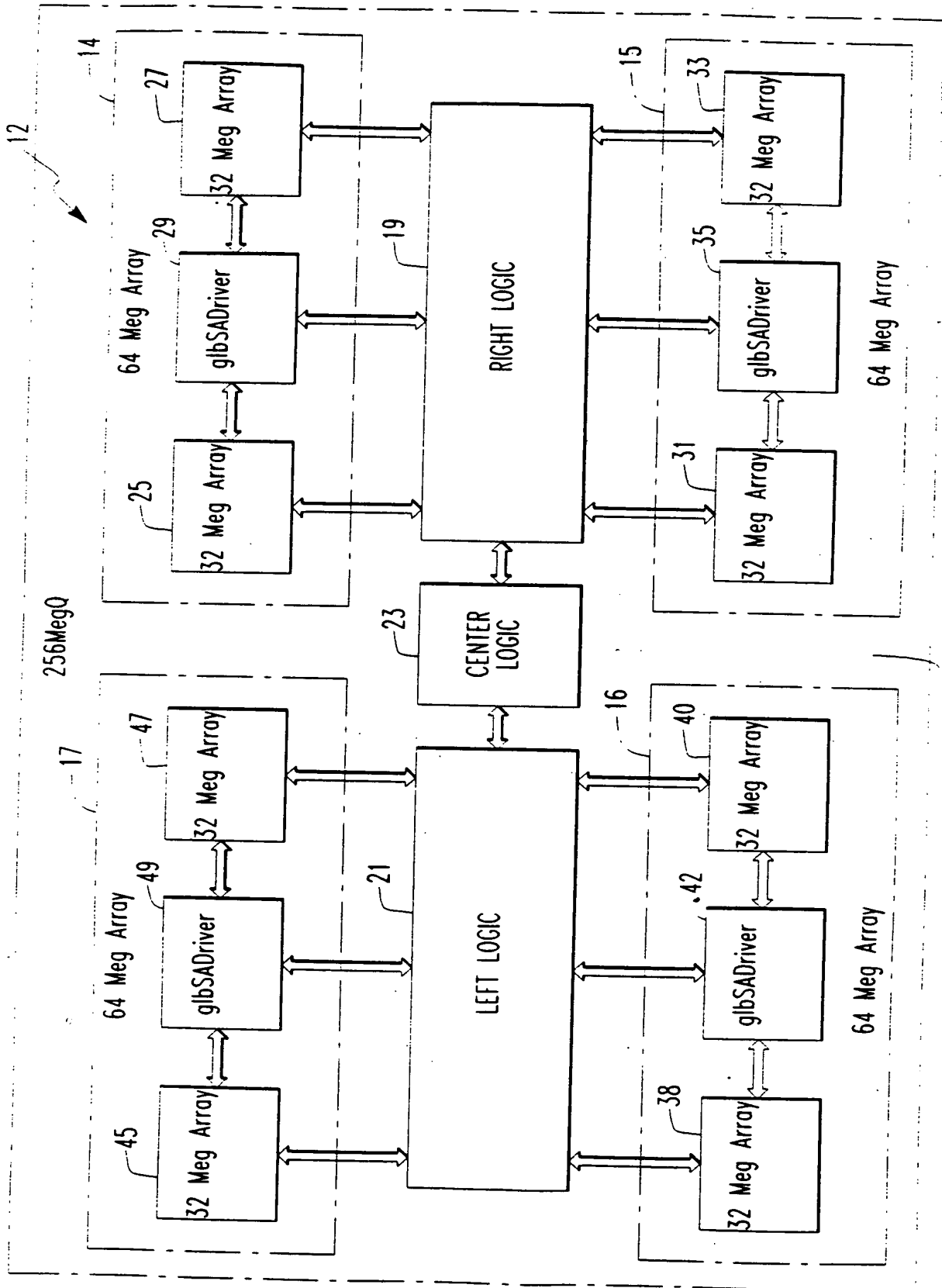
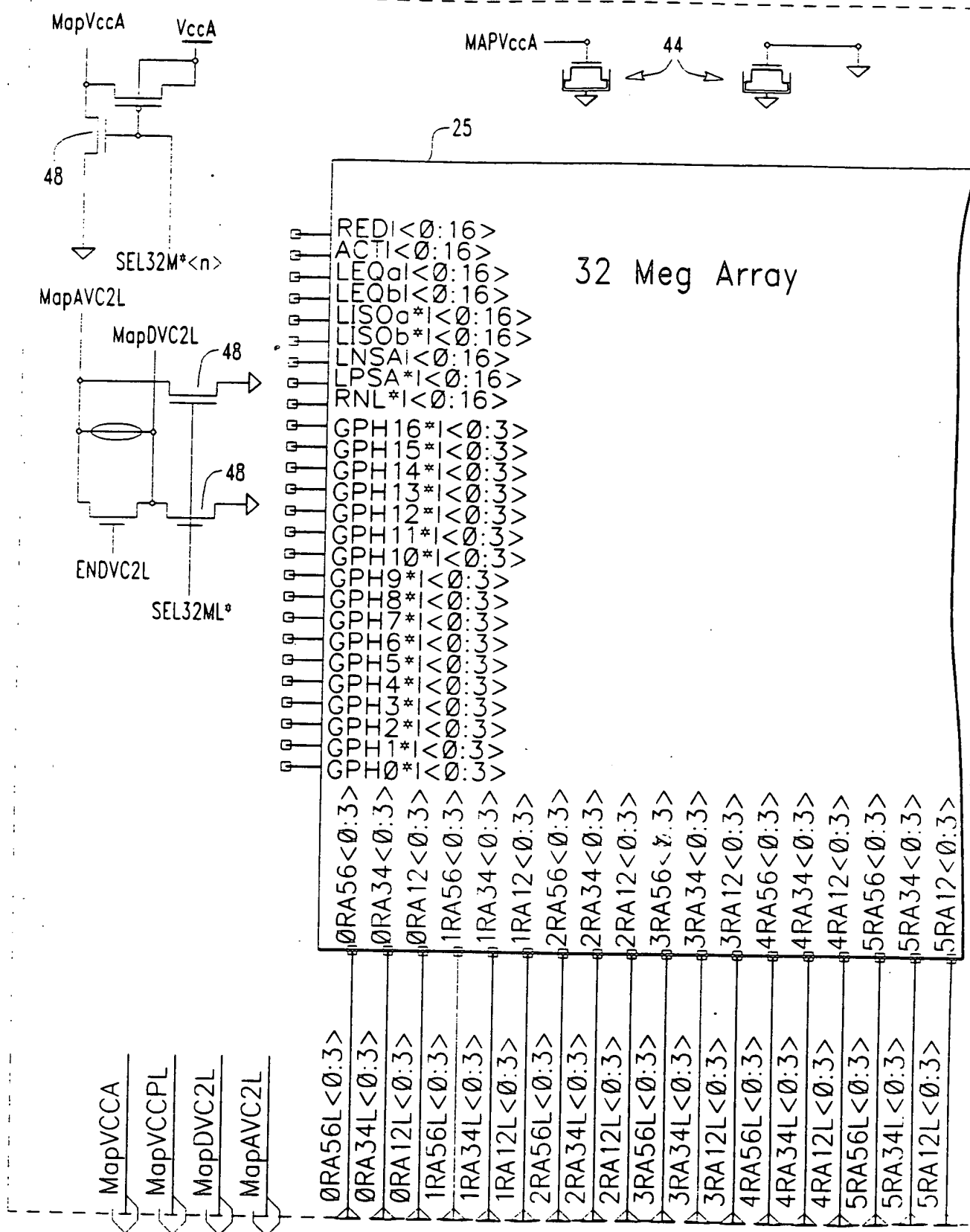
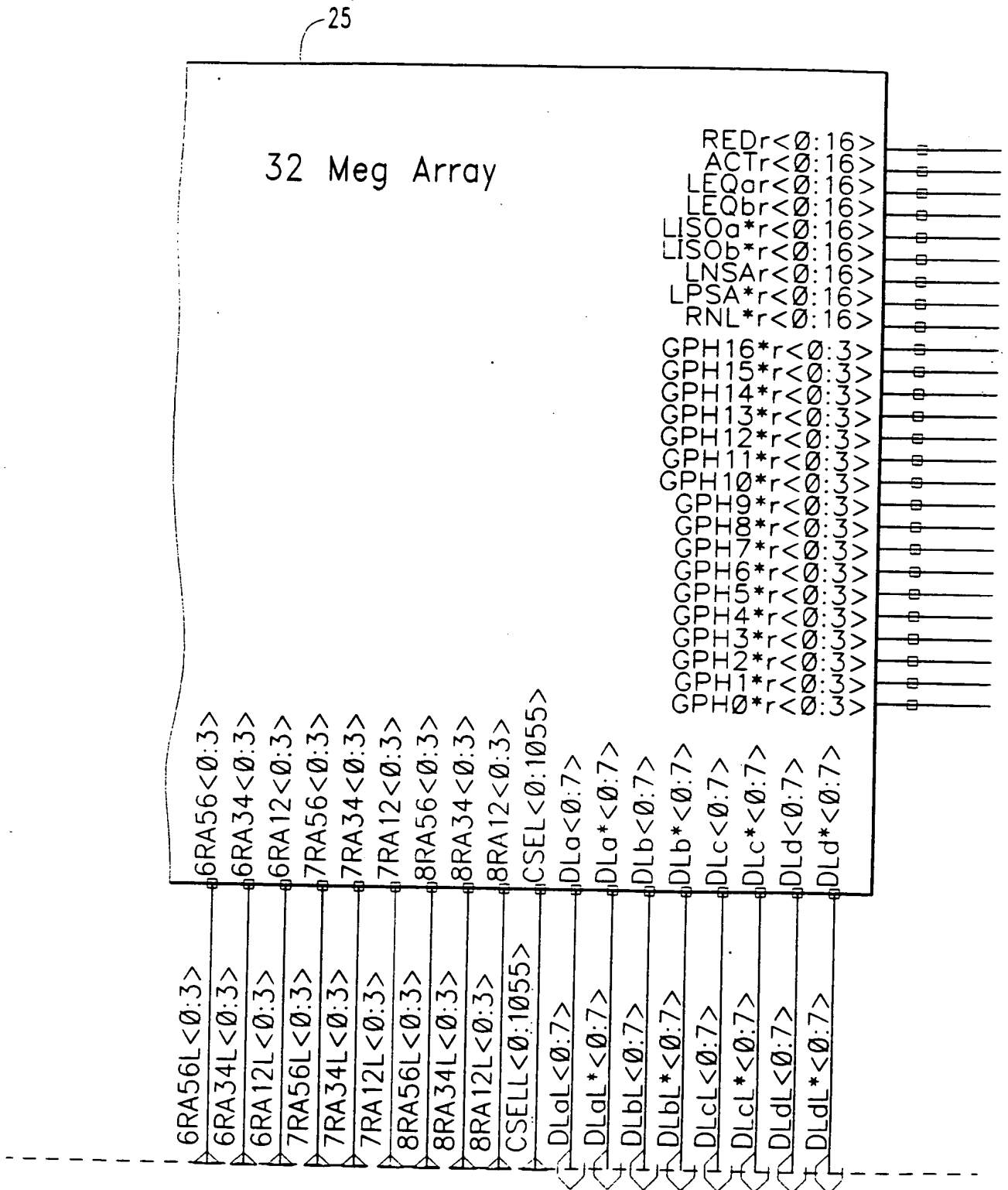


FIG. 2

FIG. 3A



[illegible]

- 29

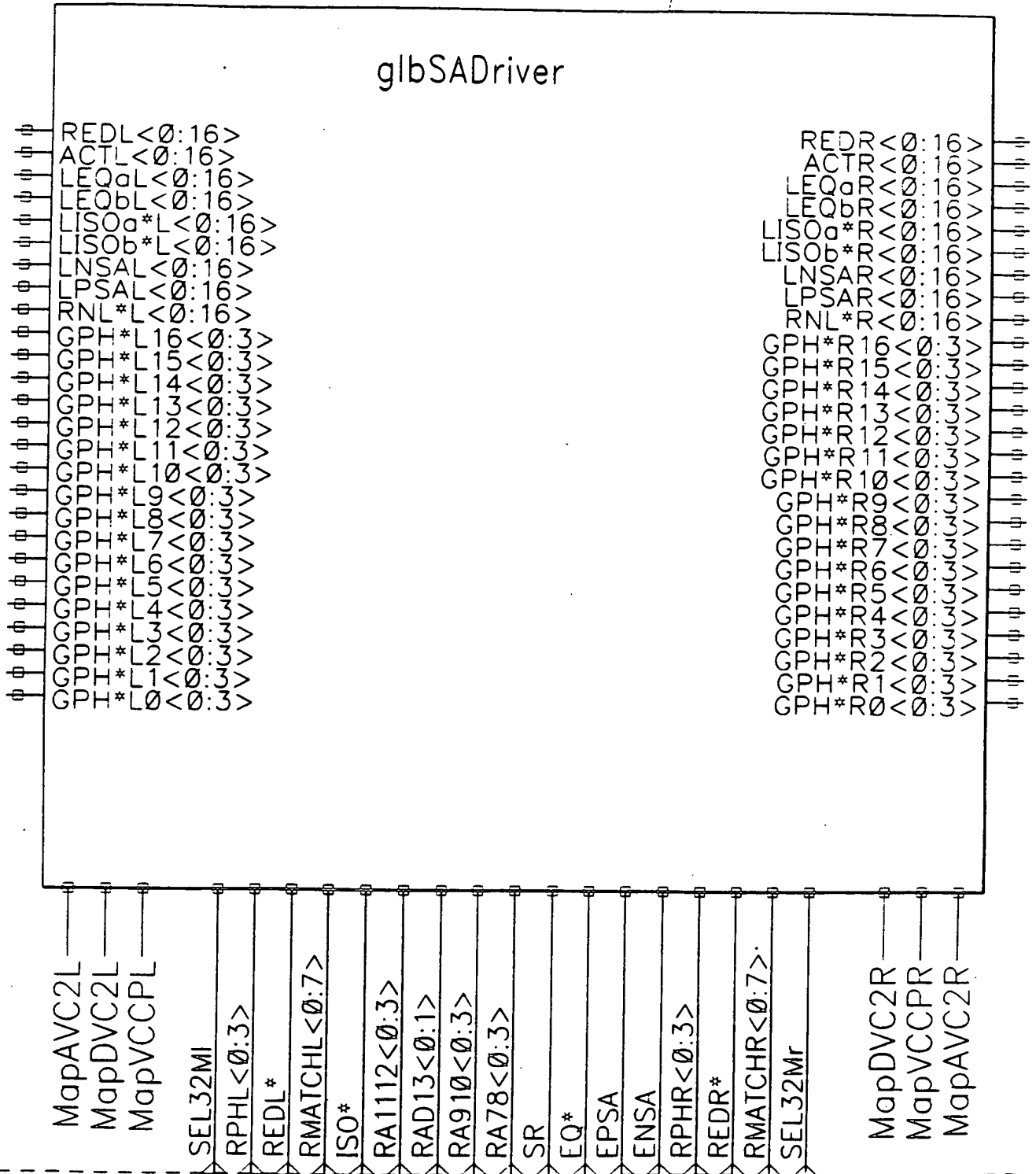


FIG. 3D

6,367

FIG. 3D 6,367

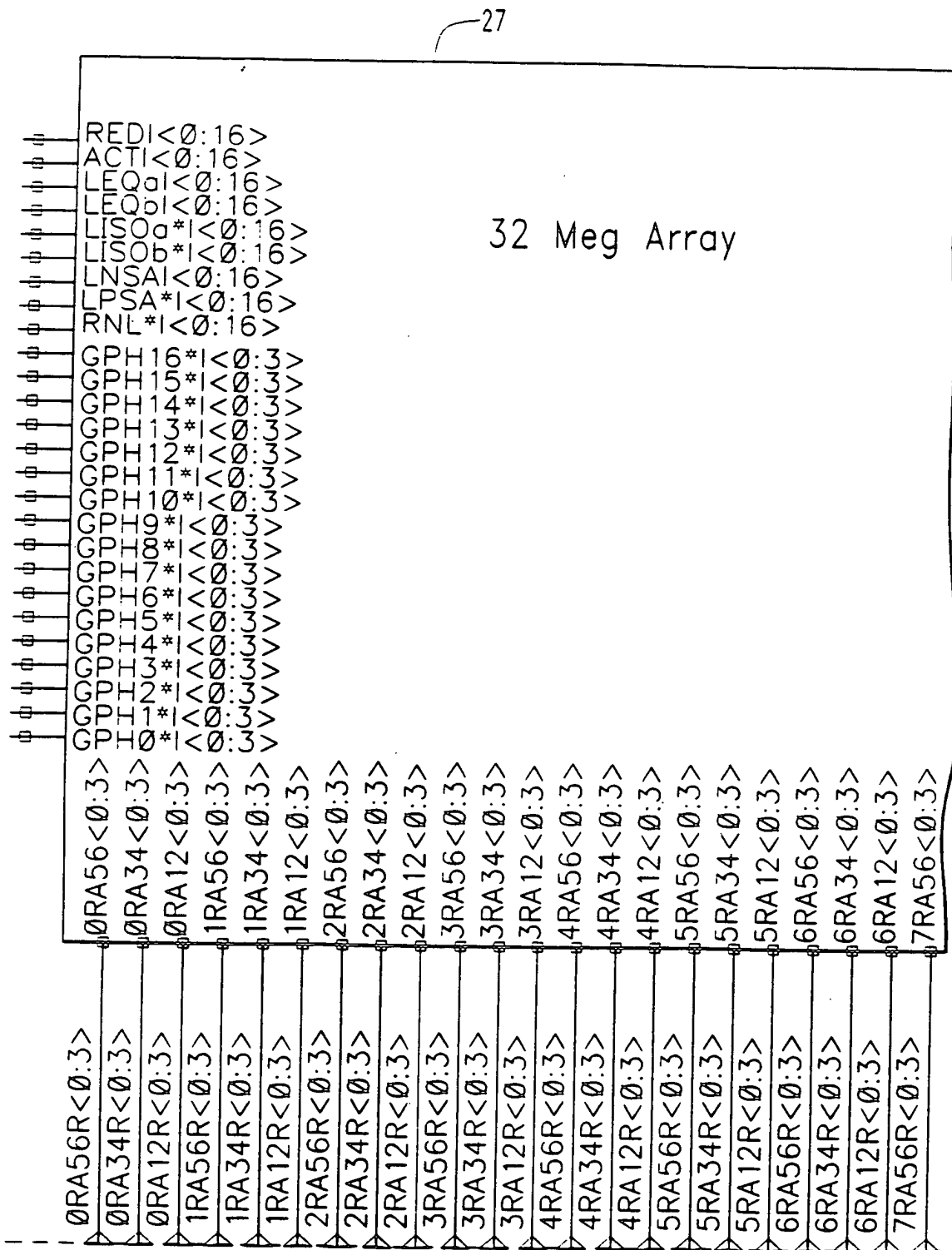
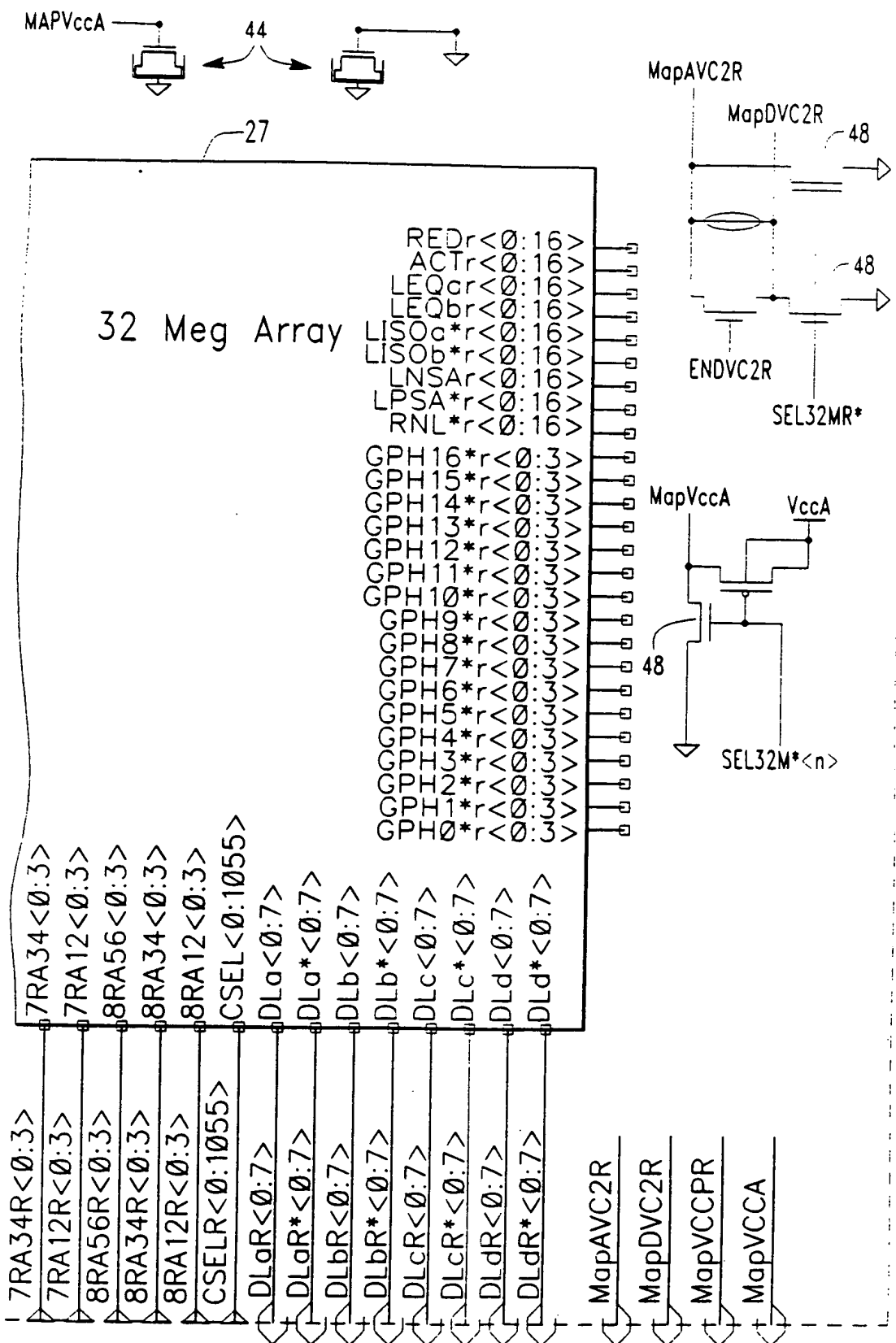


FIG. 3E

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32MEG ARRAY

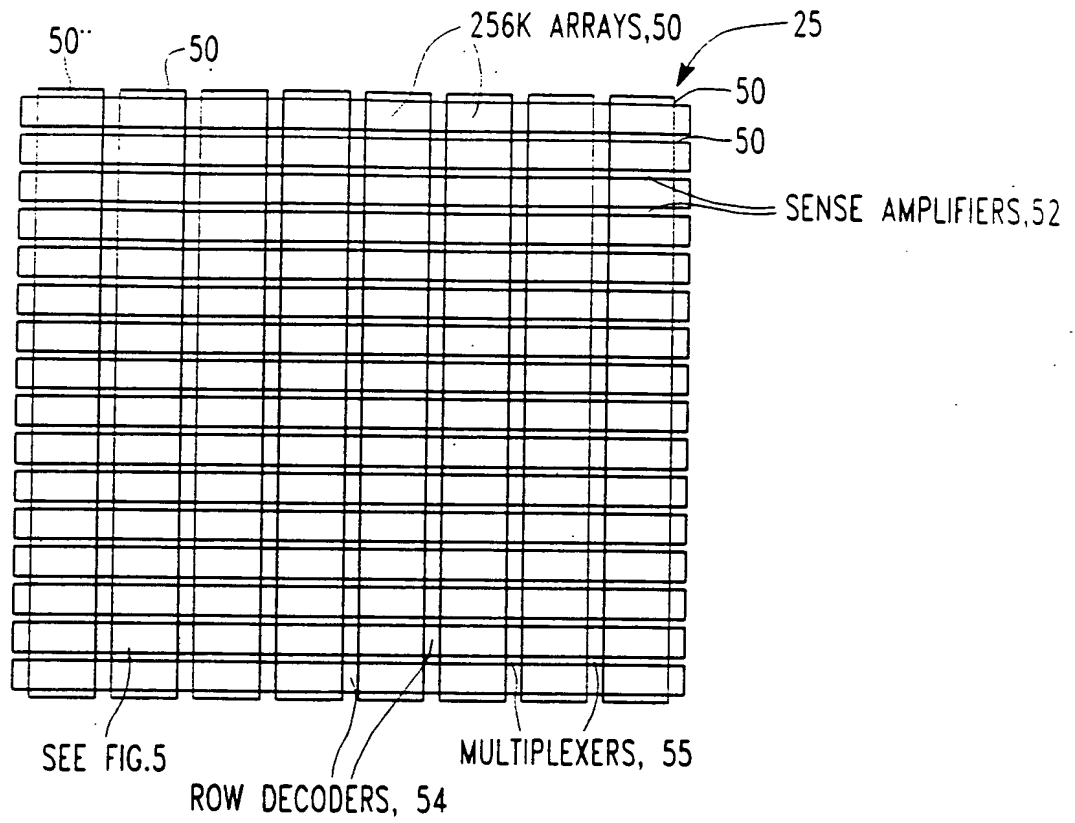


FIG. 4

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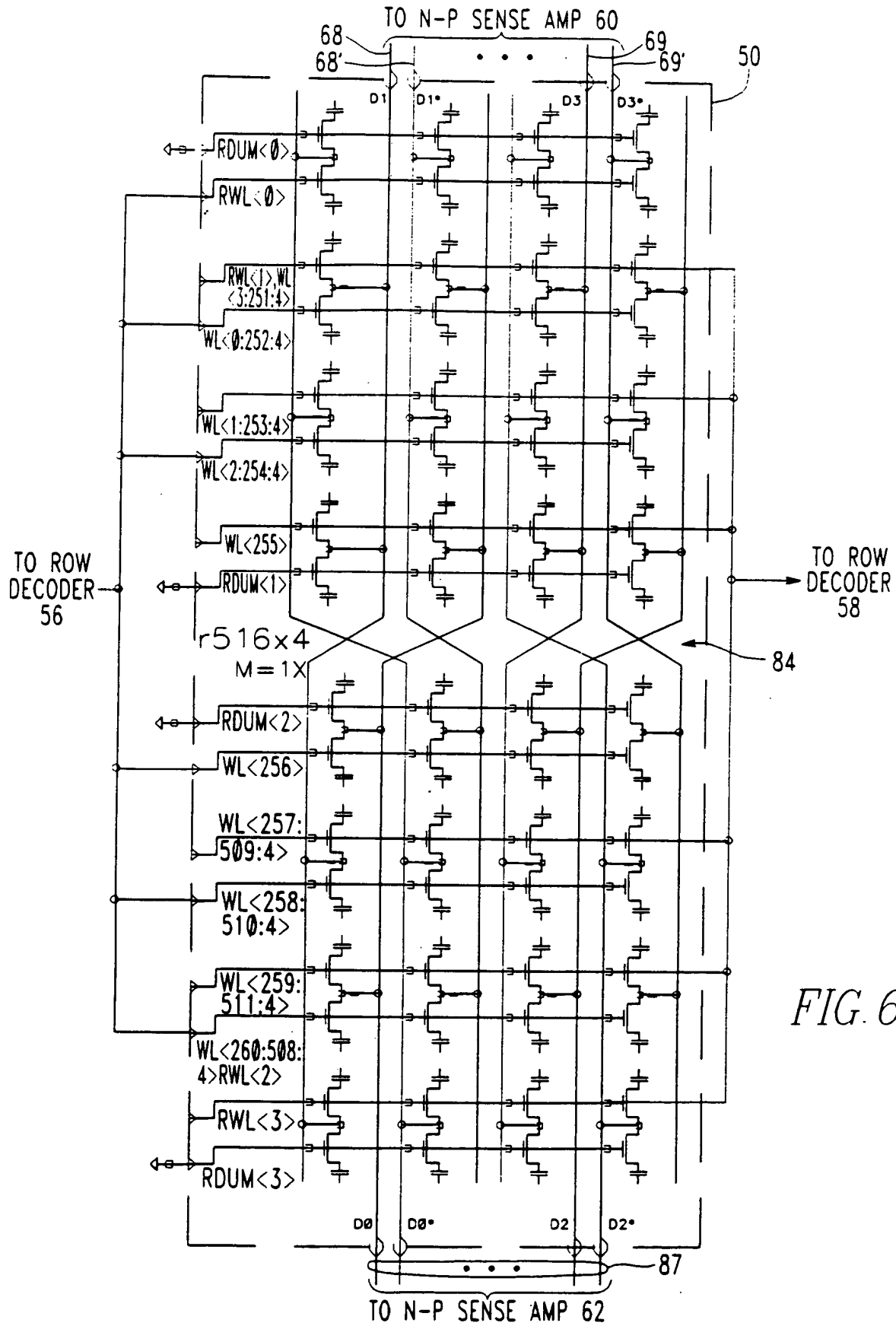
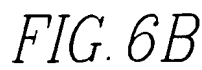
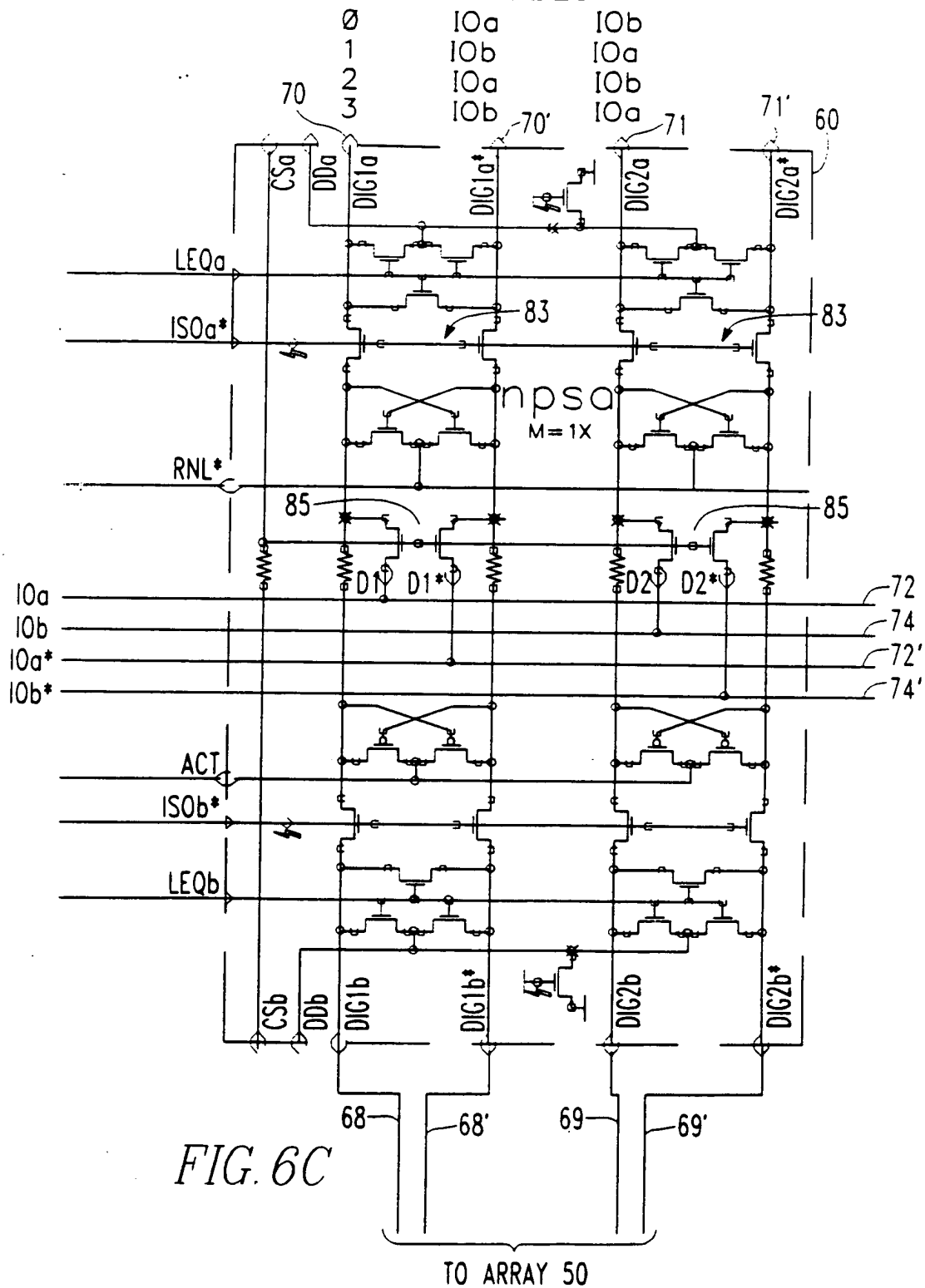


FIG. 6A



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Connections of odd/even
columns to IOa and IOb
alternates with odd/even
column select lines:
CA01* D1(even) D2(odd)



13,367

78

LNSA

LNSA

DATA
LINES

64

LEQa

LEQa

ISOa*

RNL*

IOab

IObb

IOab*

IObb*

IOat

IObt

IOat*

IObt*

IOa

IOb

IOa*

IOb*

63

AC

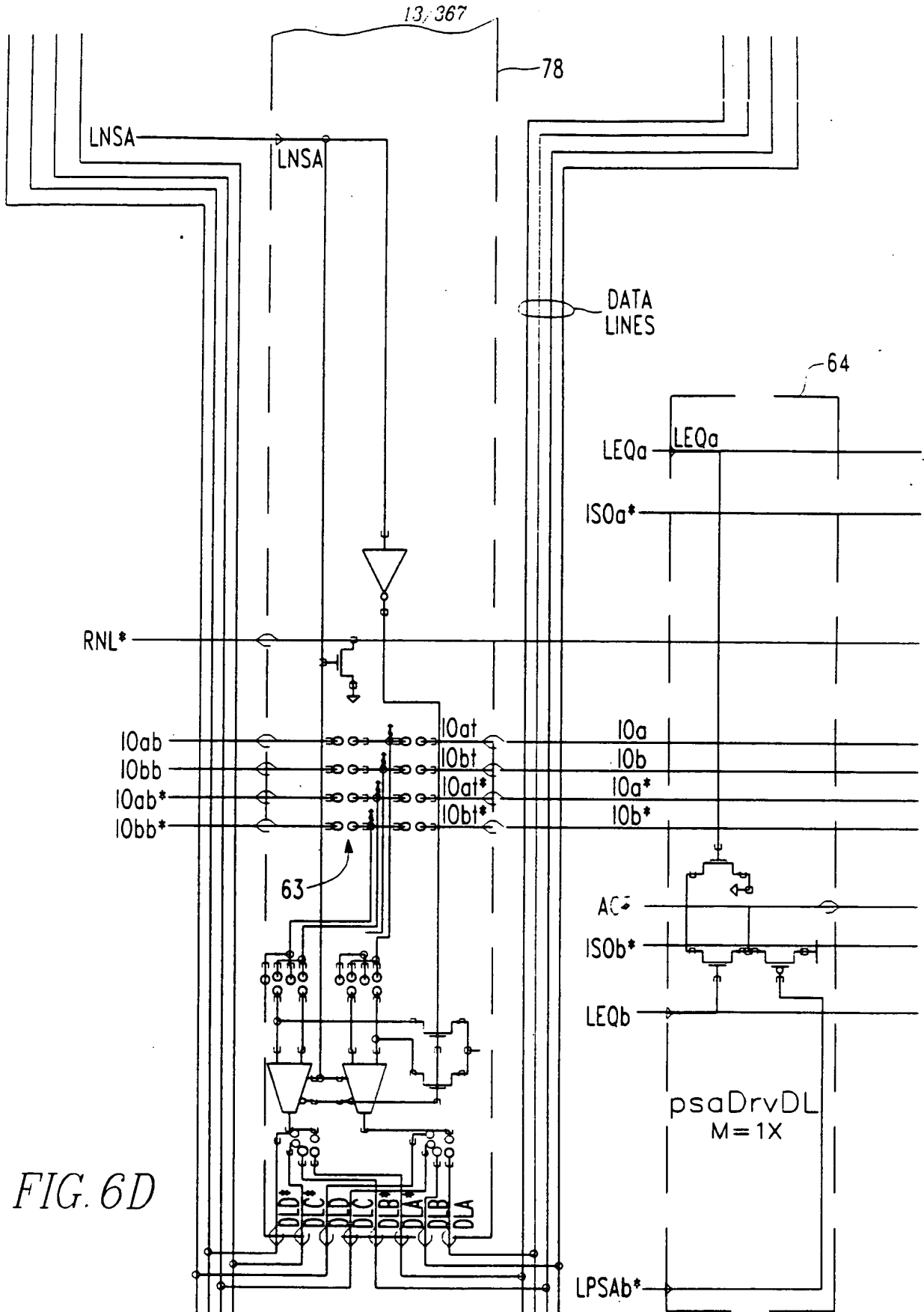
ISOb*

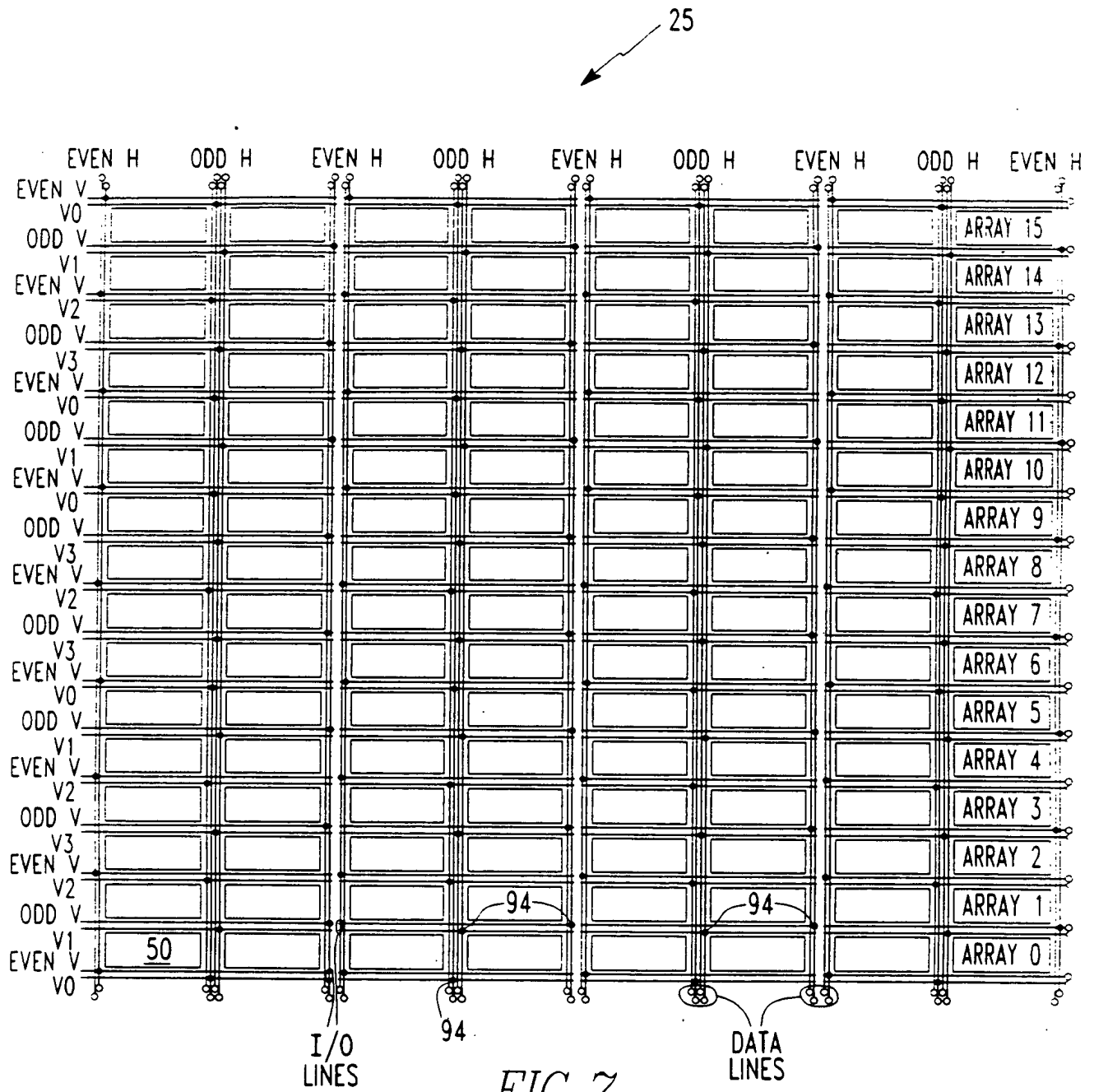
LEQb

psaDrvDL
M=1X

LPSAb*

FIG. 6D





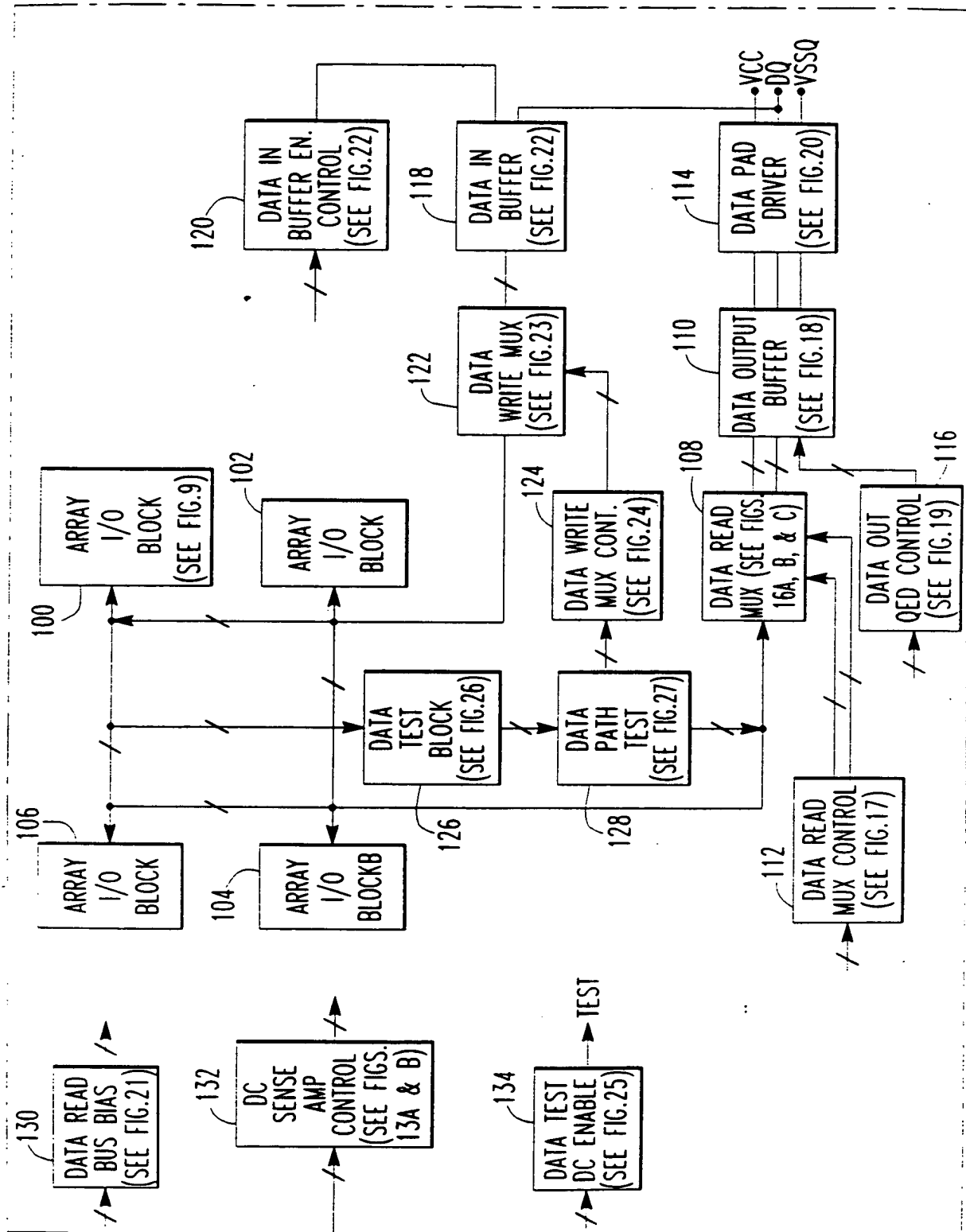


FIG. 8

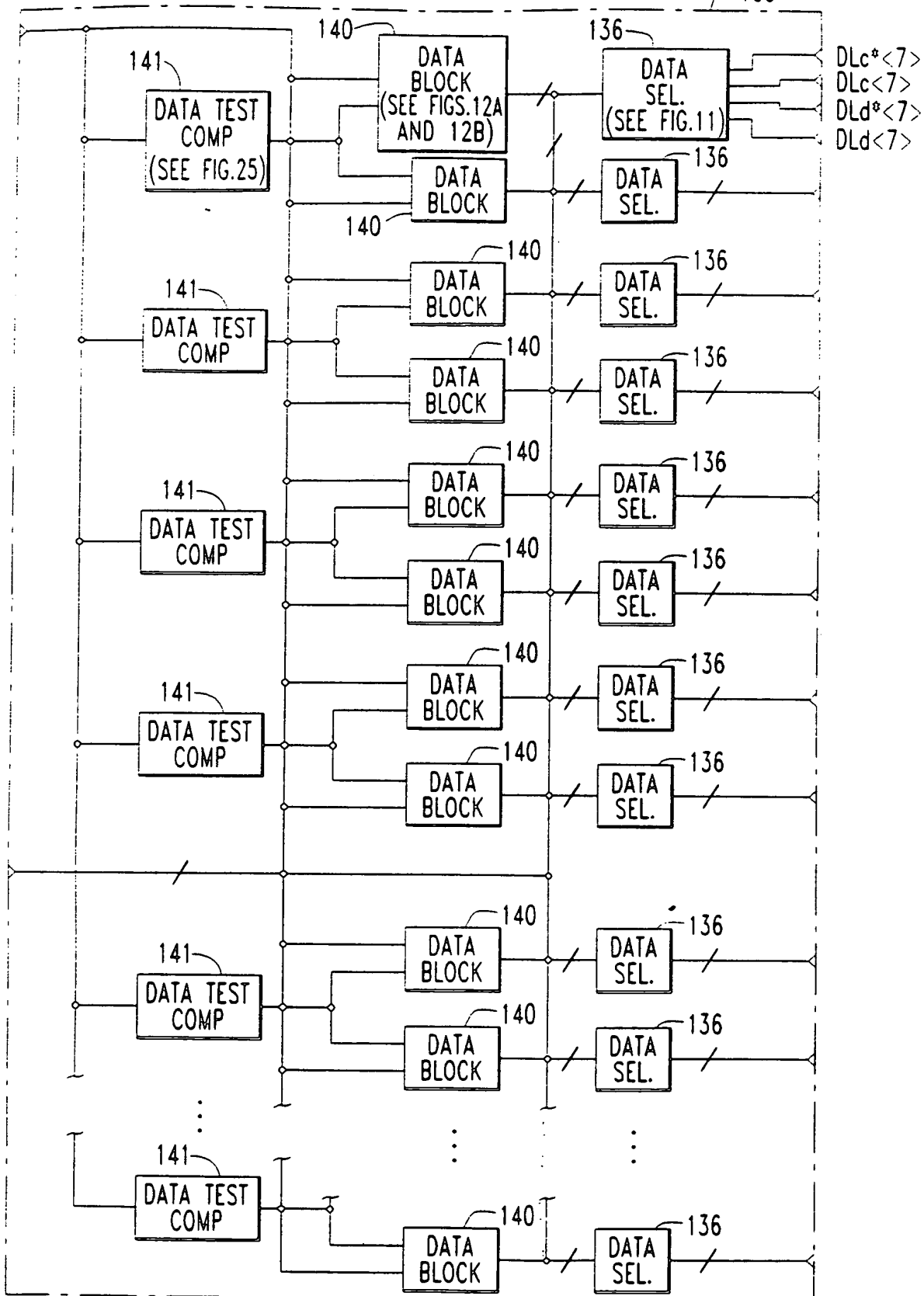
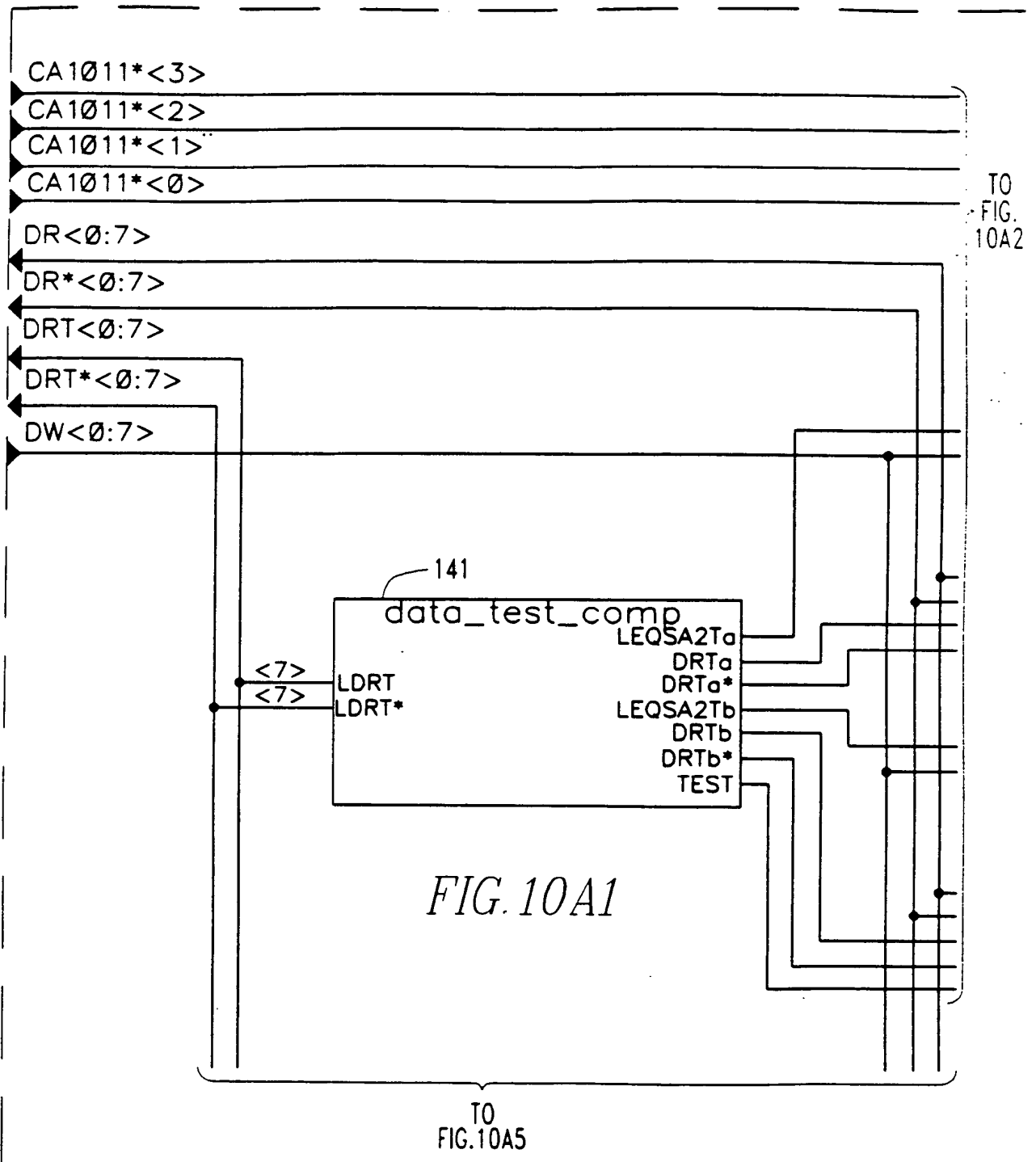


FIG. 9



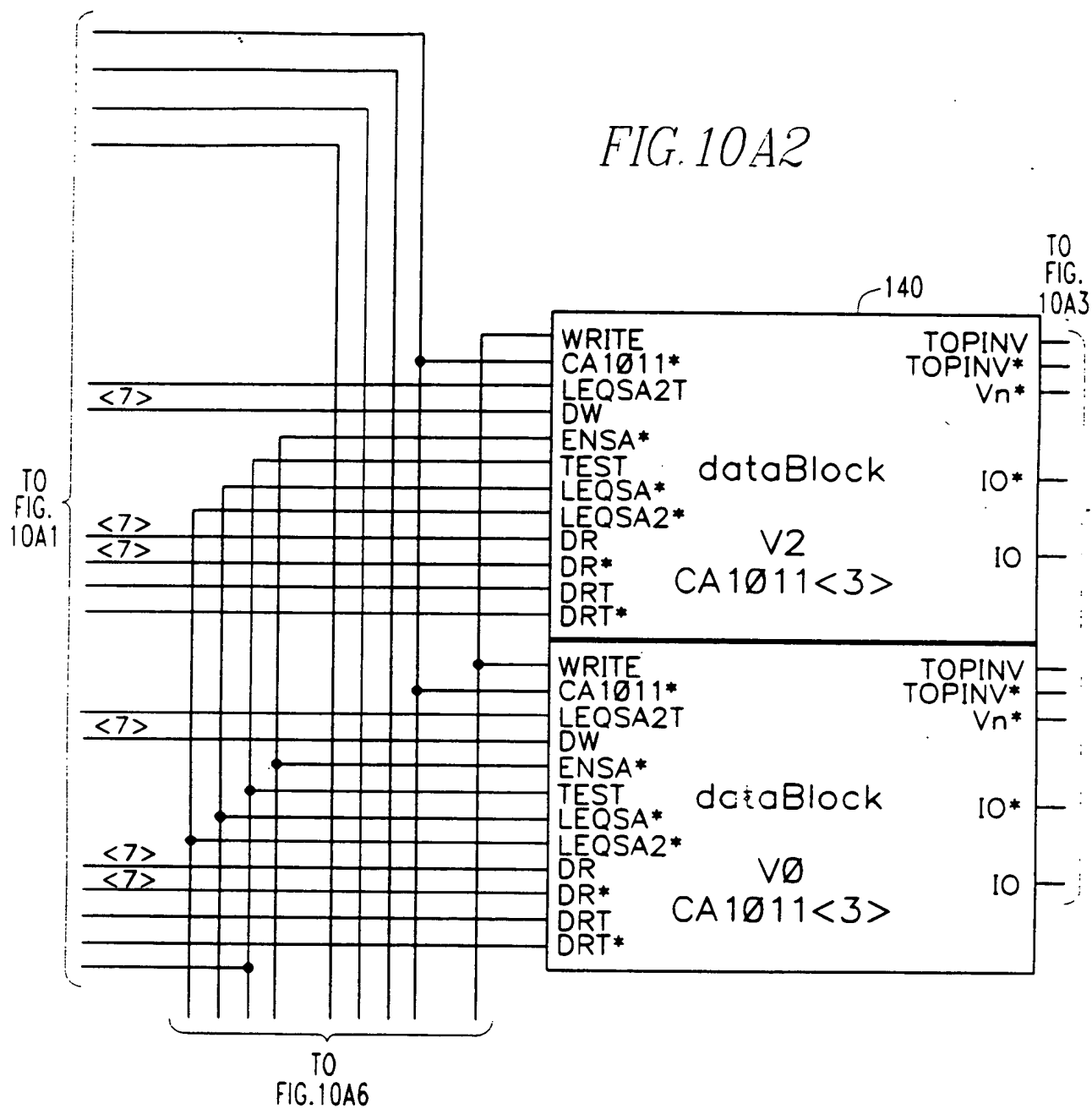
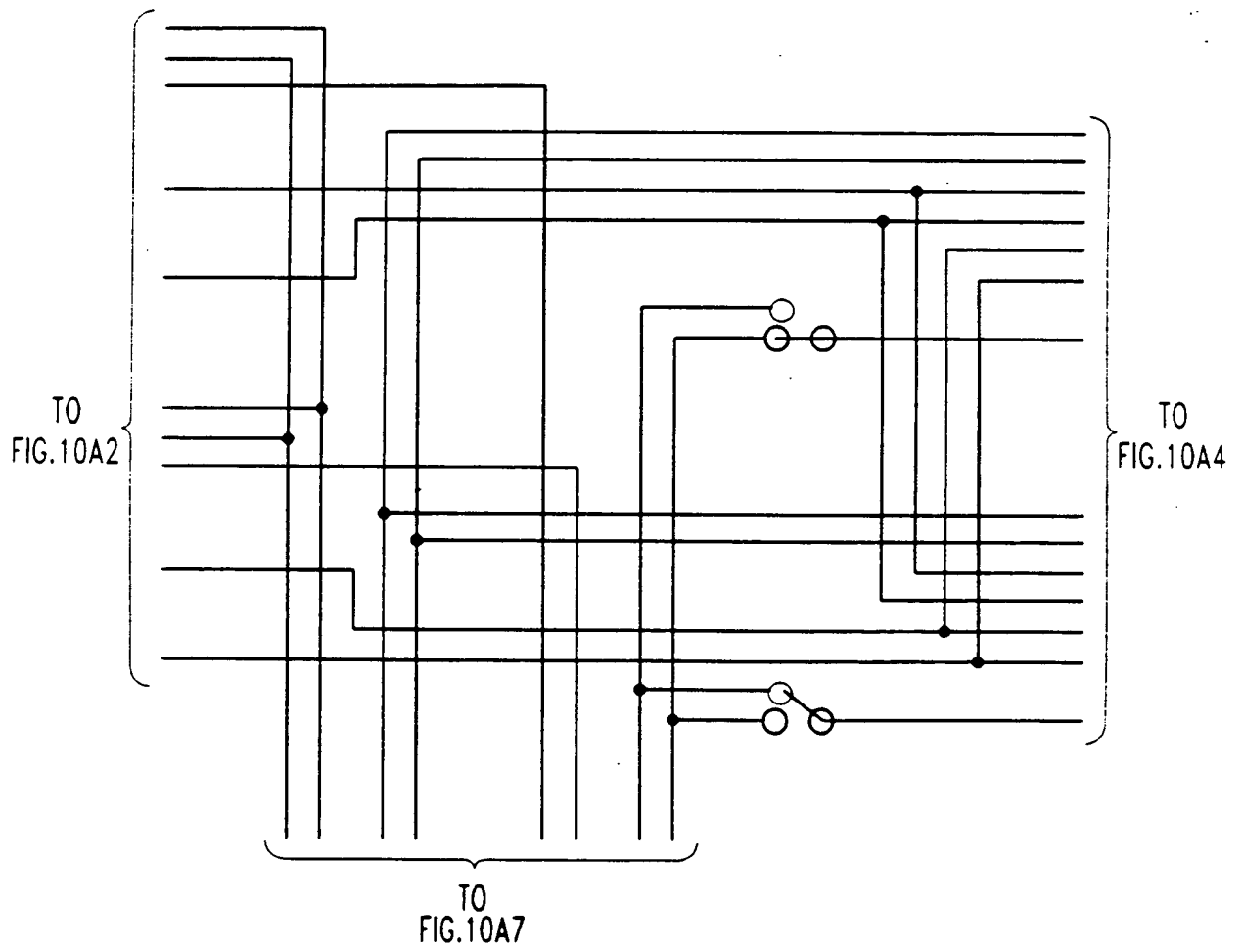


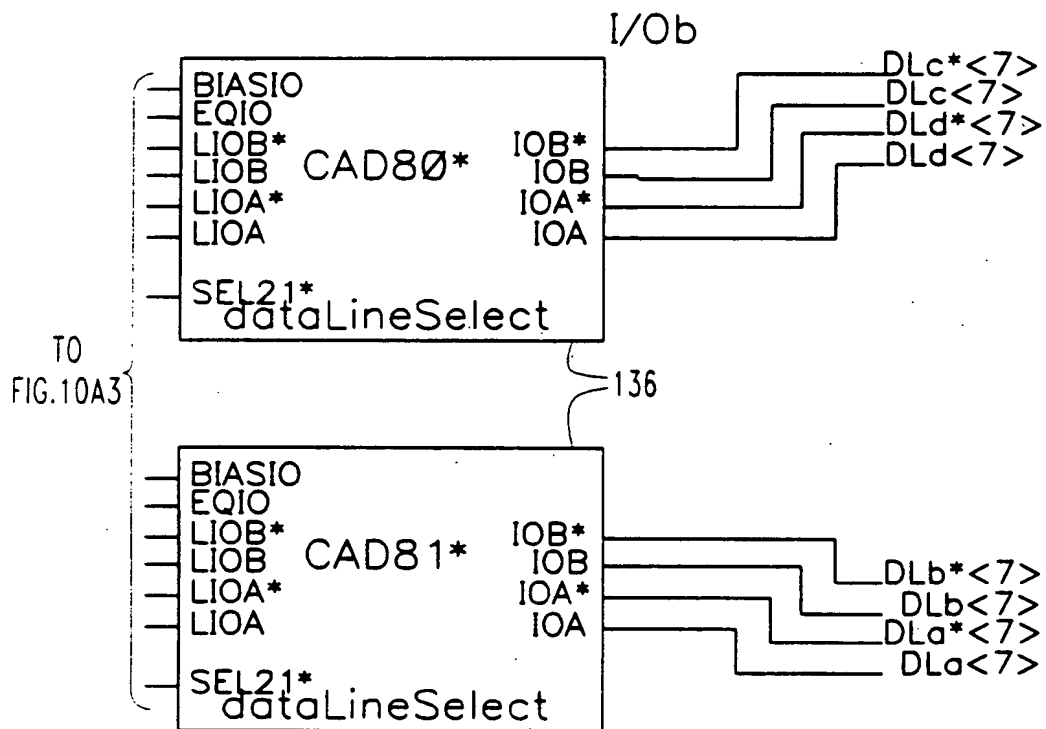
FIG. 10A3



arrayIOBlock

100

FIG. 10A4

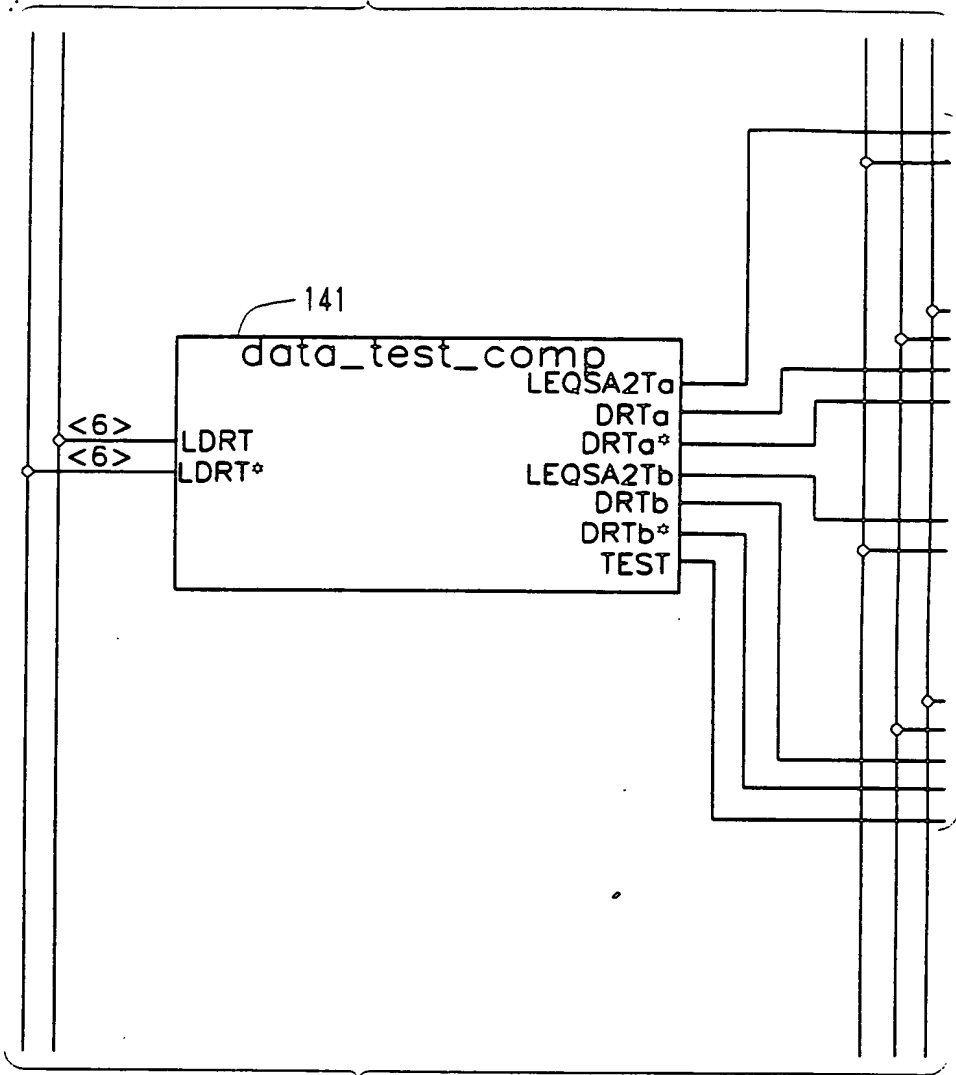


TO
FIG.10A1

TO
FIG.
10A6

TO
FIG.10B1

FIG.10A5



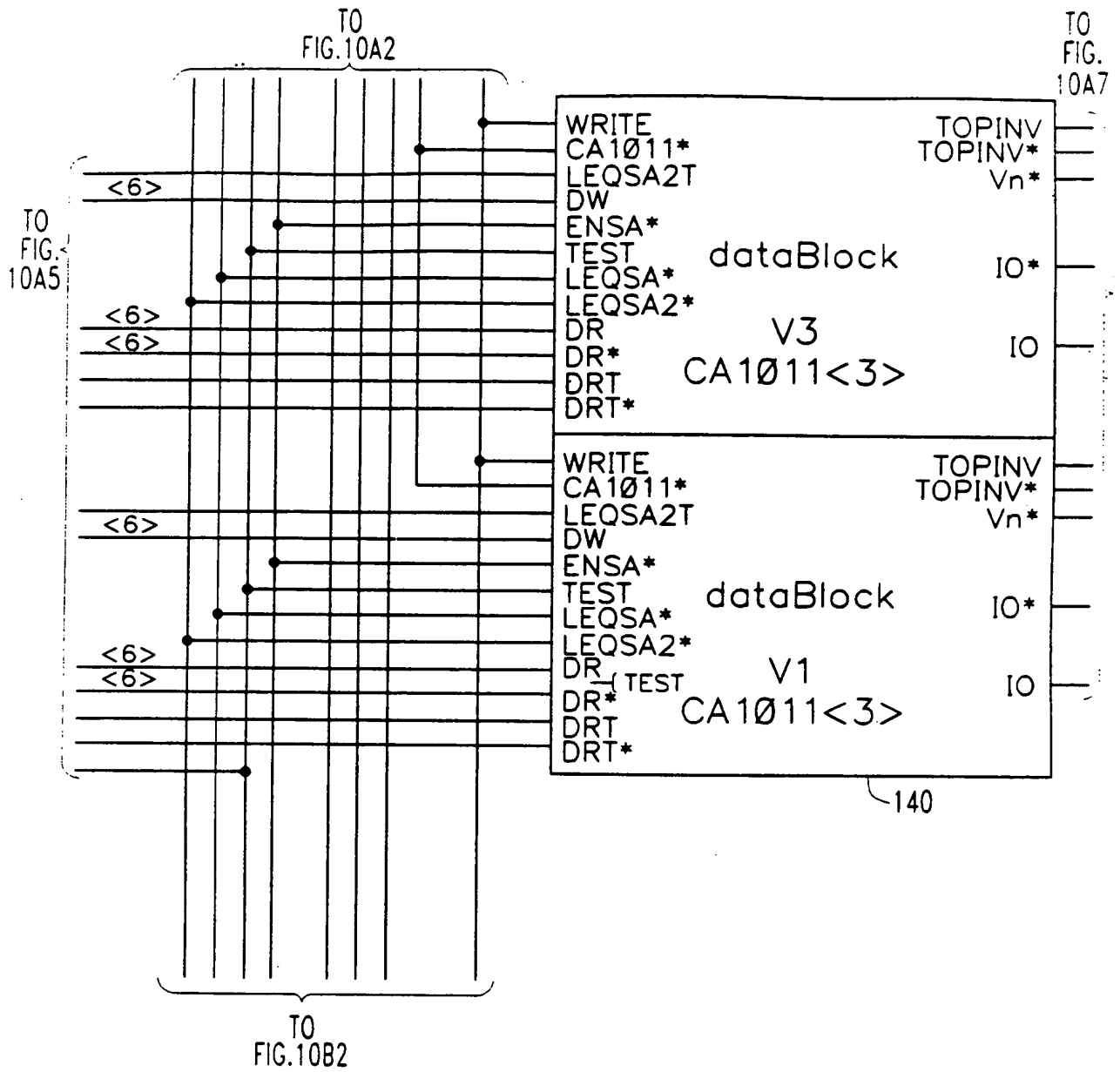


FIG. 10A6

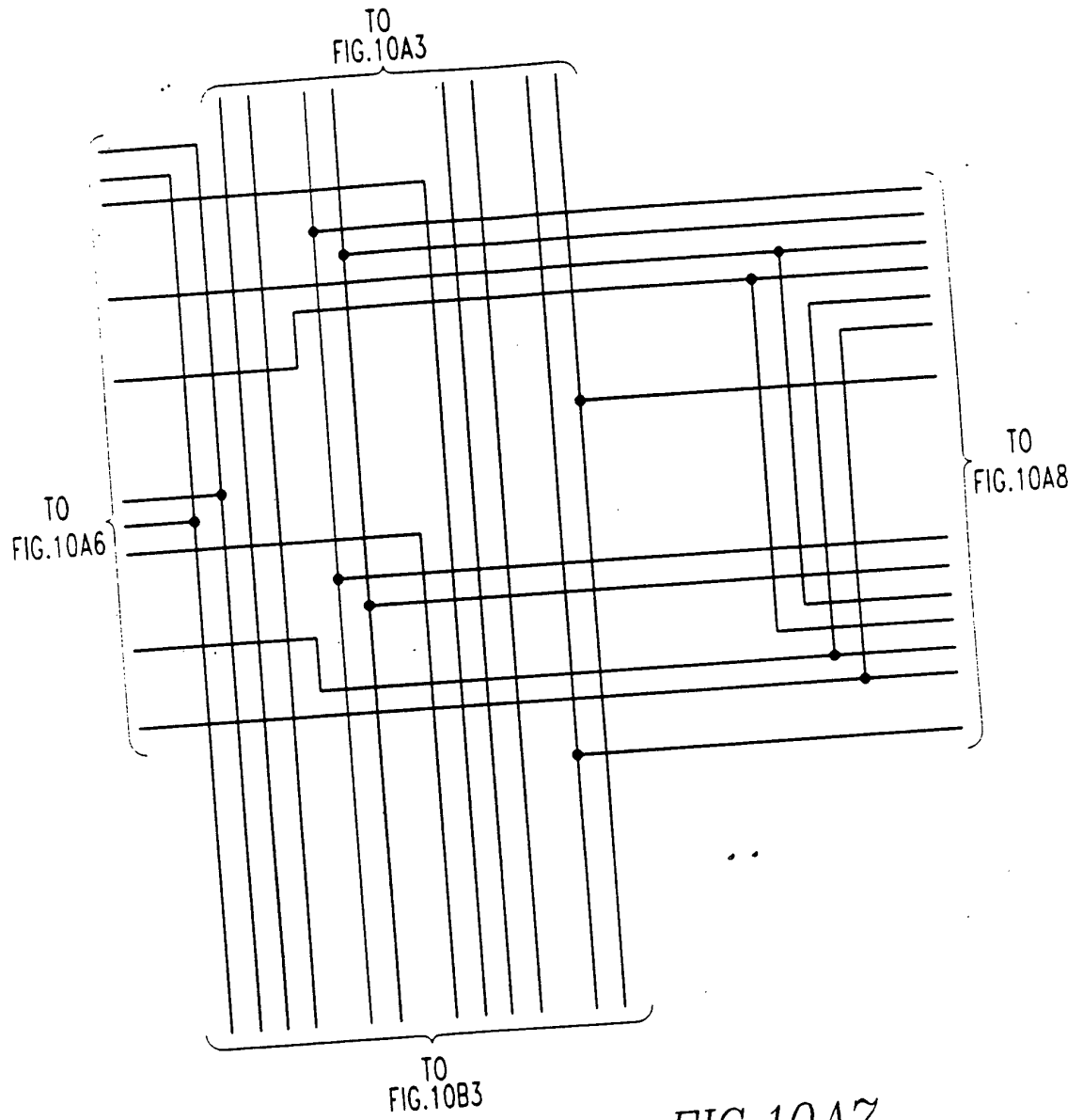


FIG. 10A7

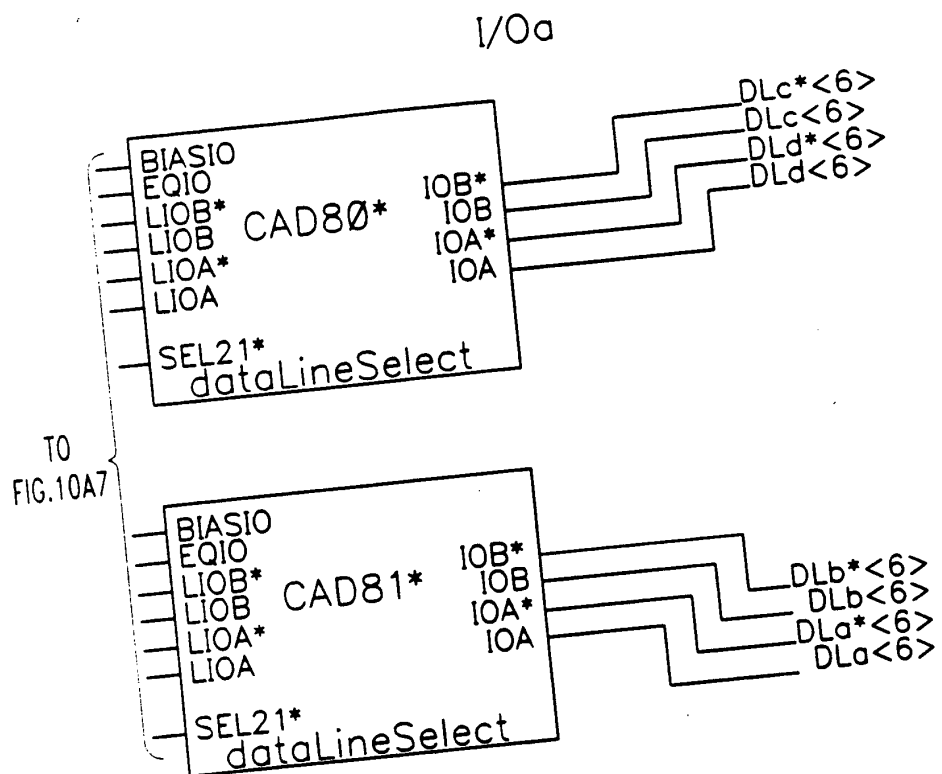


FIG. 10A8

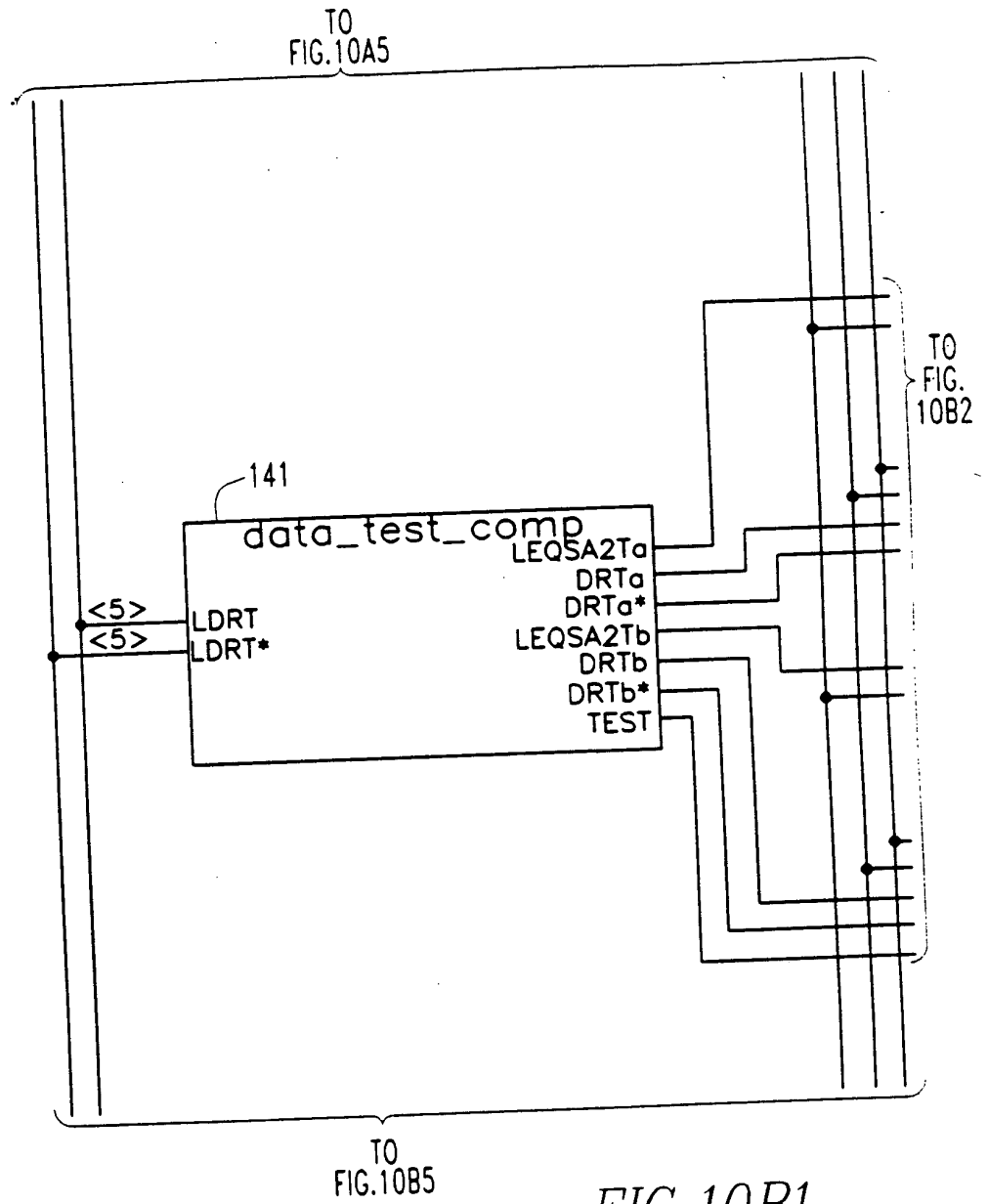


FIG. 10B1

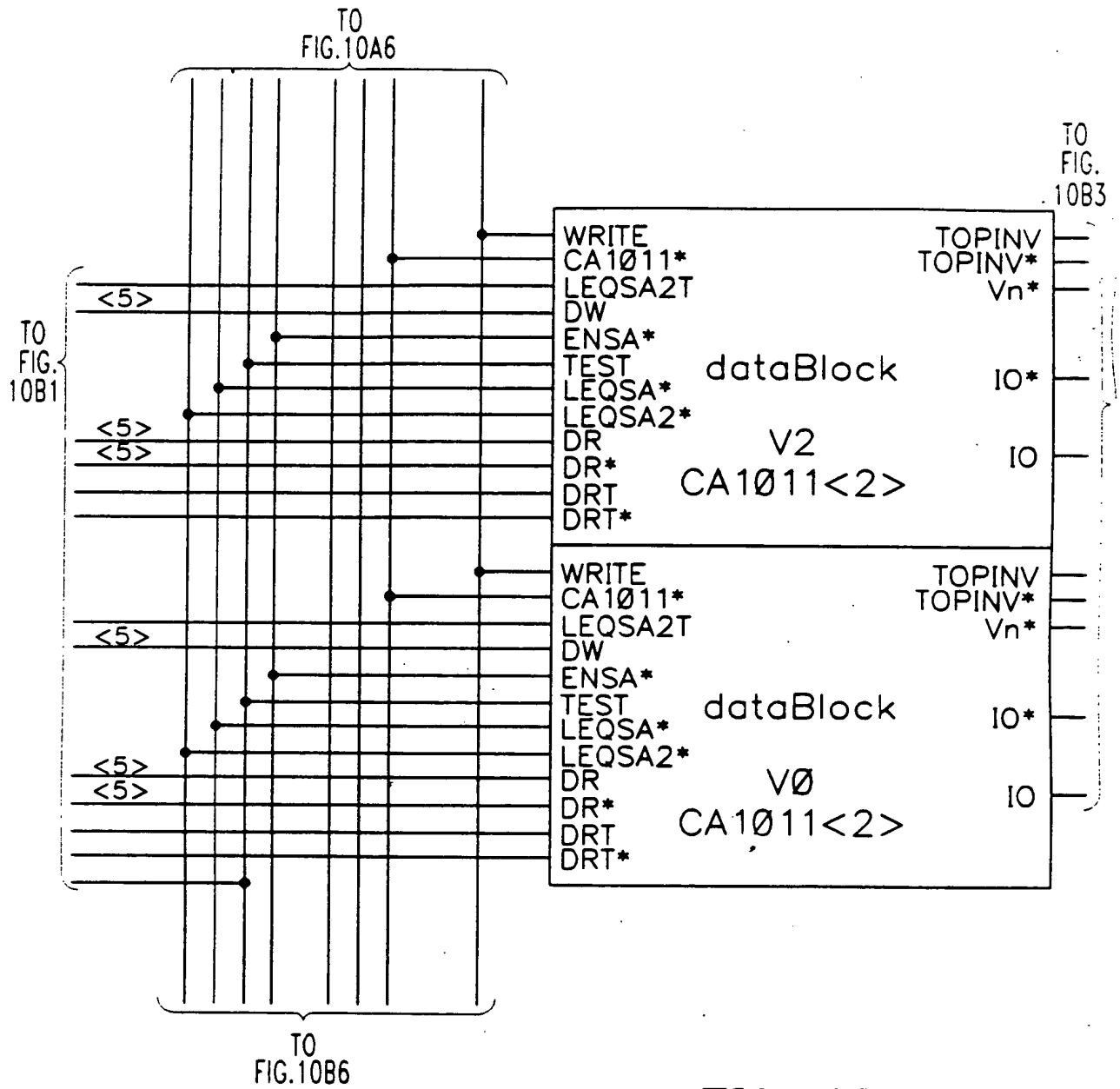
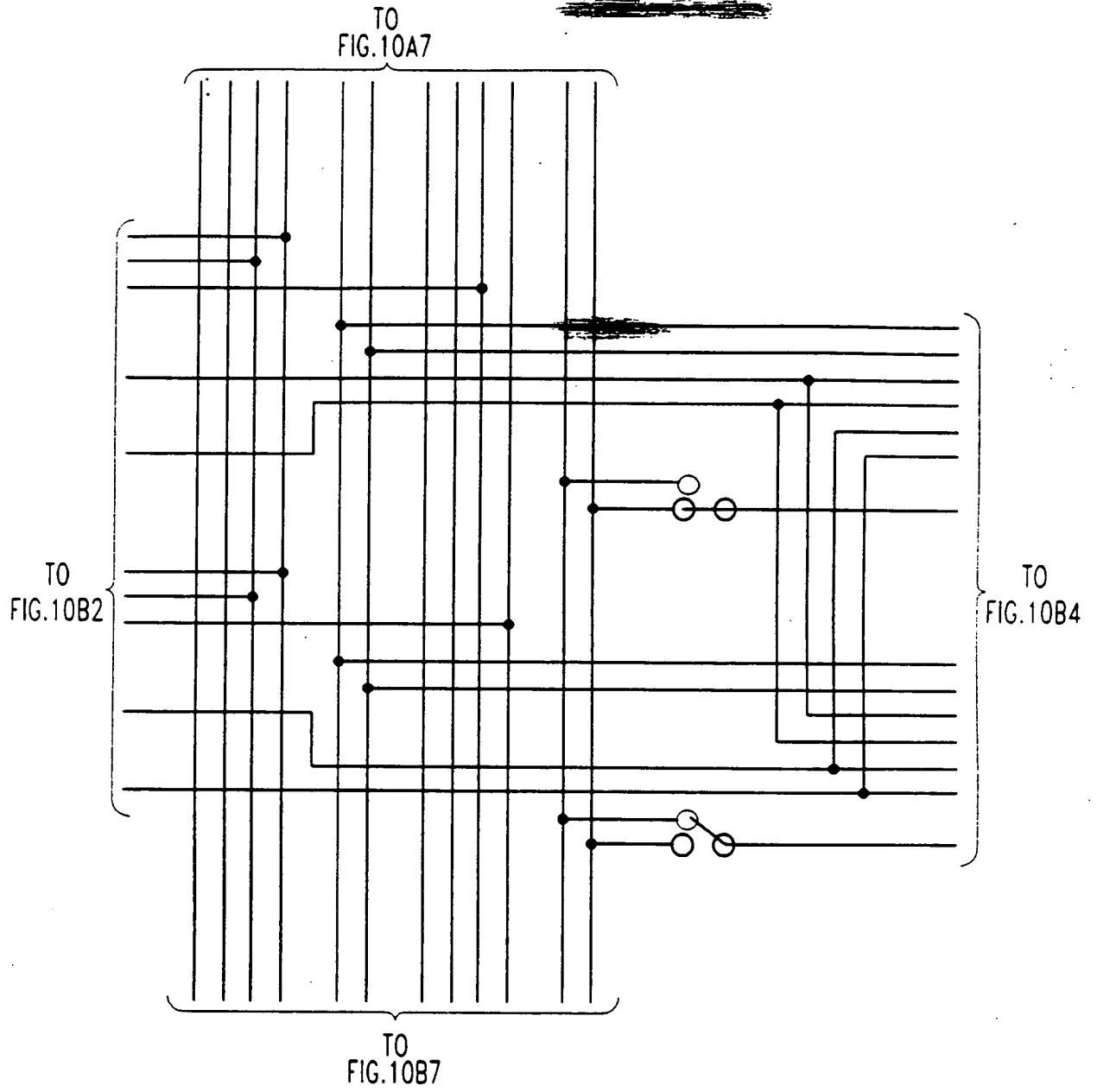


FIG. 10B2

*FIG. 10B3*

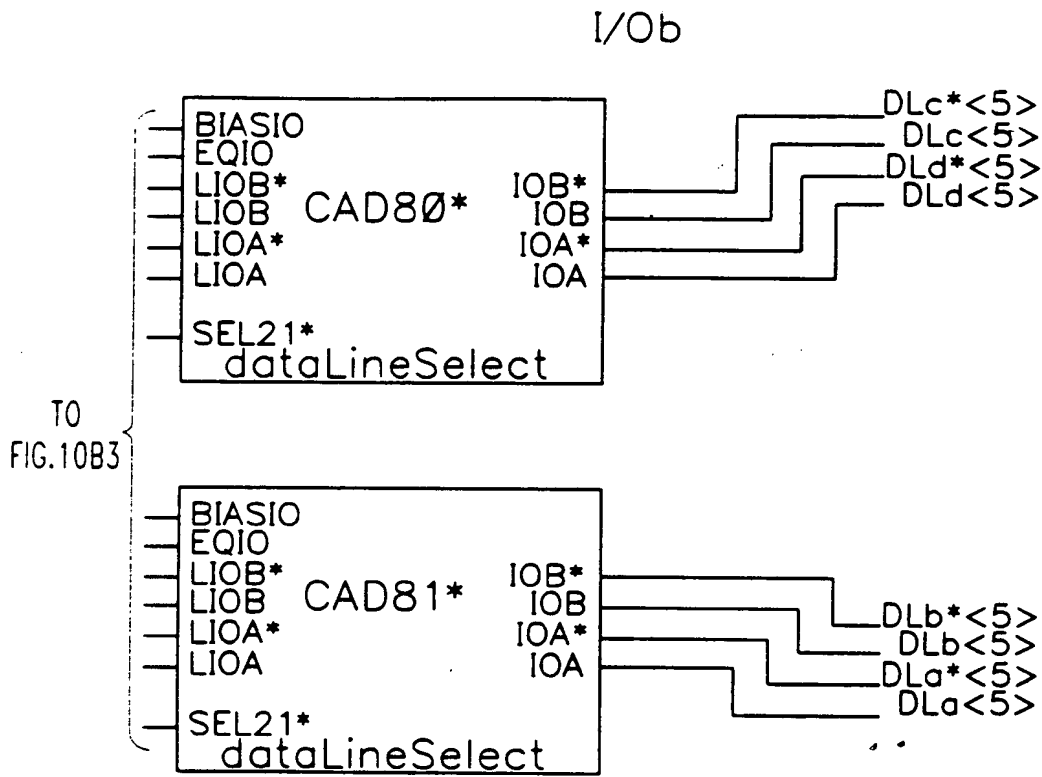
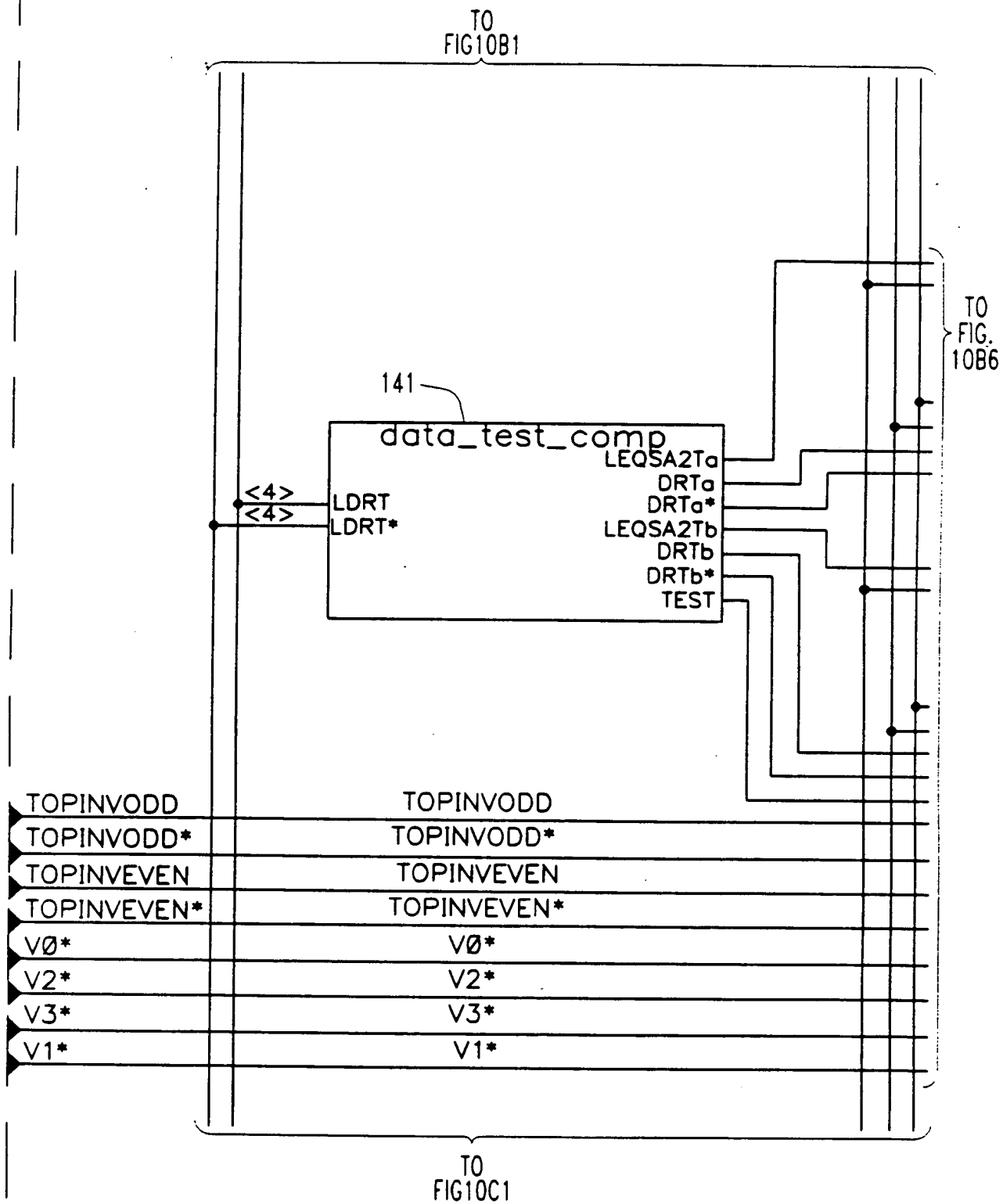


FIG.10B4

FIG. 10B5



TO
FIG. 10B2

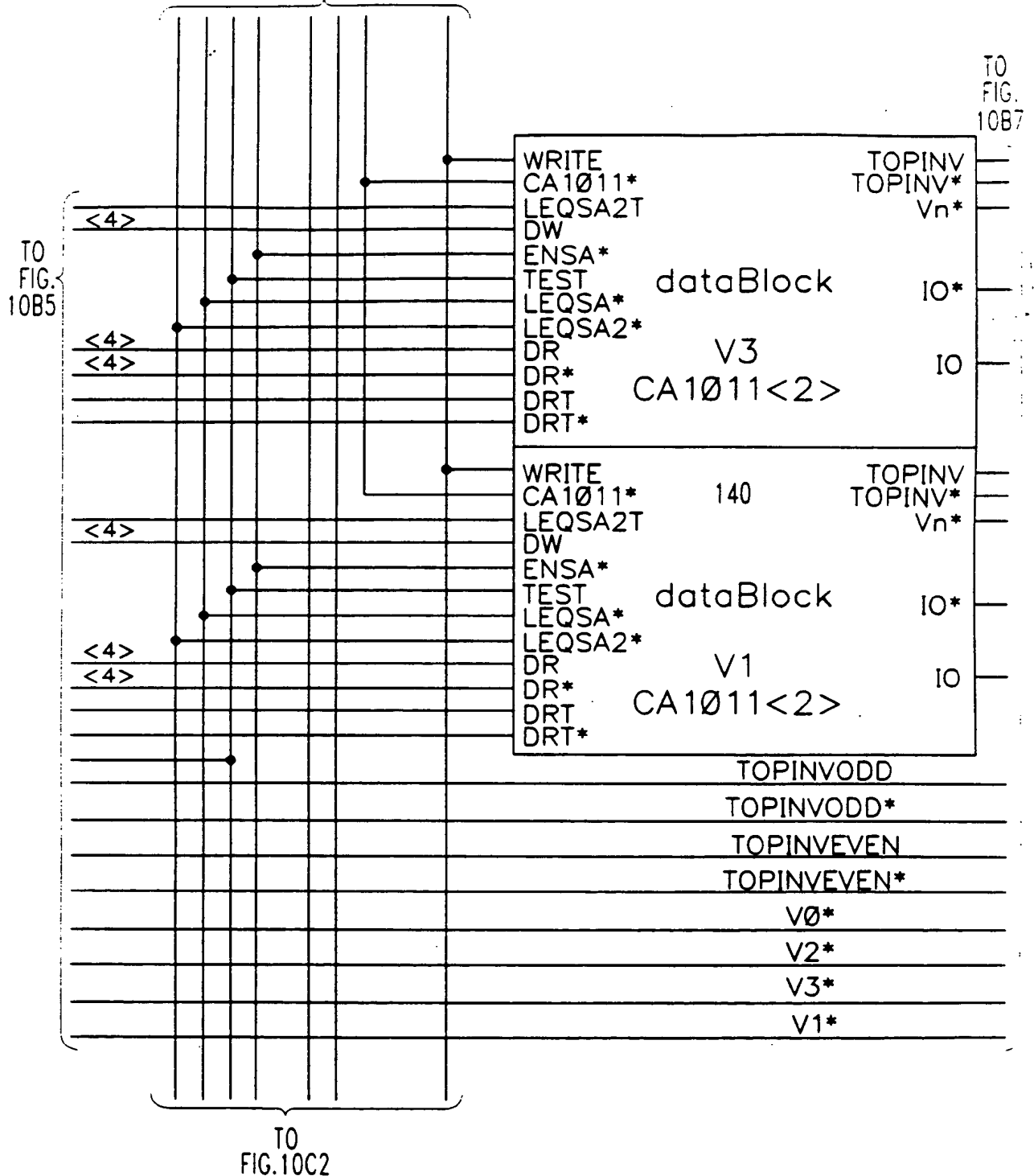


FIG. 10B7

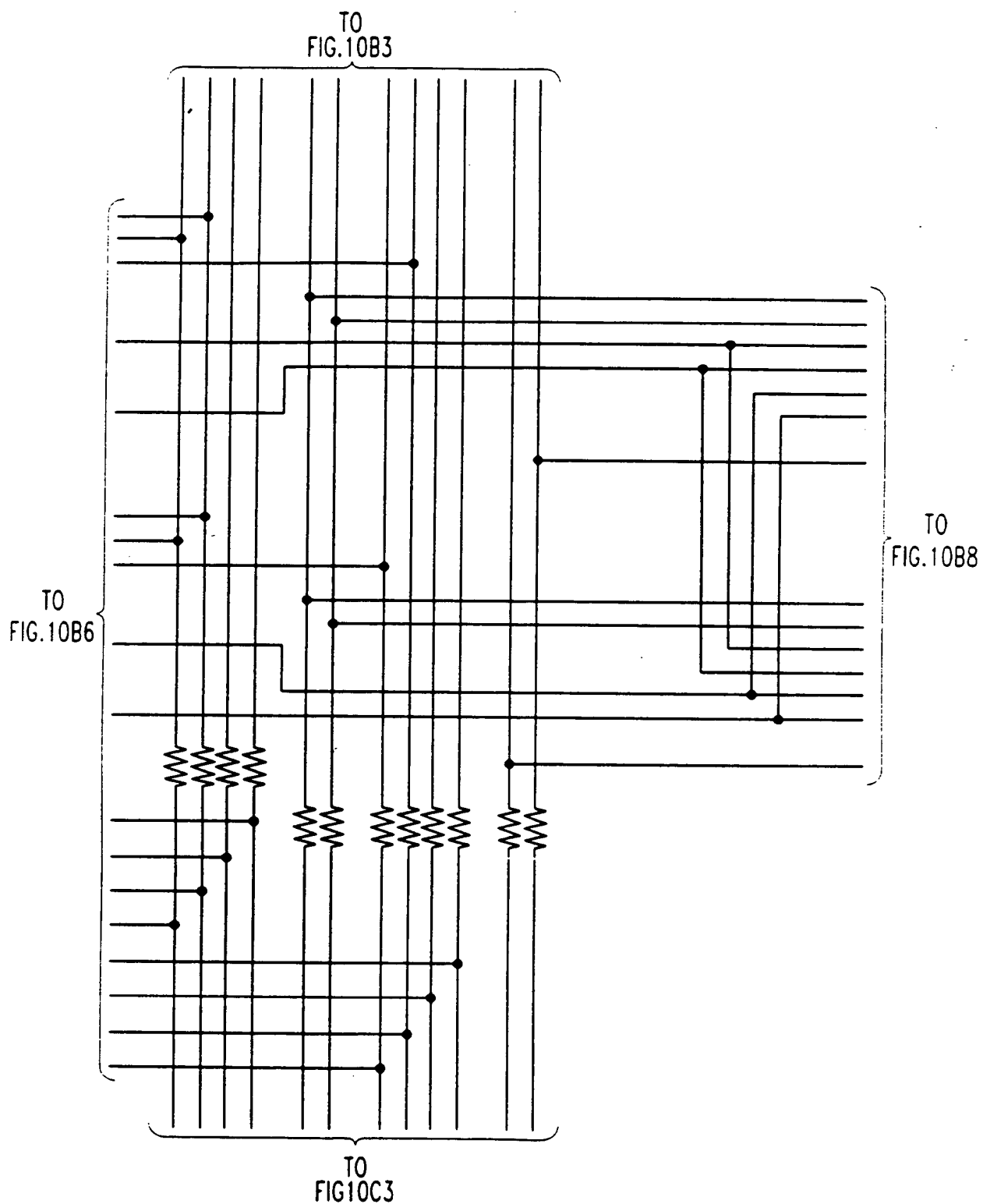
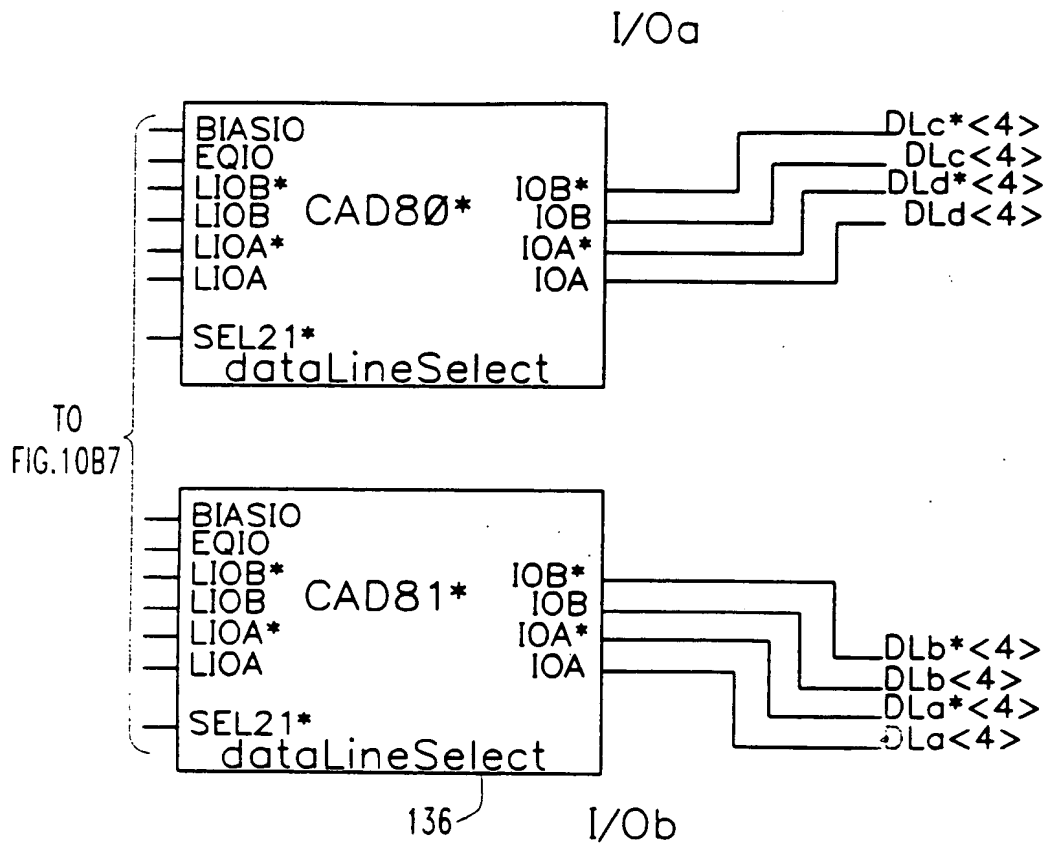


FIG. 10B8



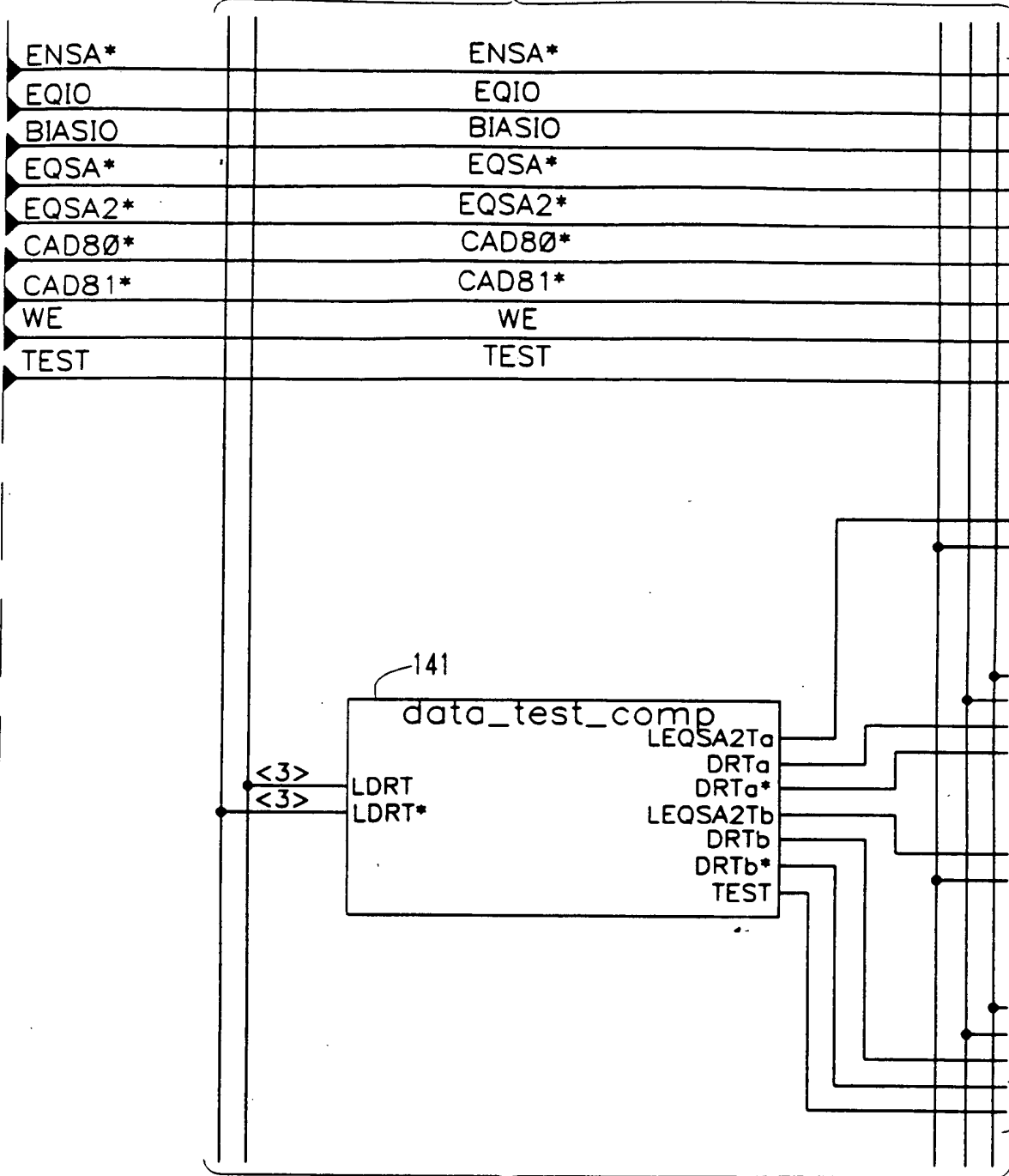
TO
FIG.10B5TO
FIG.10C5

FIG. 10C1

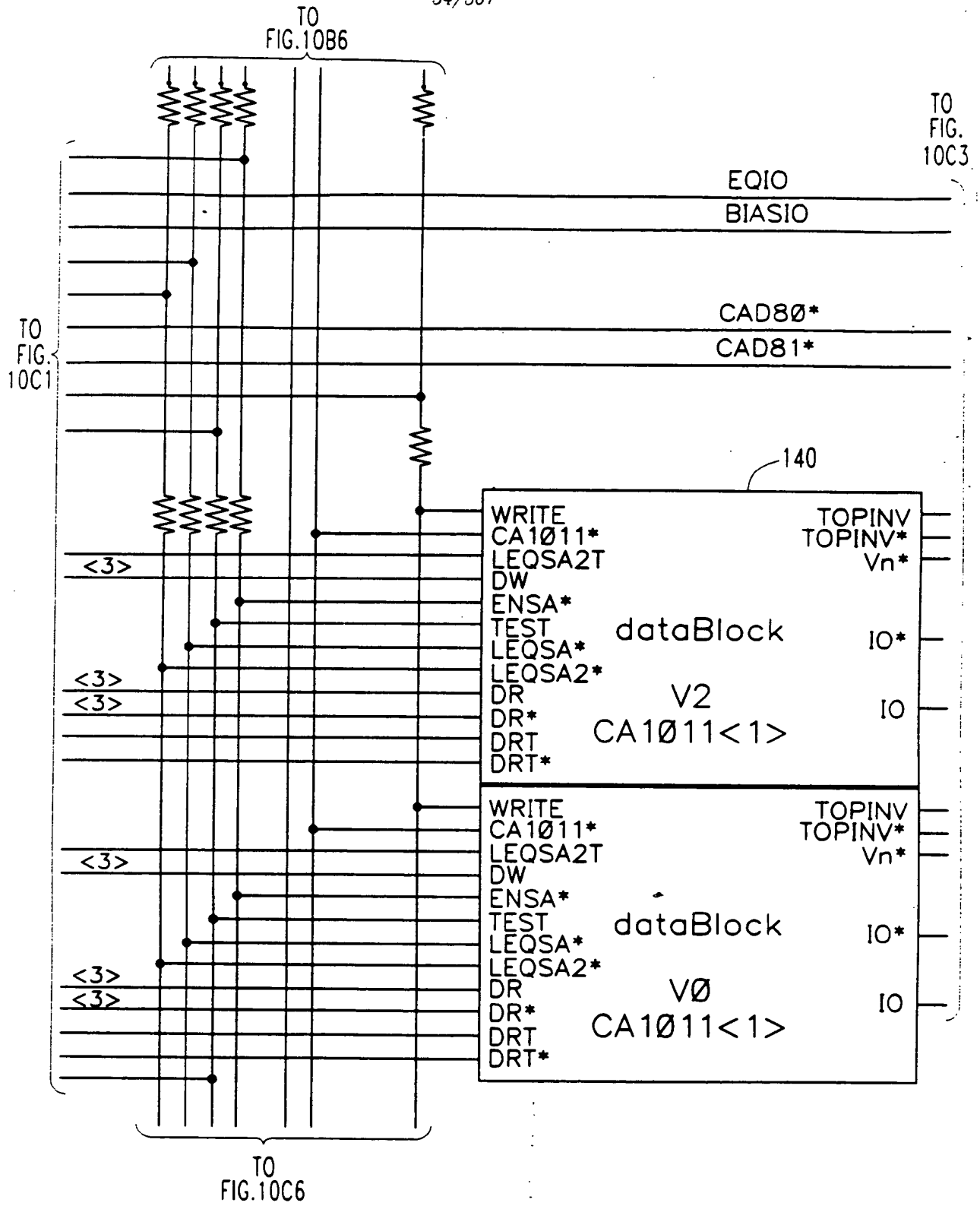


FIG. 10C2

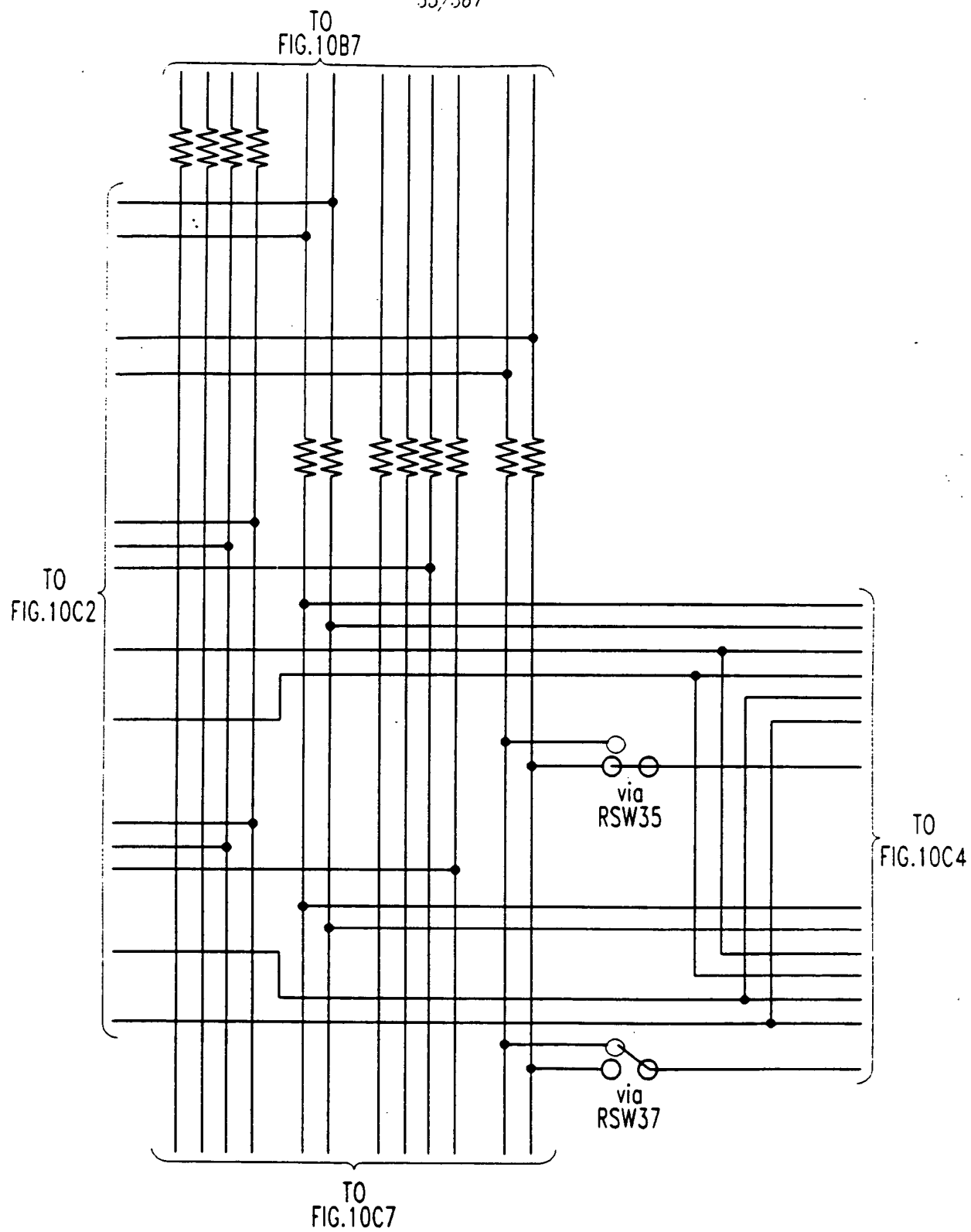
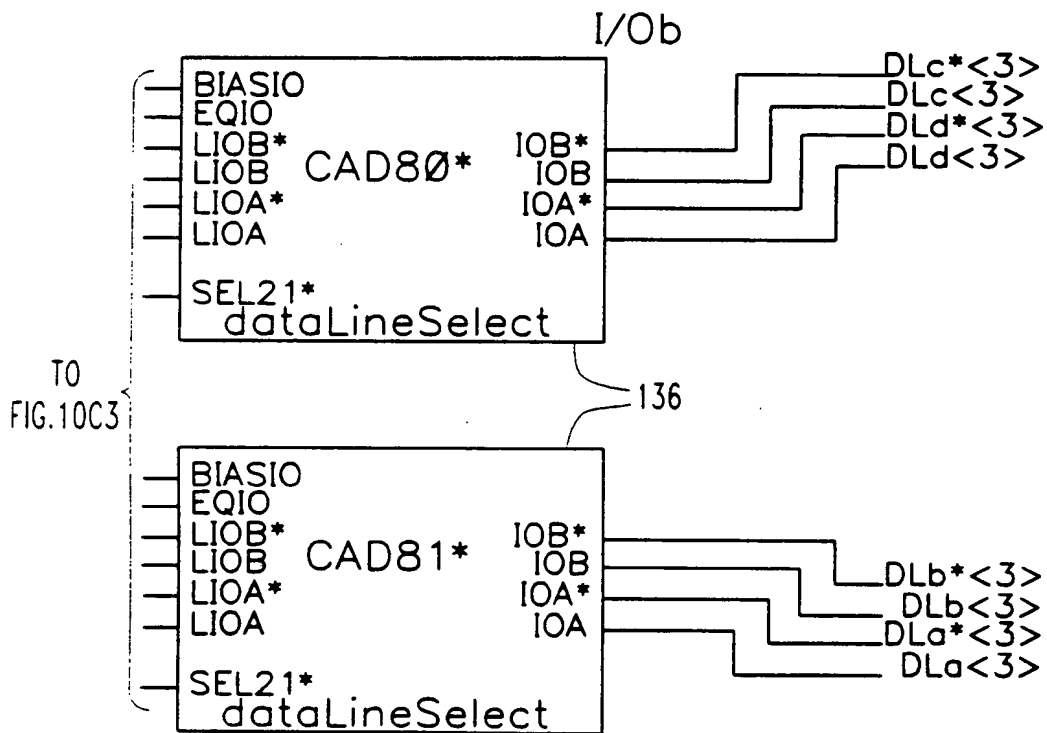


FIG. 10C3

FIG. 10C4



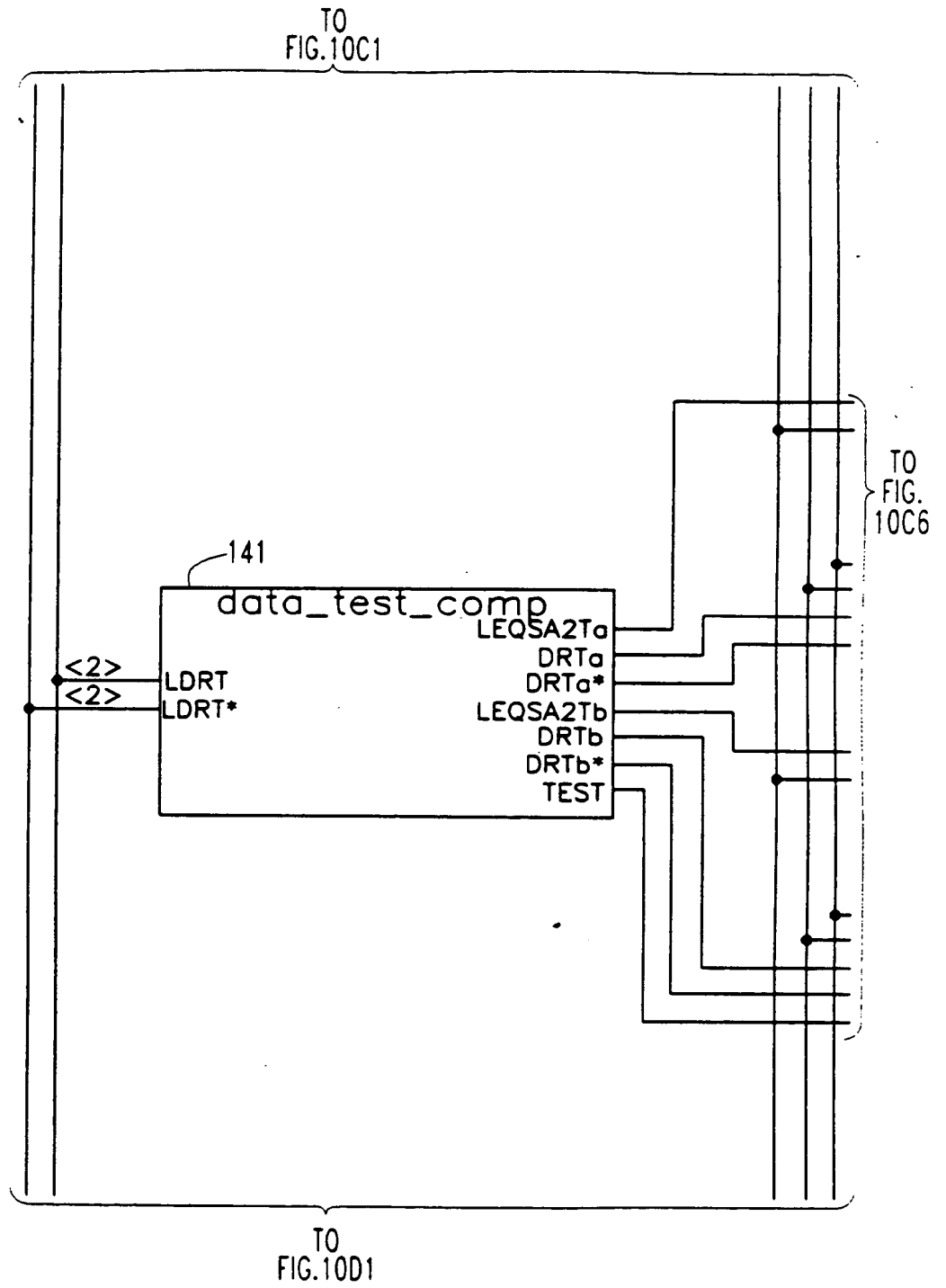


FIG. 10C5

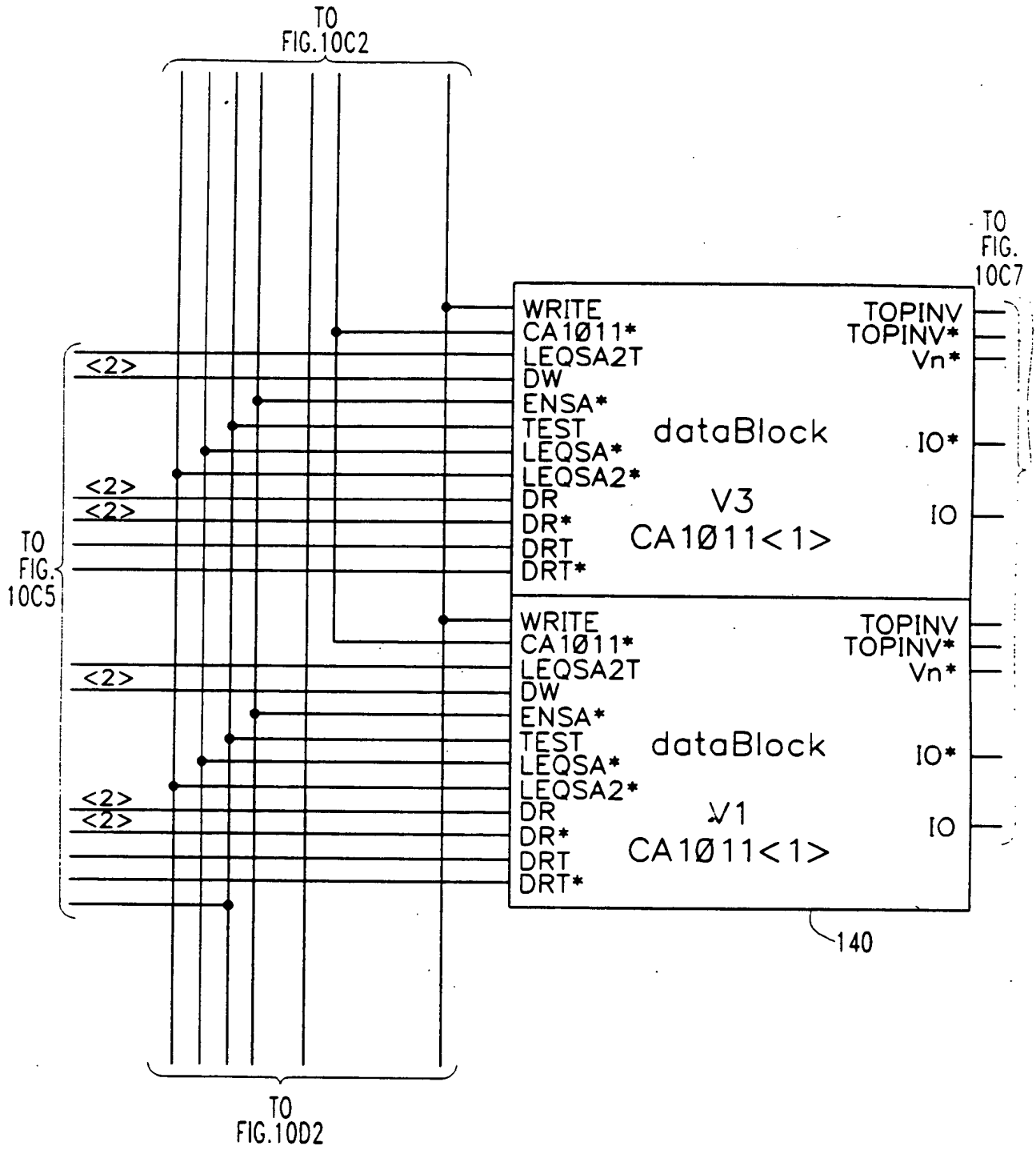
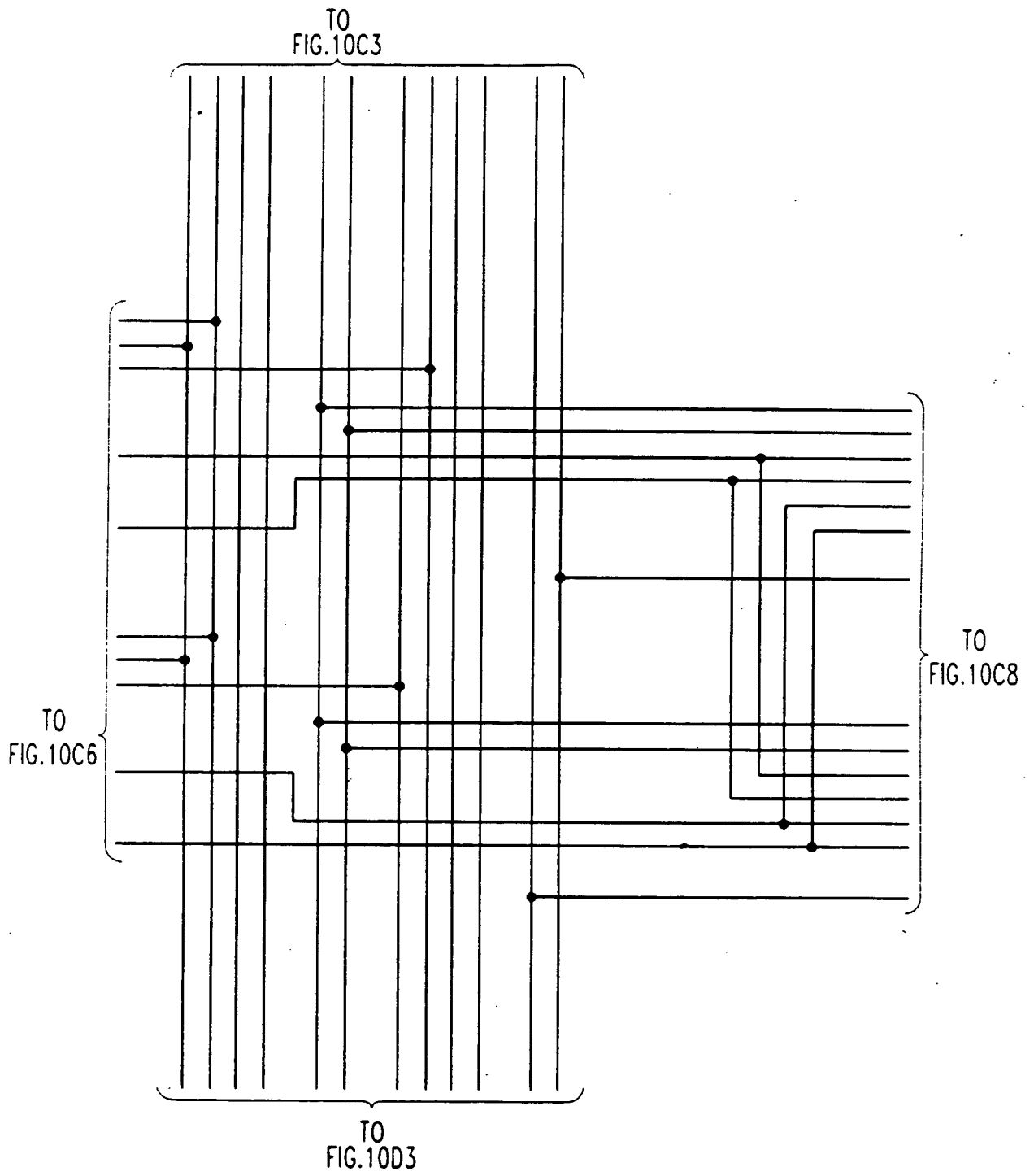


FIG. 10C6

*FIG.10C7*

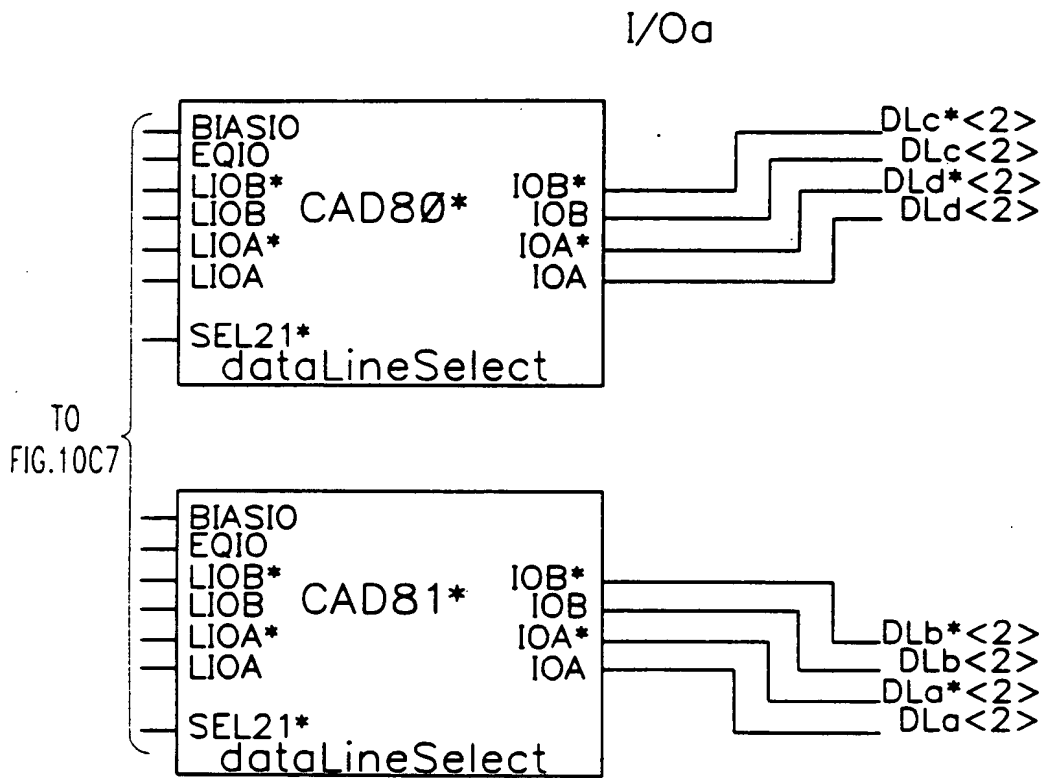


FIG.10C8

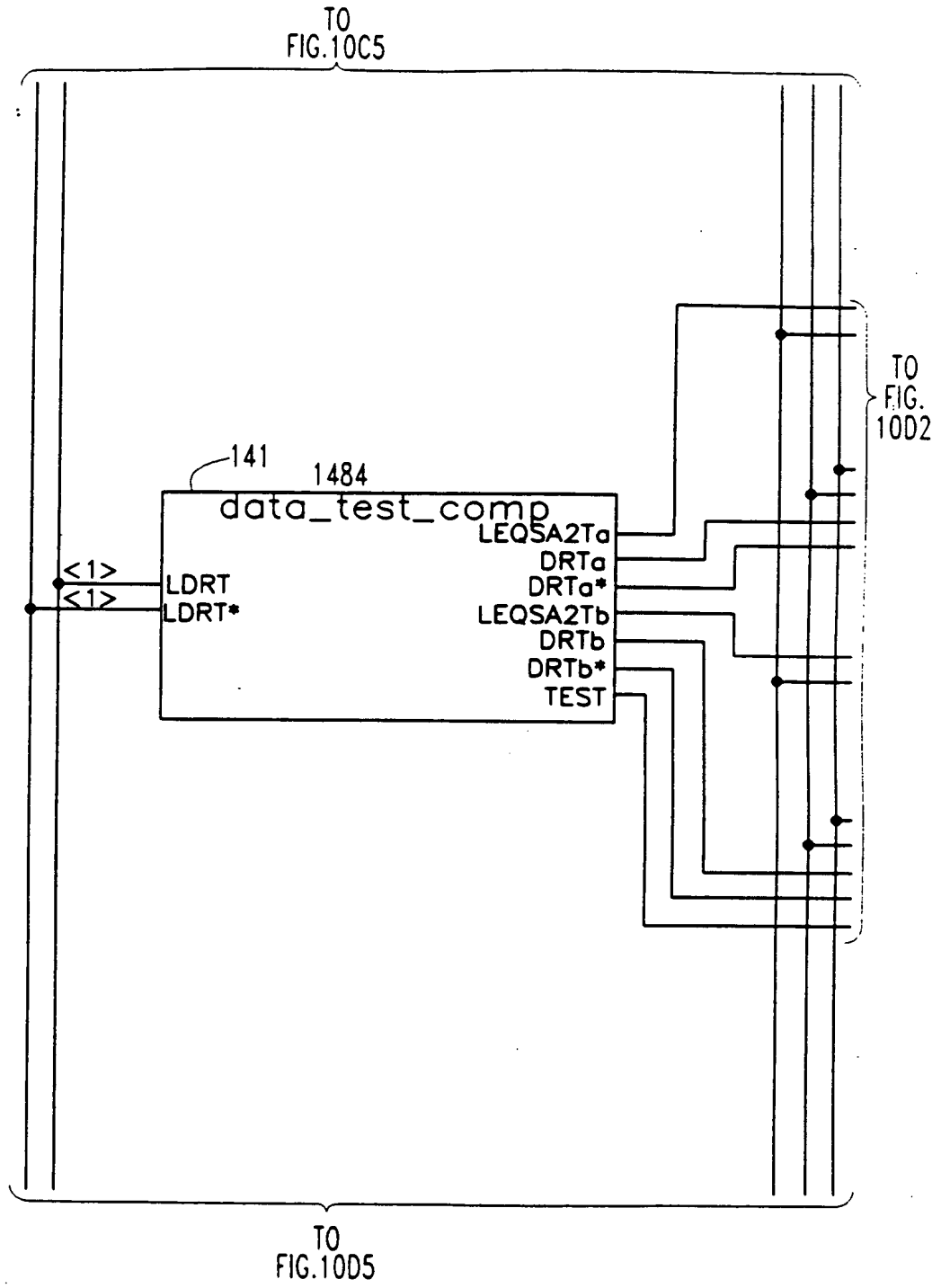


FIG. 10D1

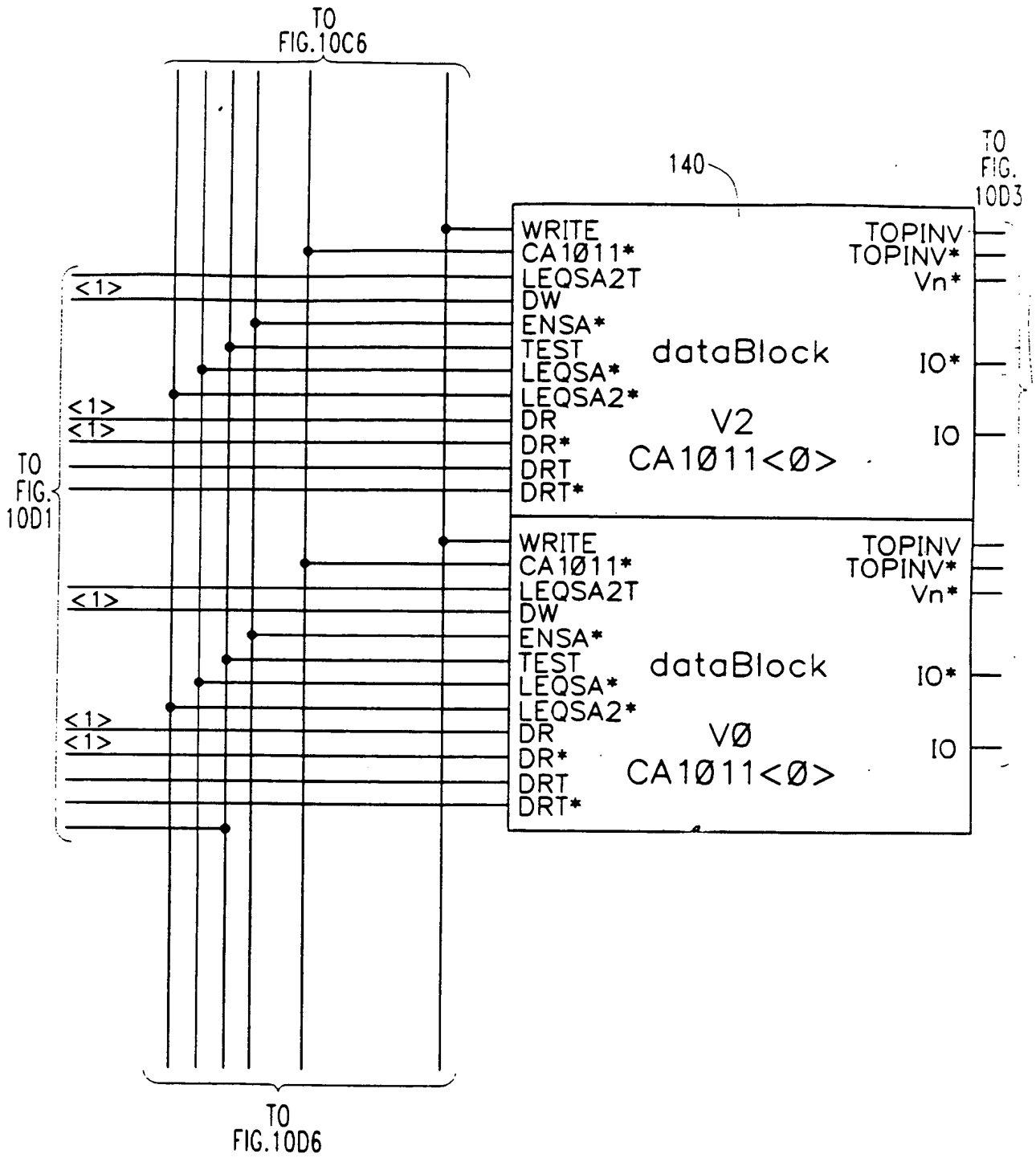
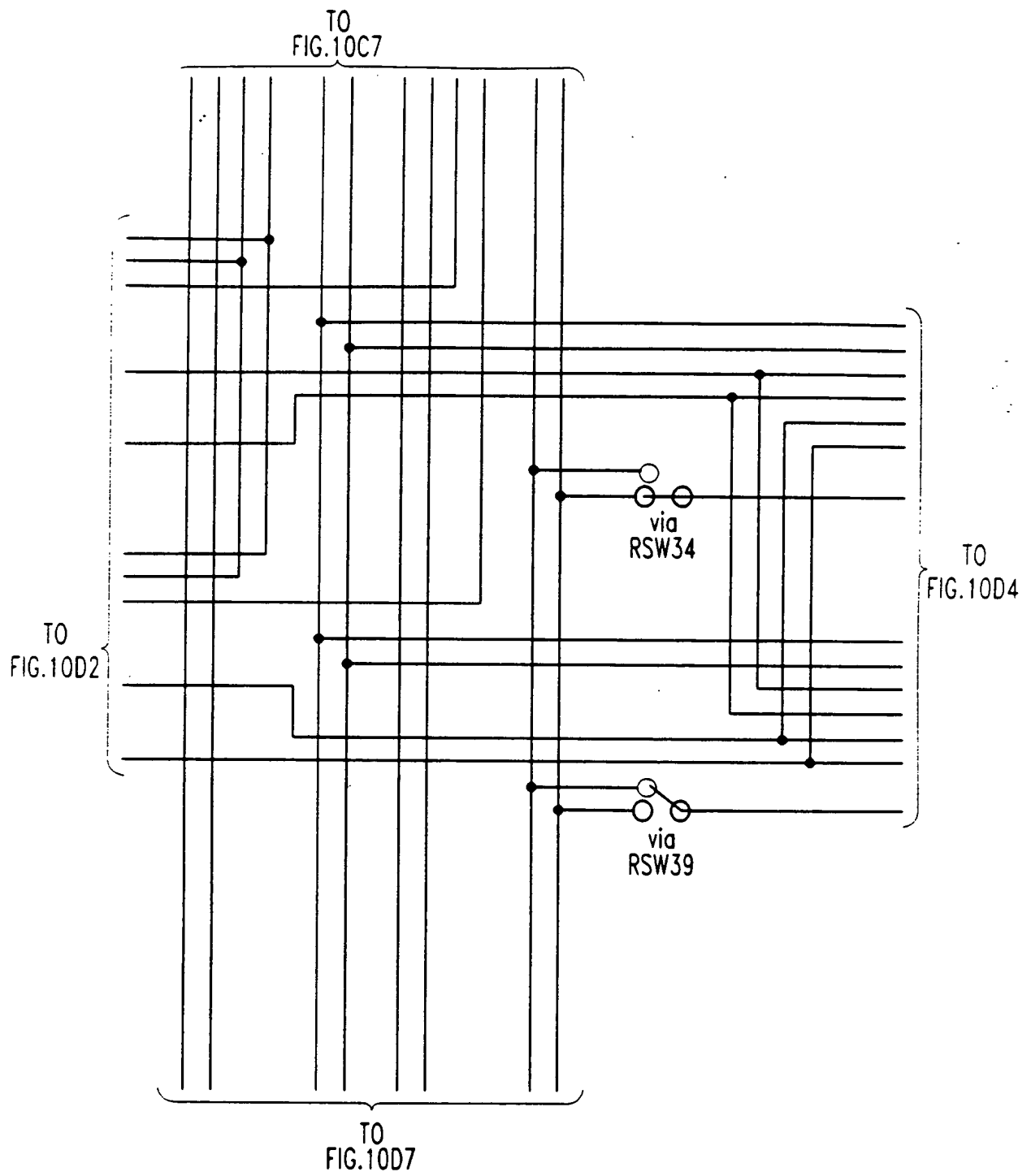


FIG. 10D2

*FIG. 10D3*

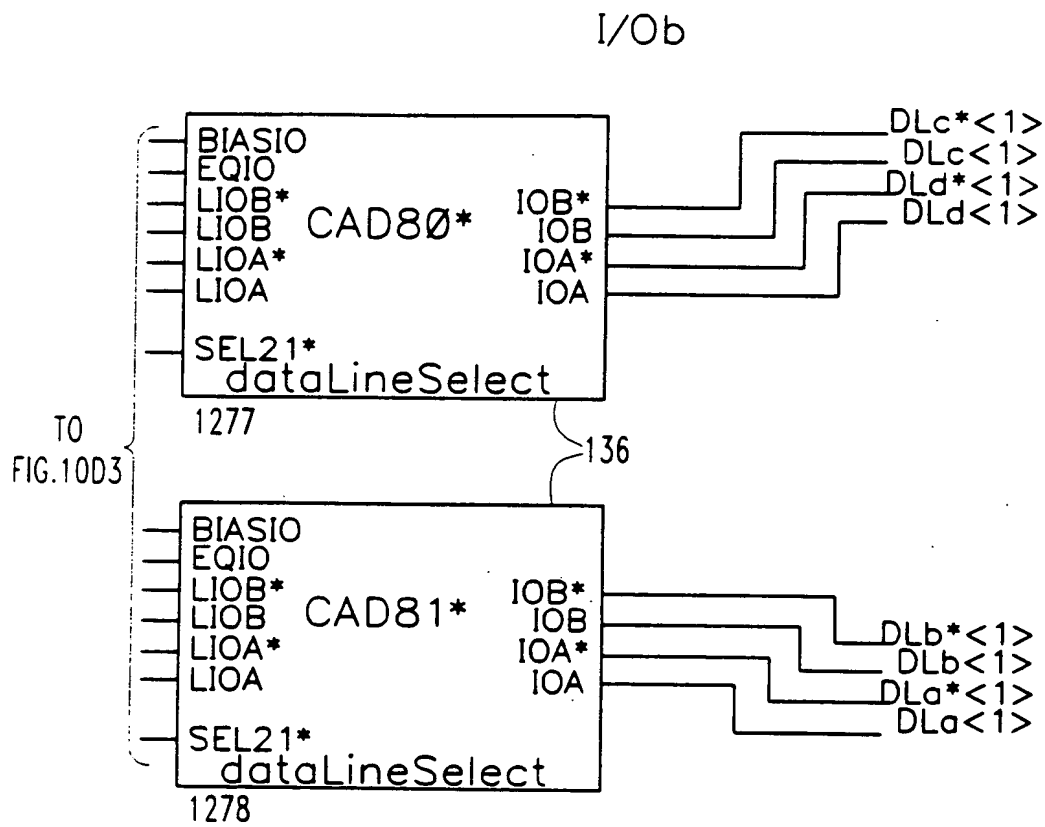


FIG. 10D4

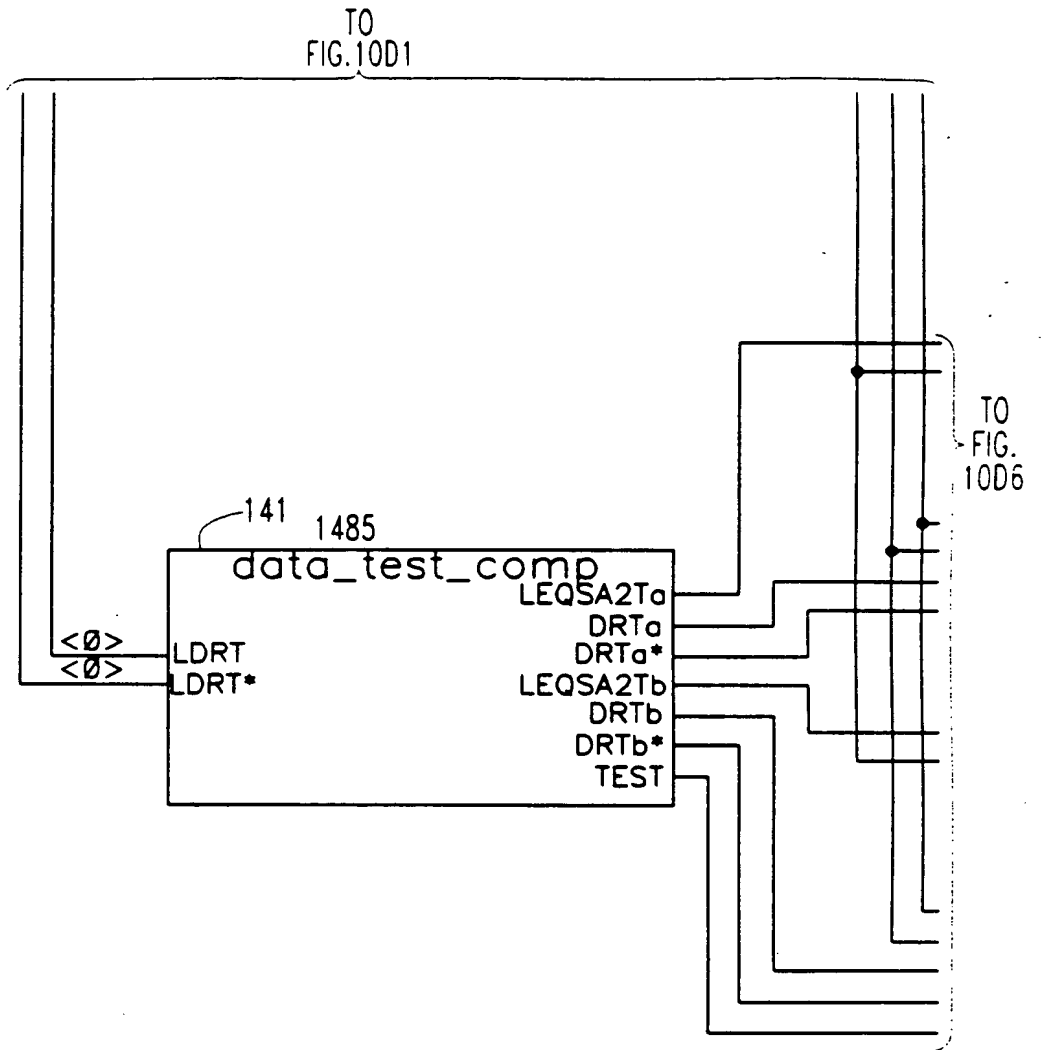


FIG. 10D5

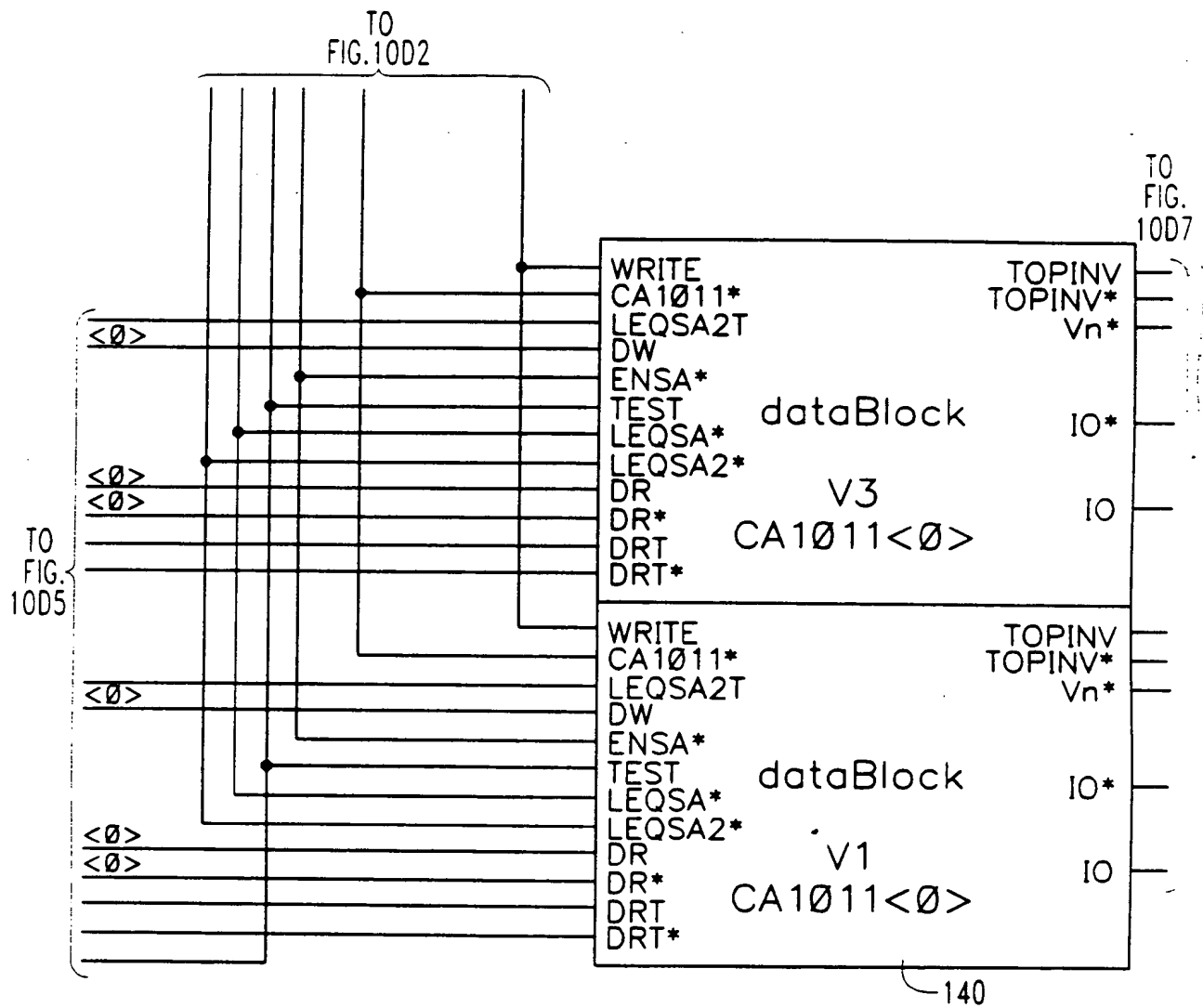


FIG. 10D6

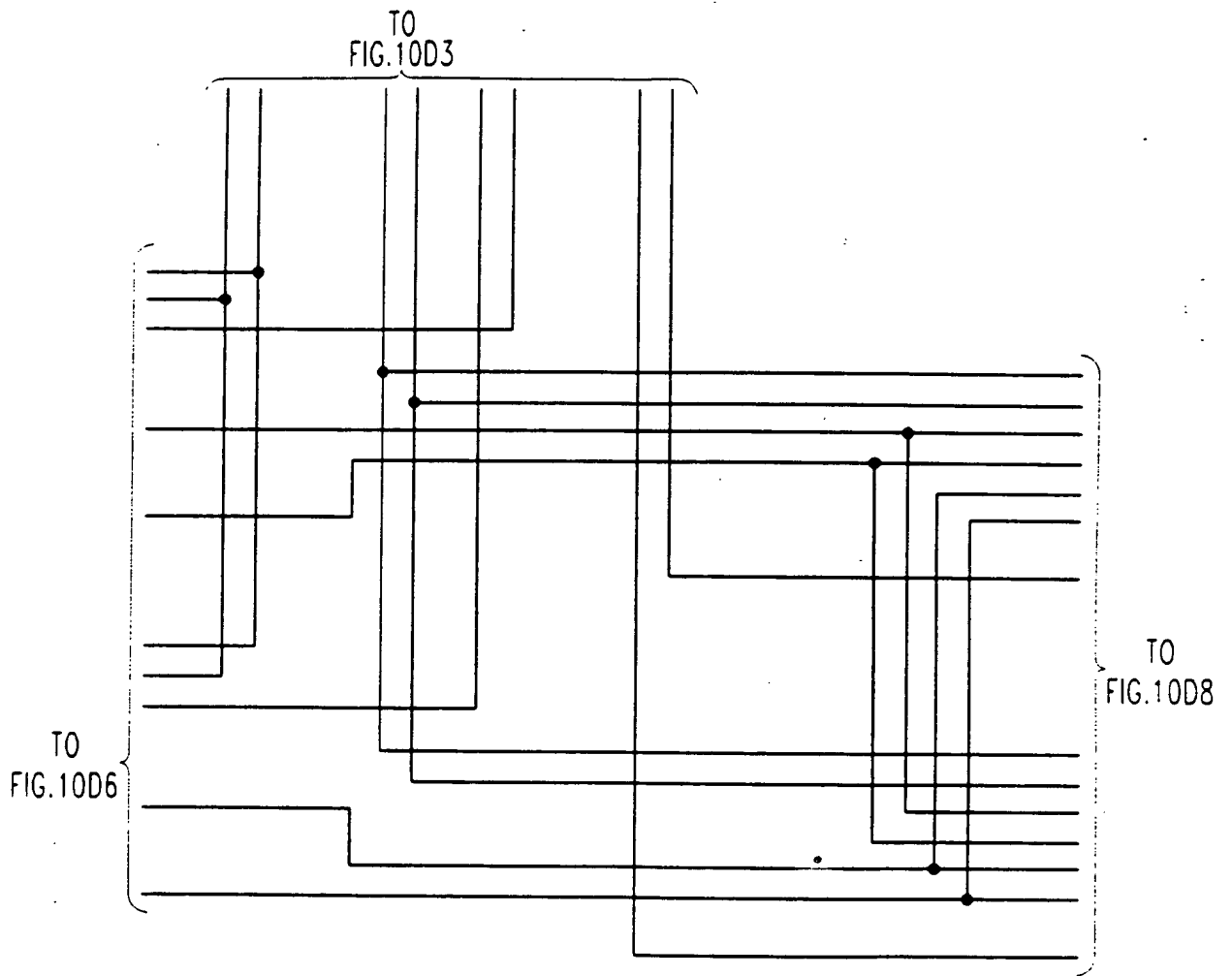


FIG. 10D7

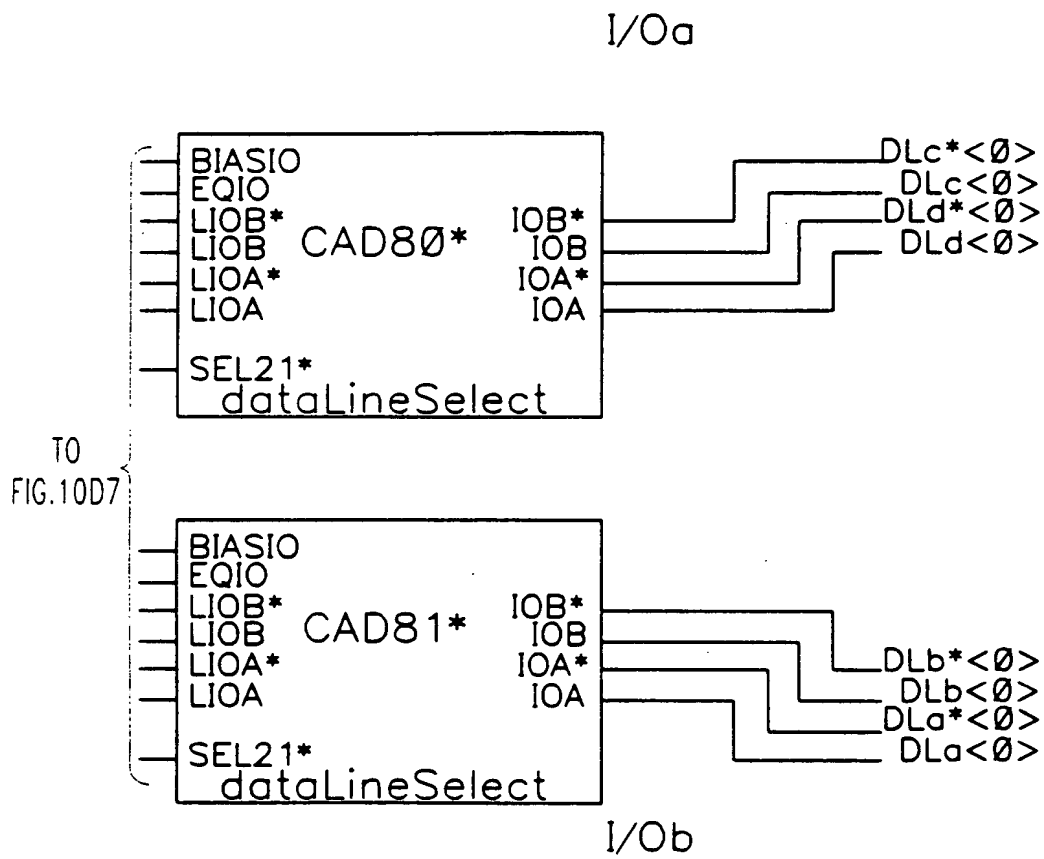
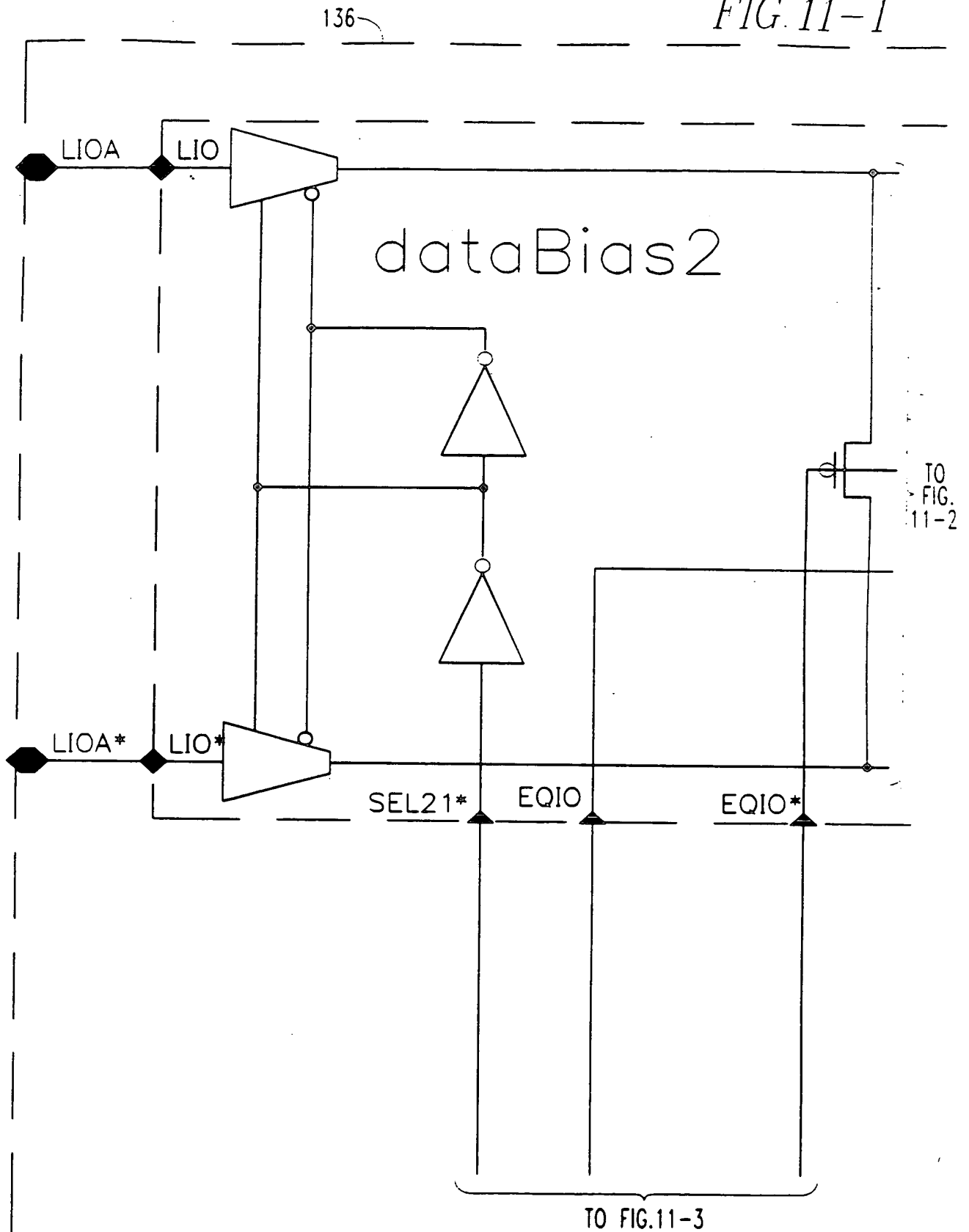
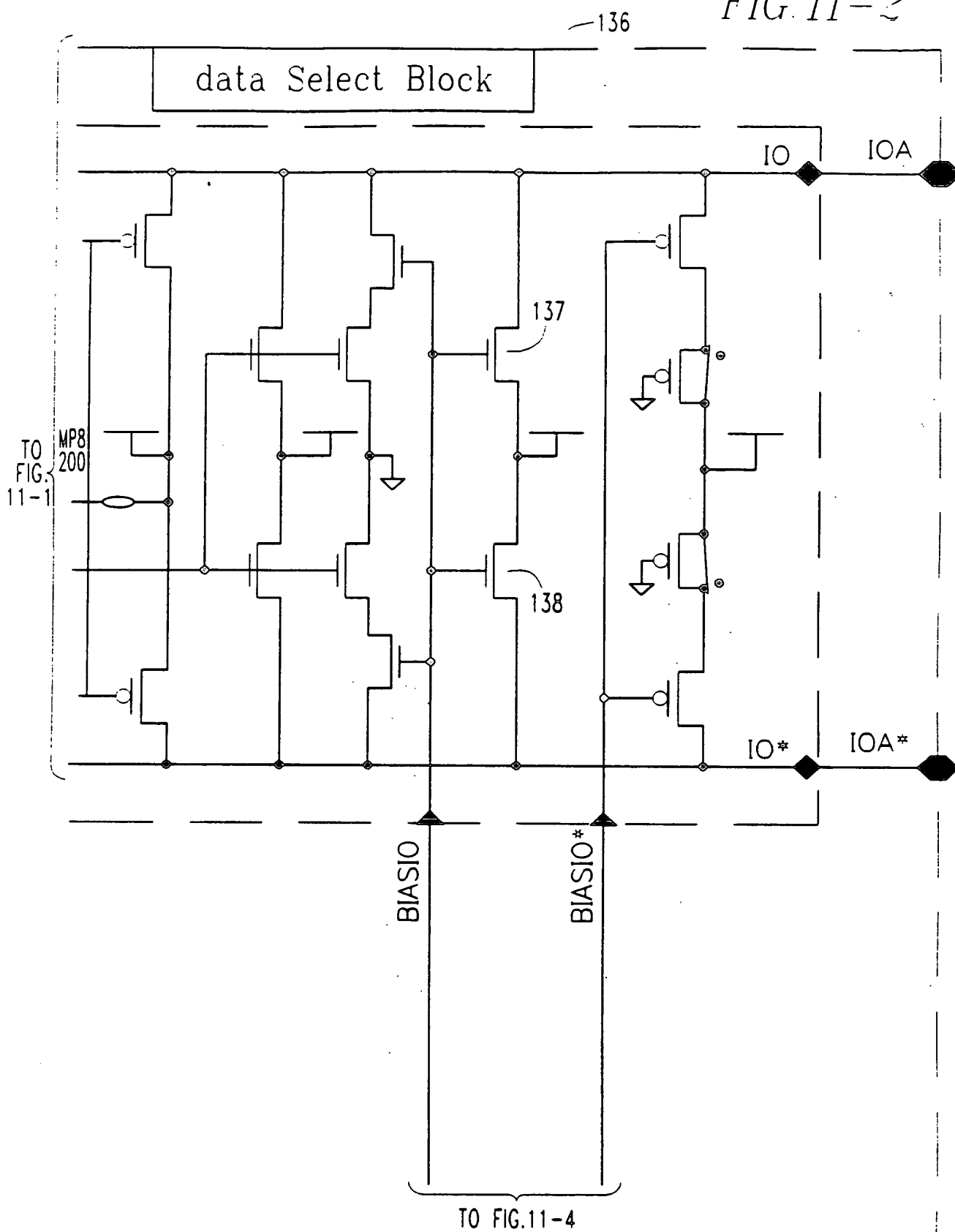


FIG.10D8





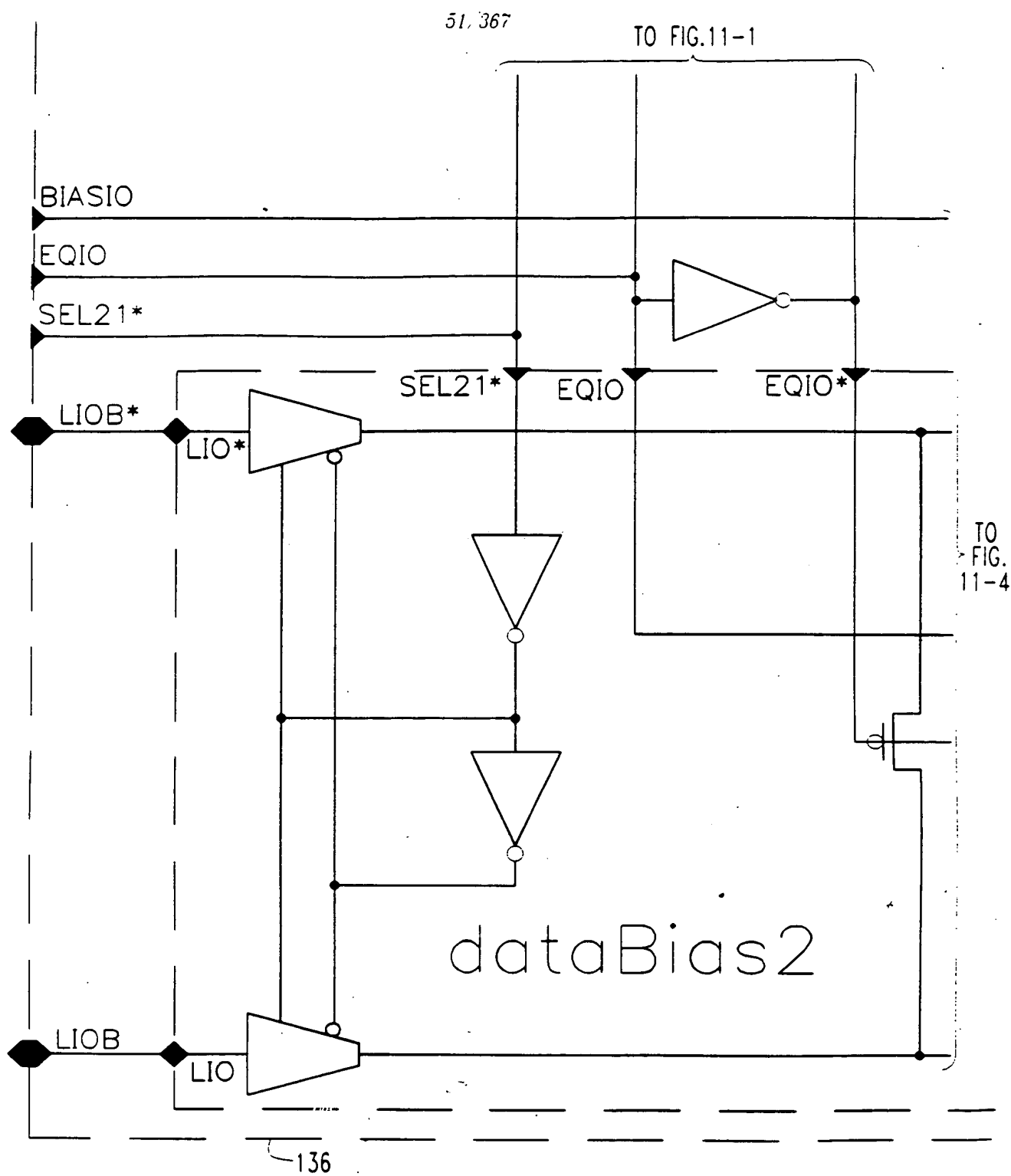


FIG. 11-3

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TO FIG. 11-2

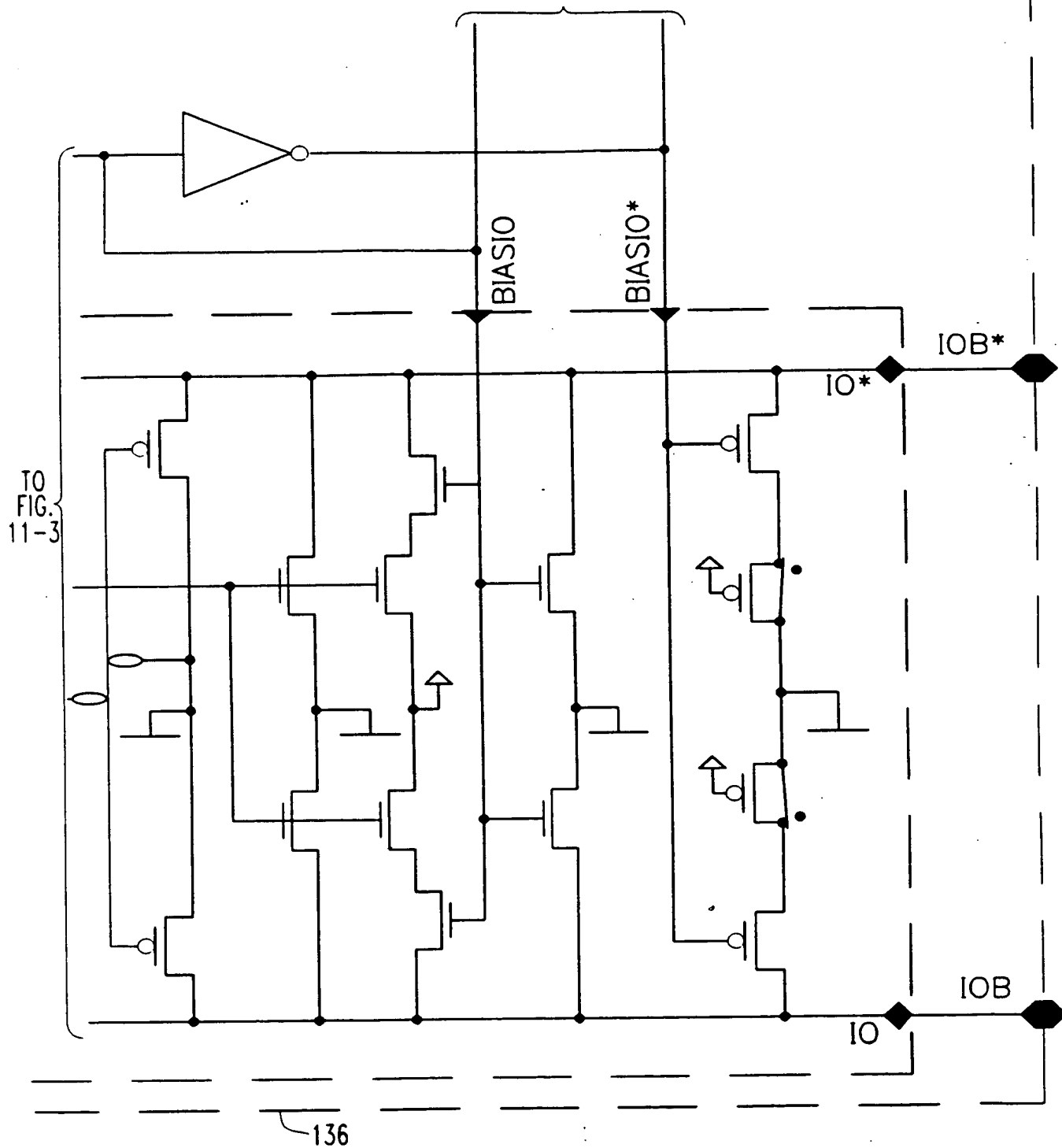
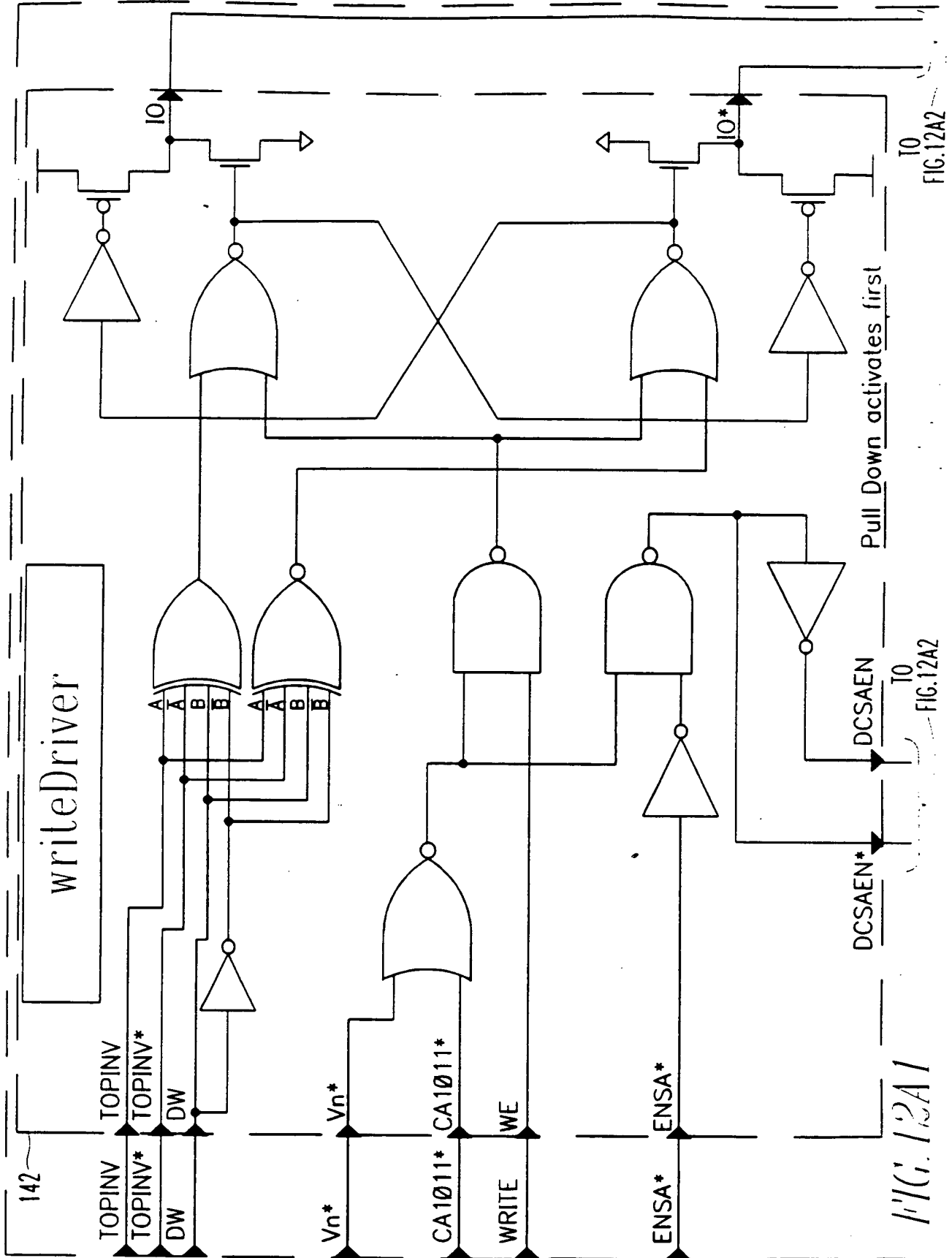


FIG. 11-4



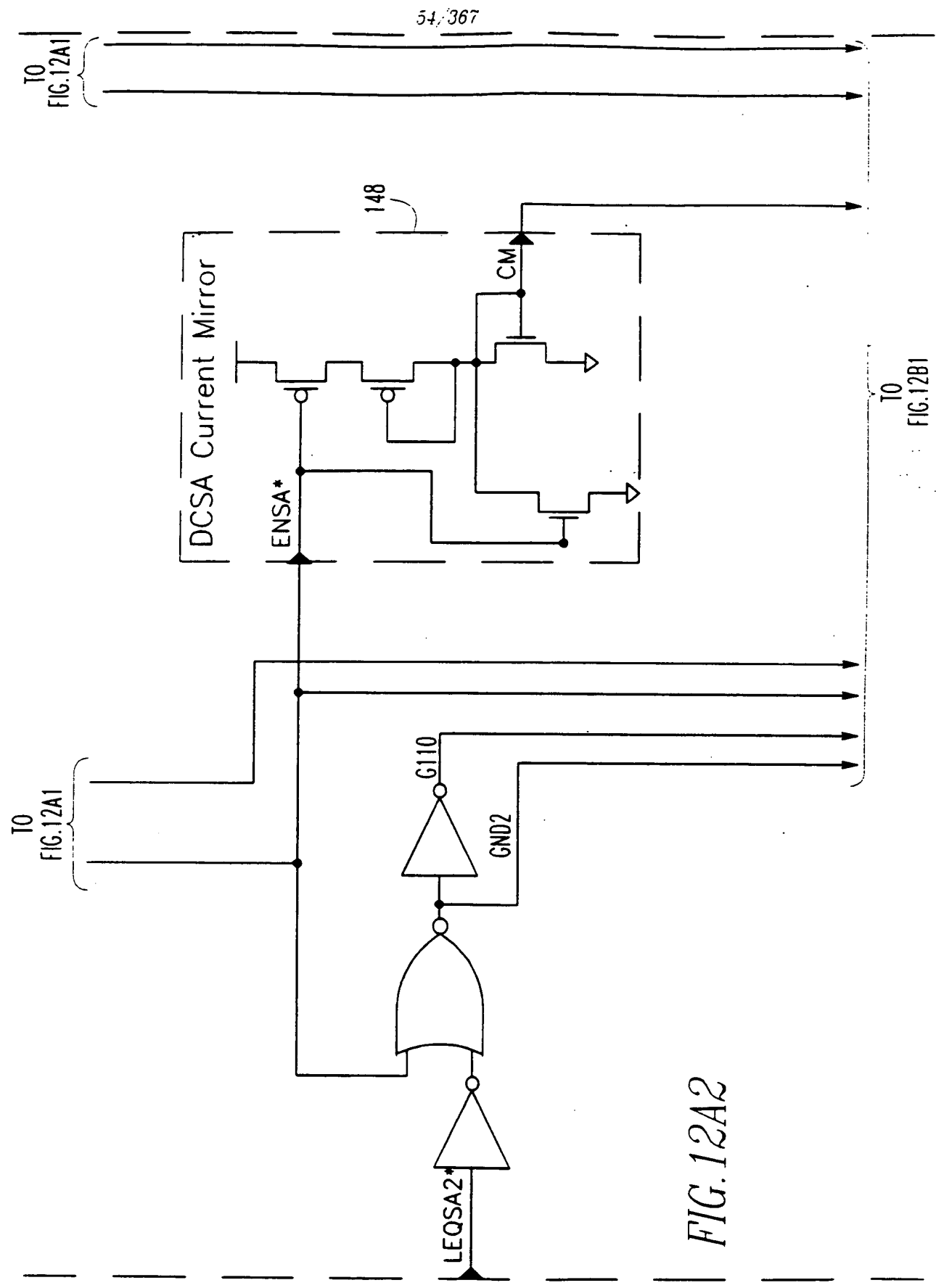
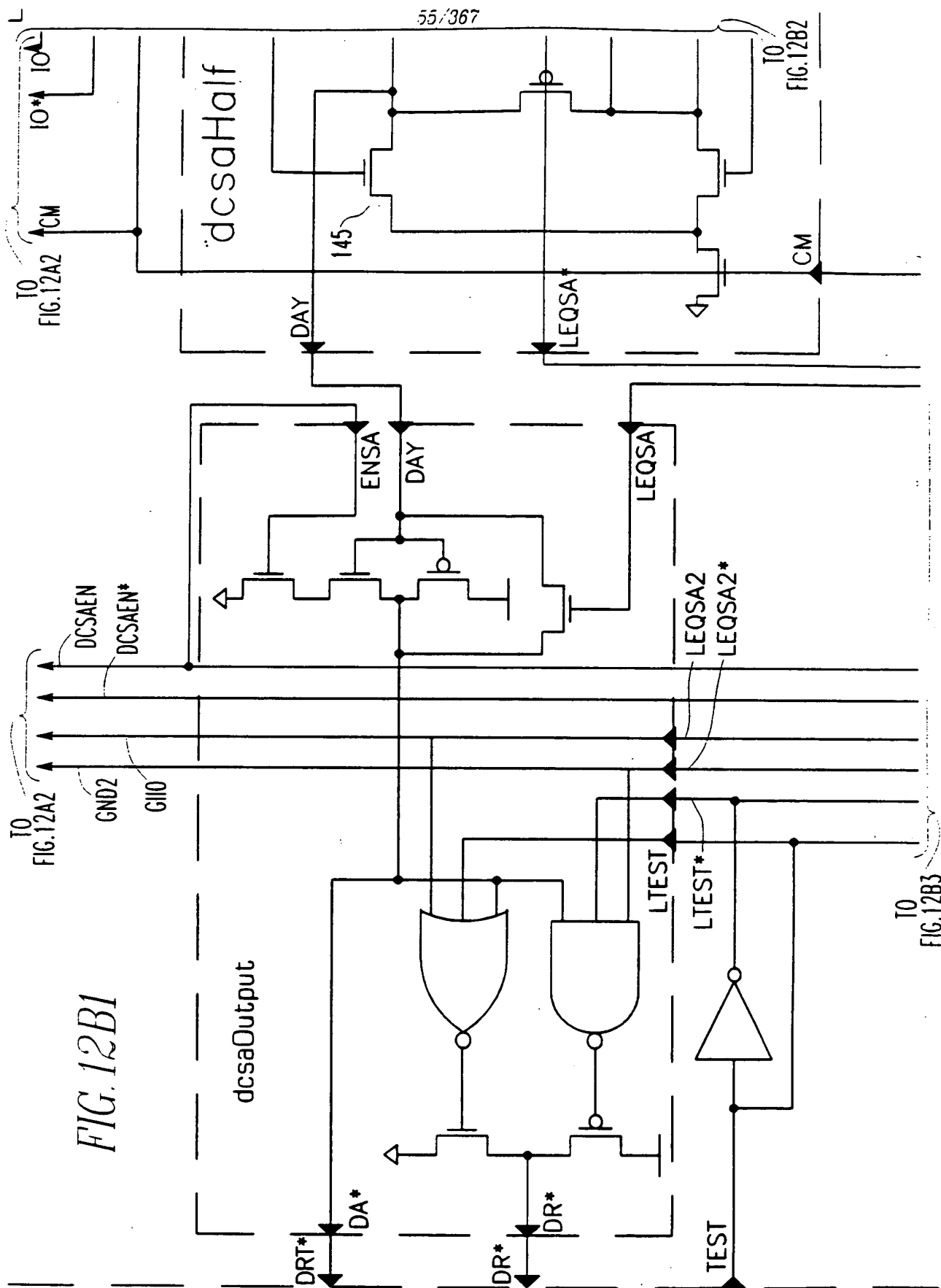


FIG. 12A2



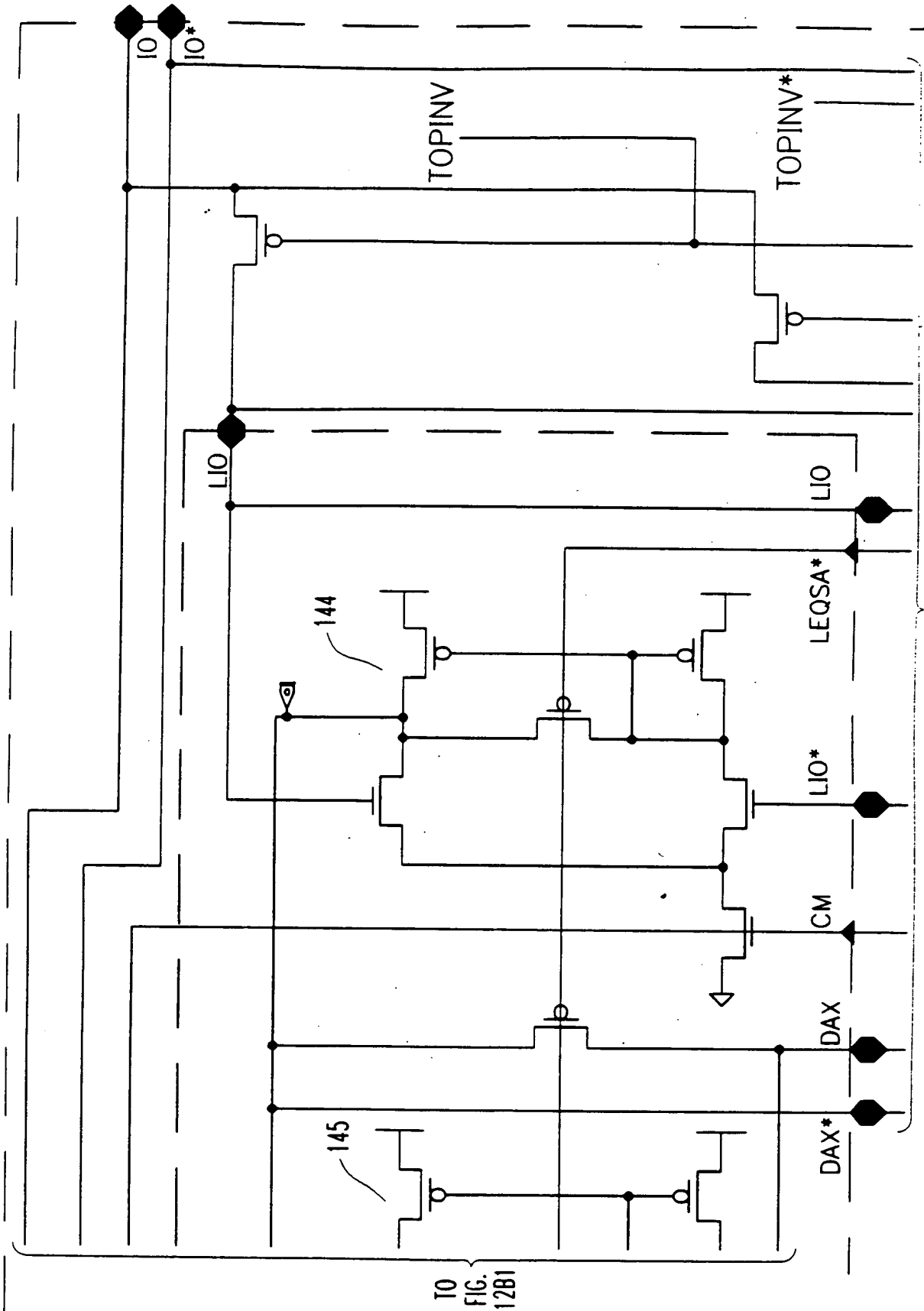
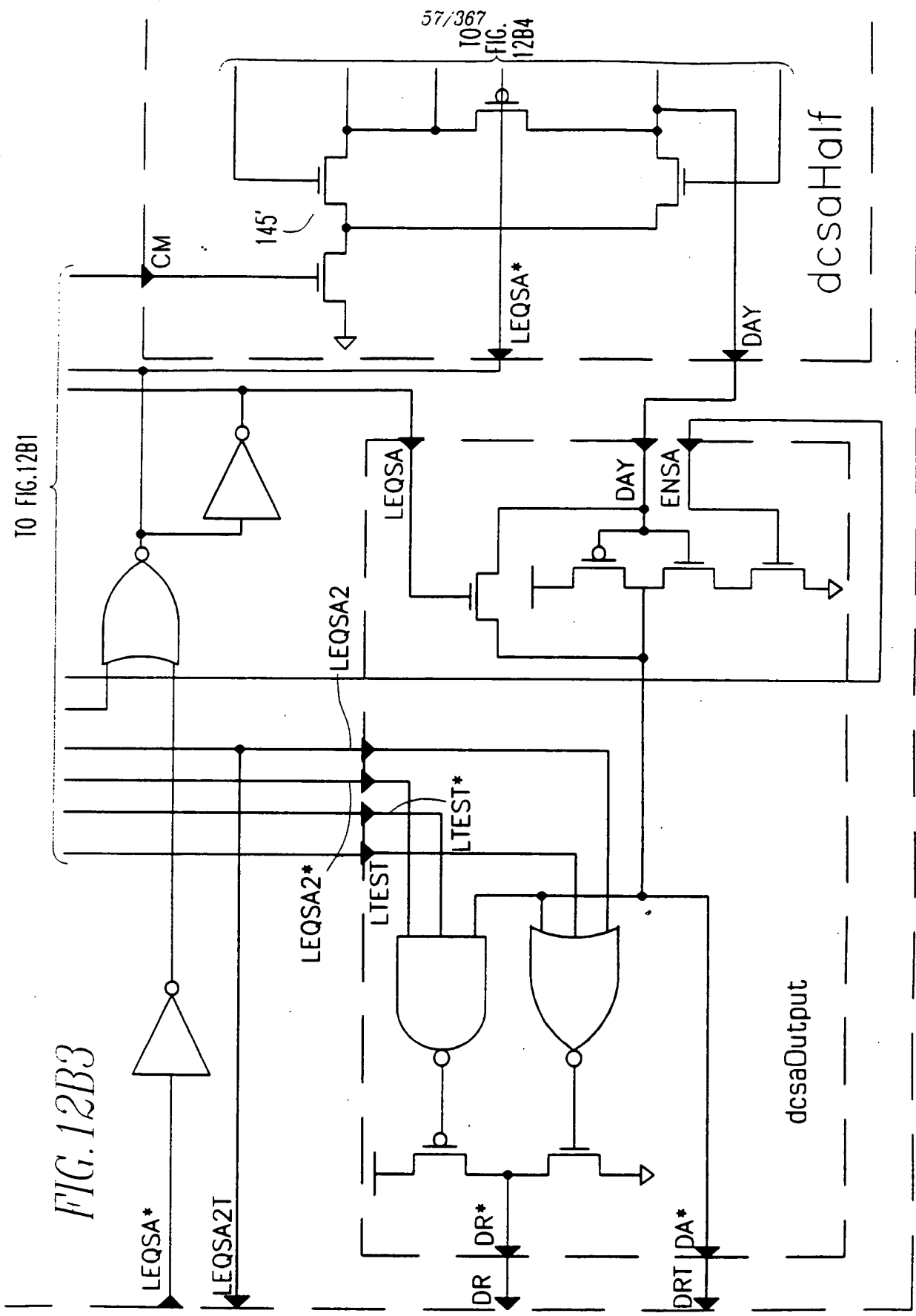
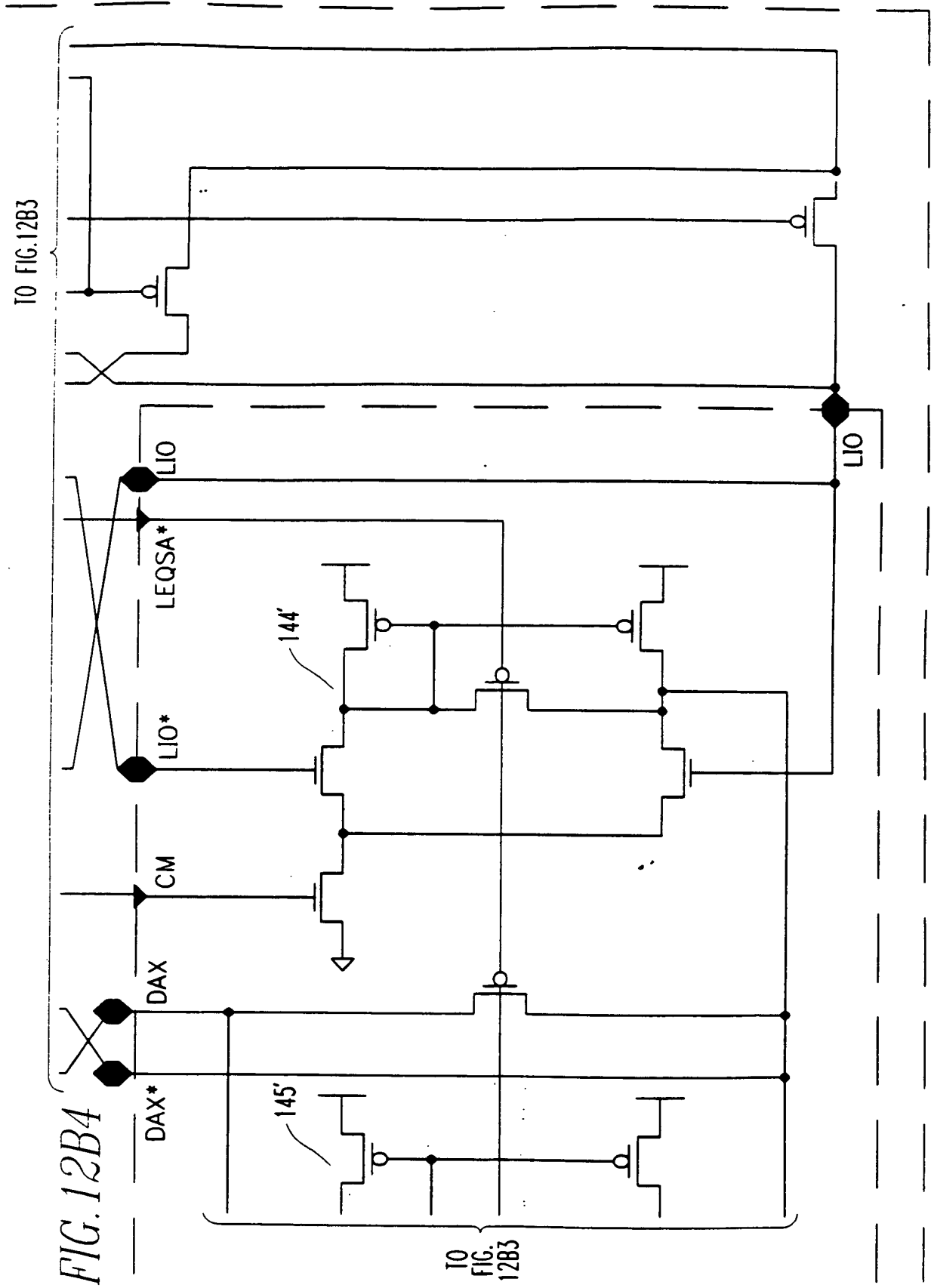


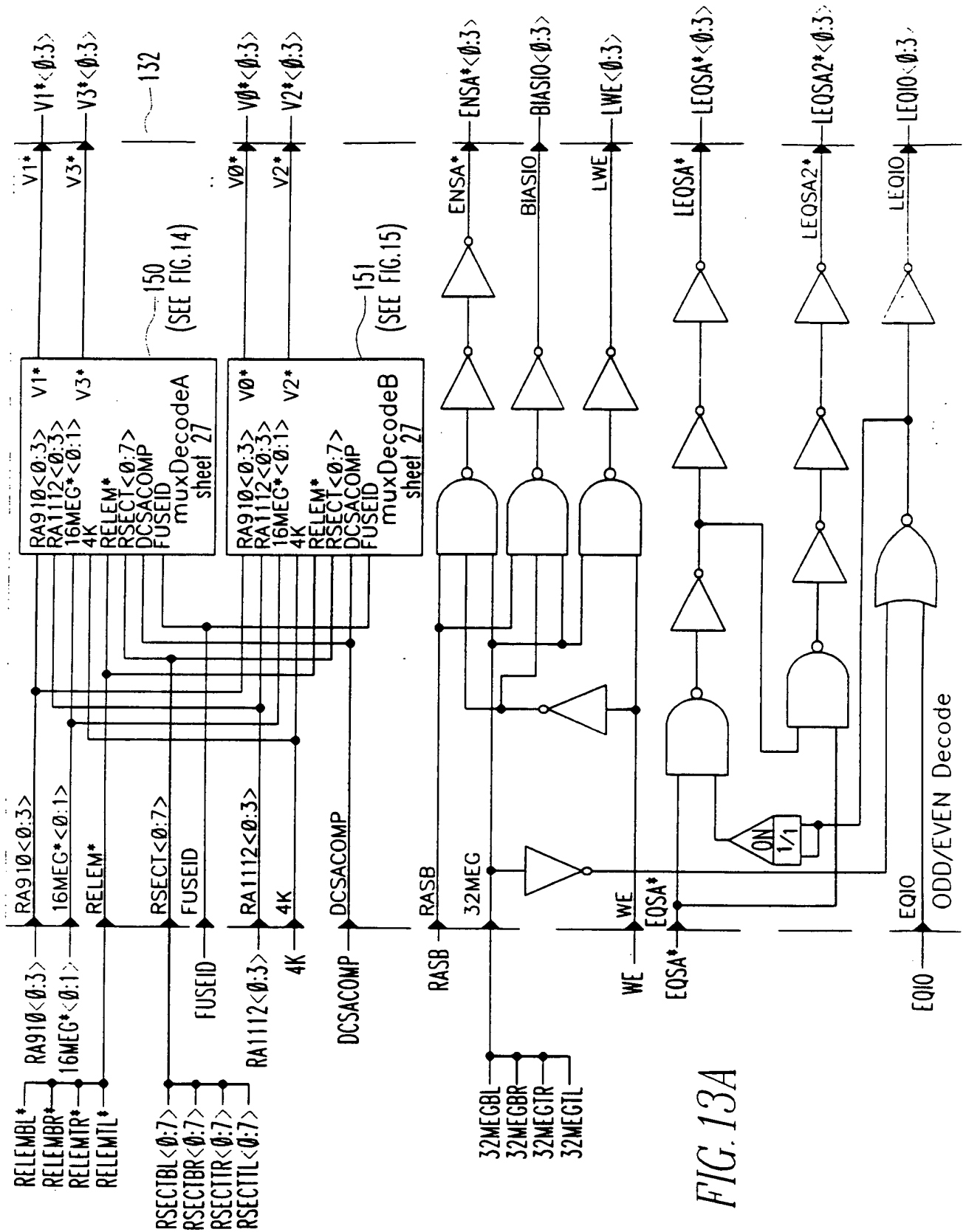
FIG. 12B1

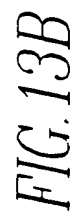
FIG. 12B2

FIG. 12B1









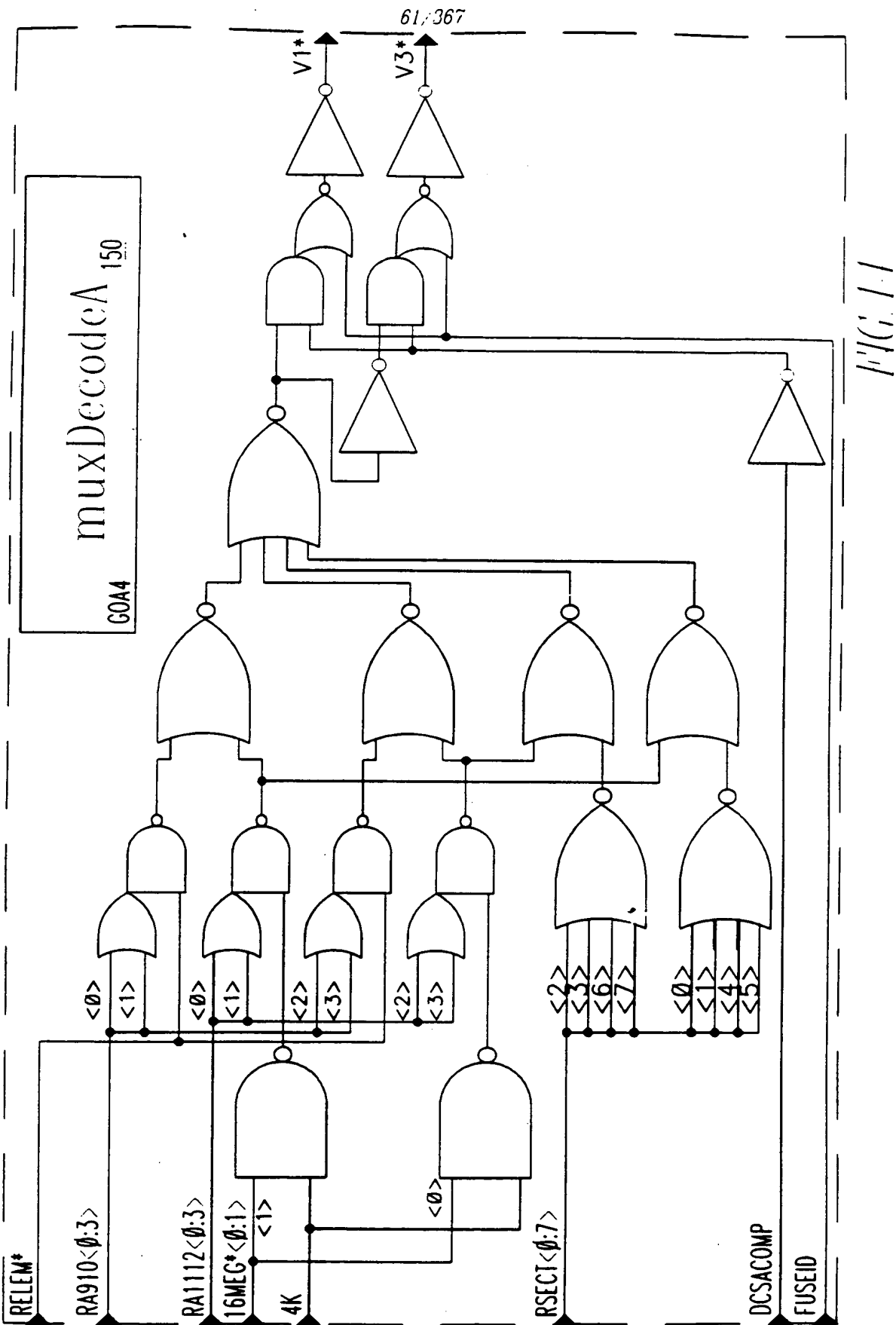
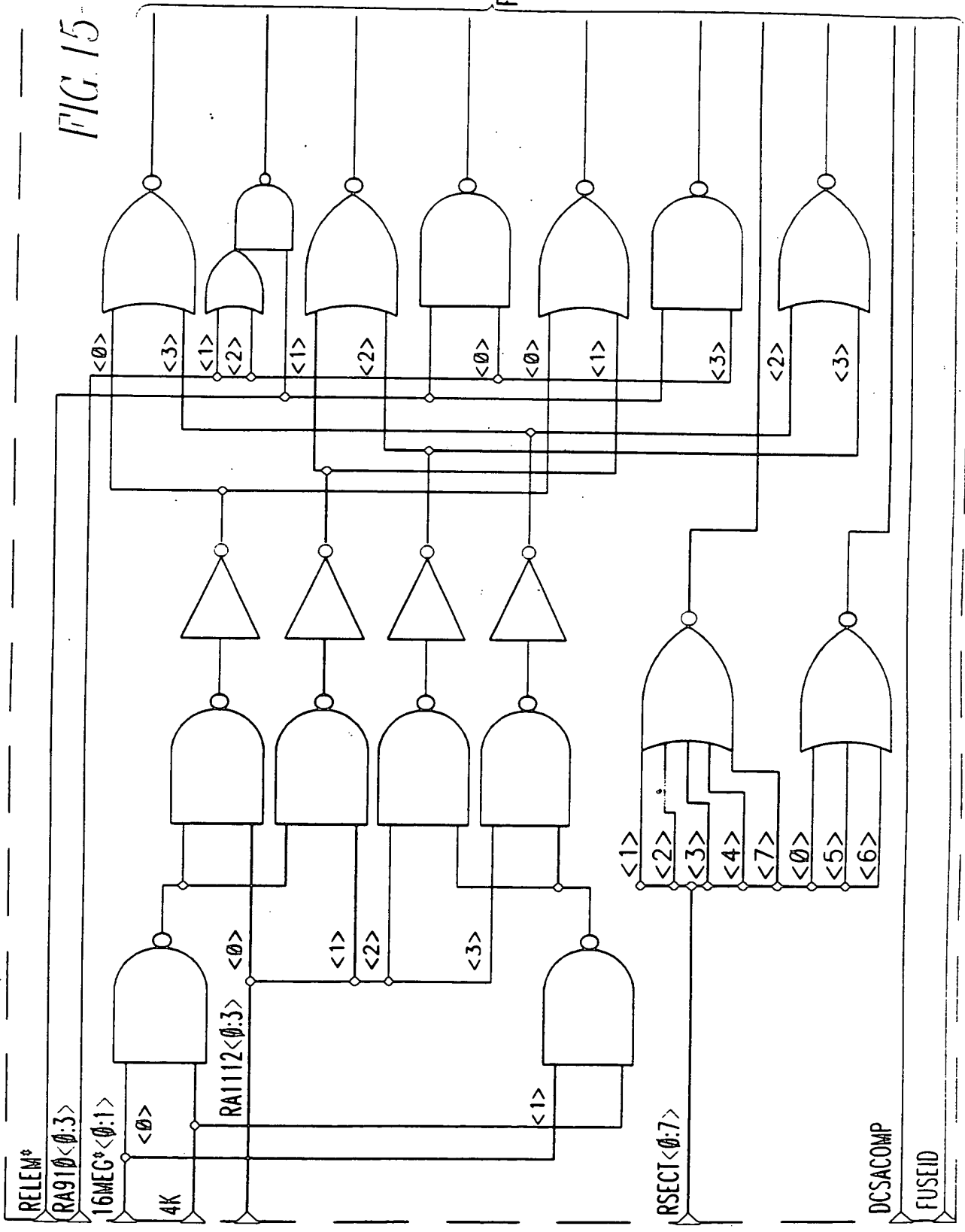


FIG. 15-1

62.367

FIG. 15-2



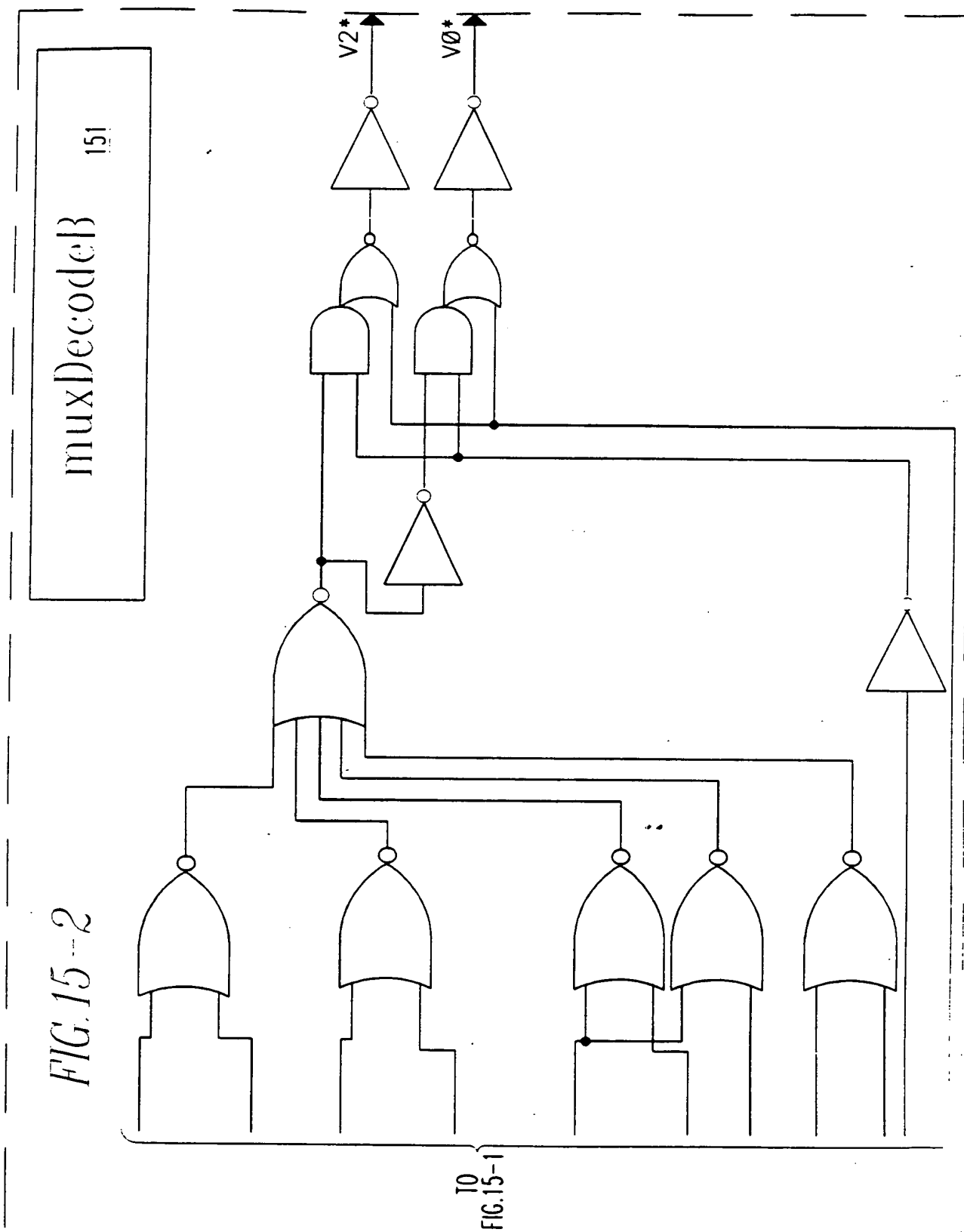
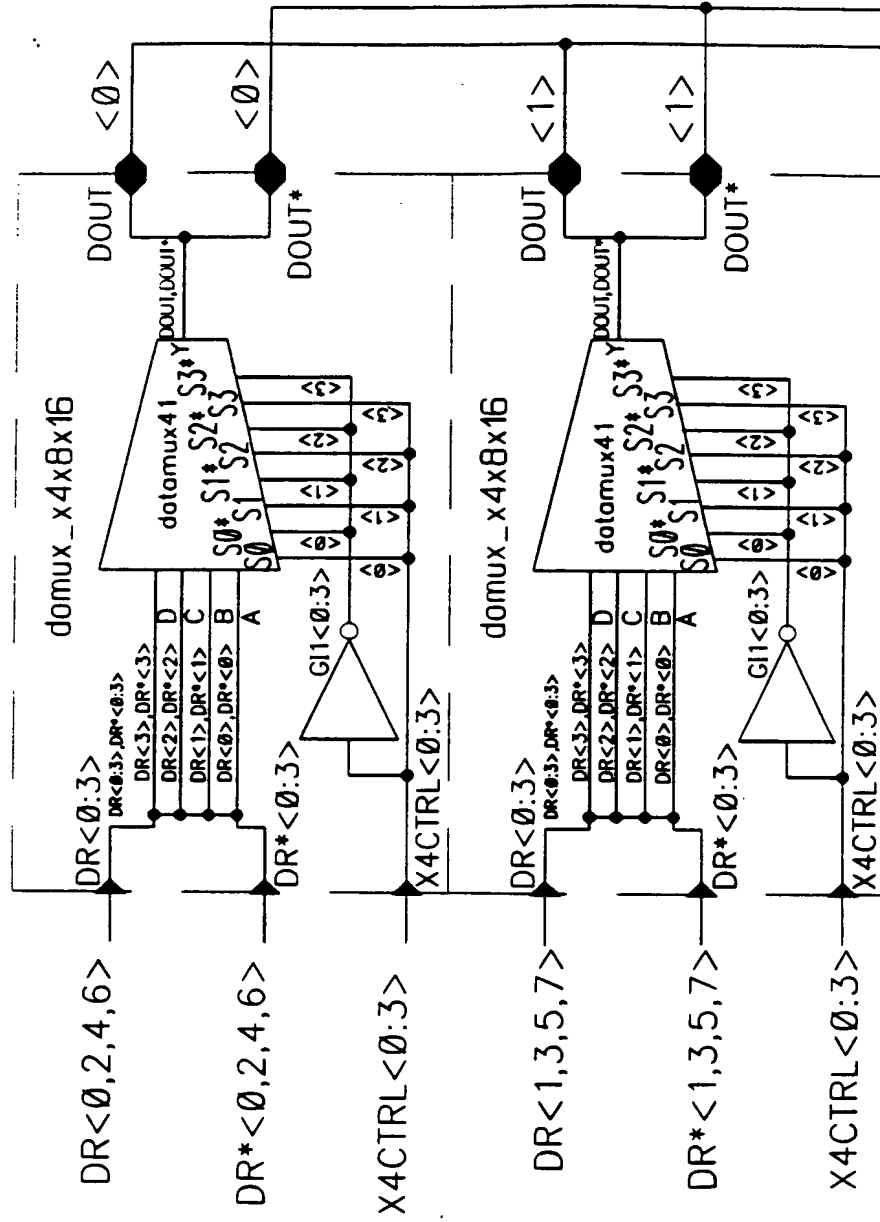


FIG. 16A



TO FIG. 16B

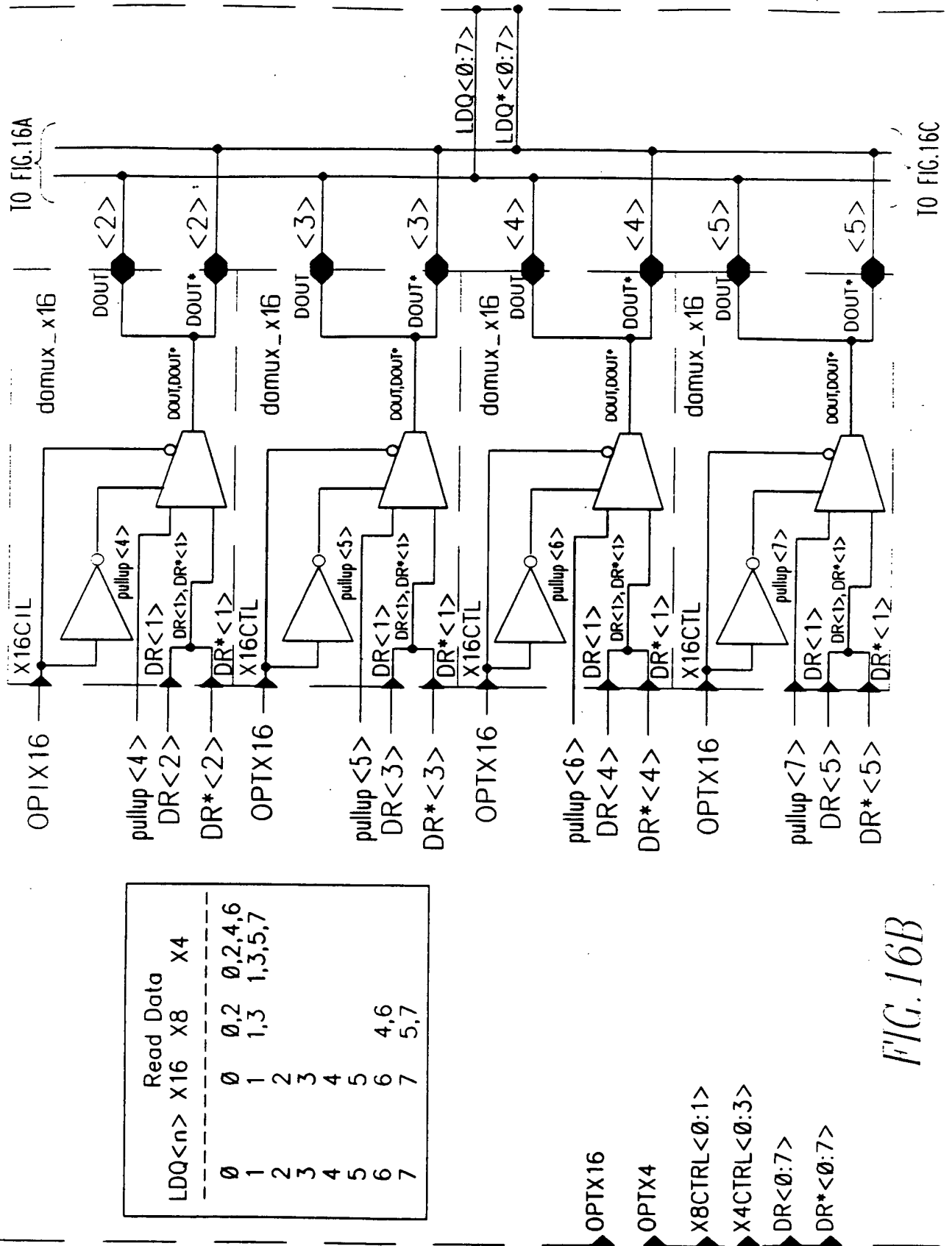
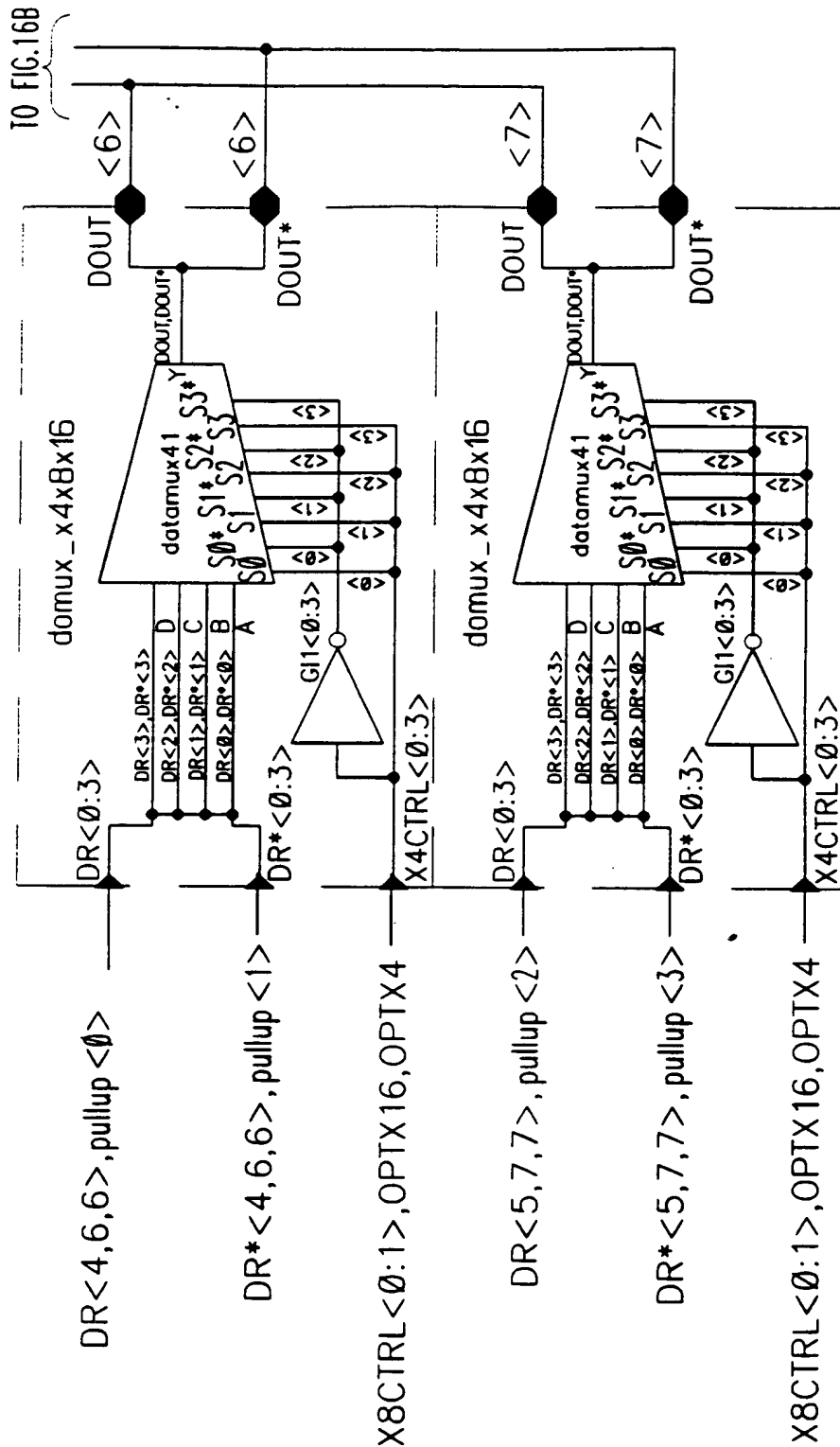
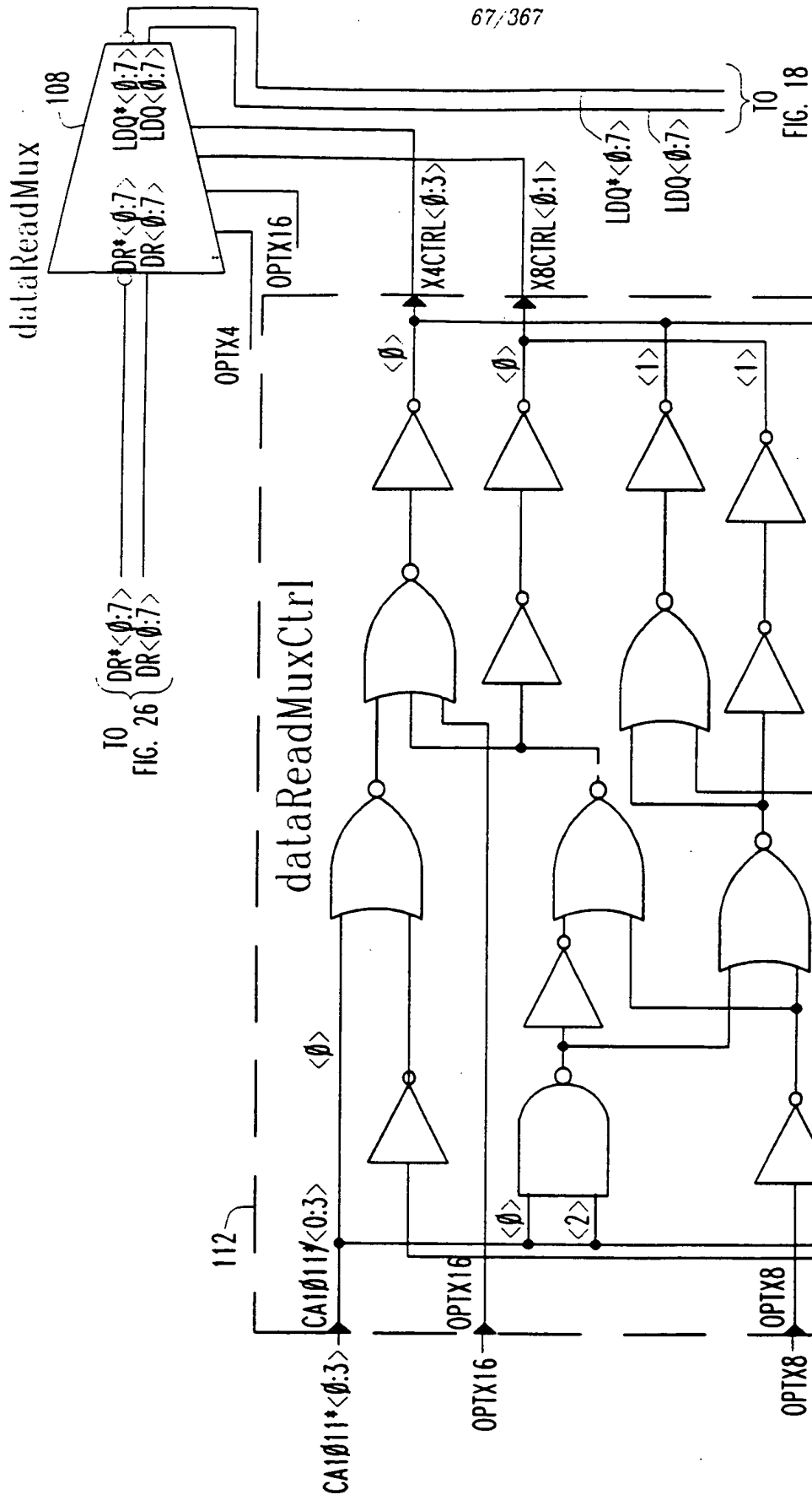


FIG. 16B



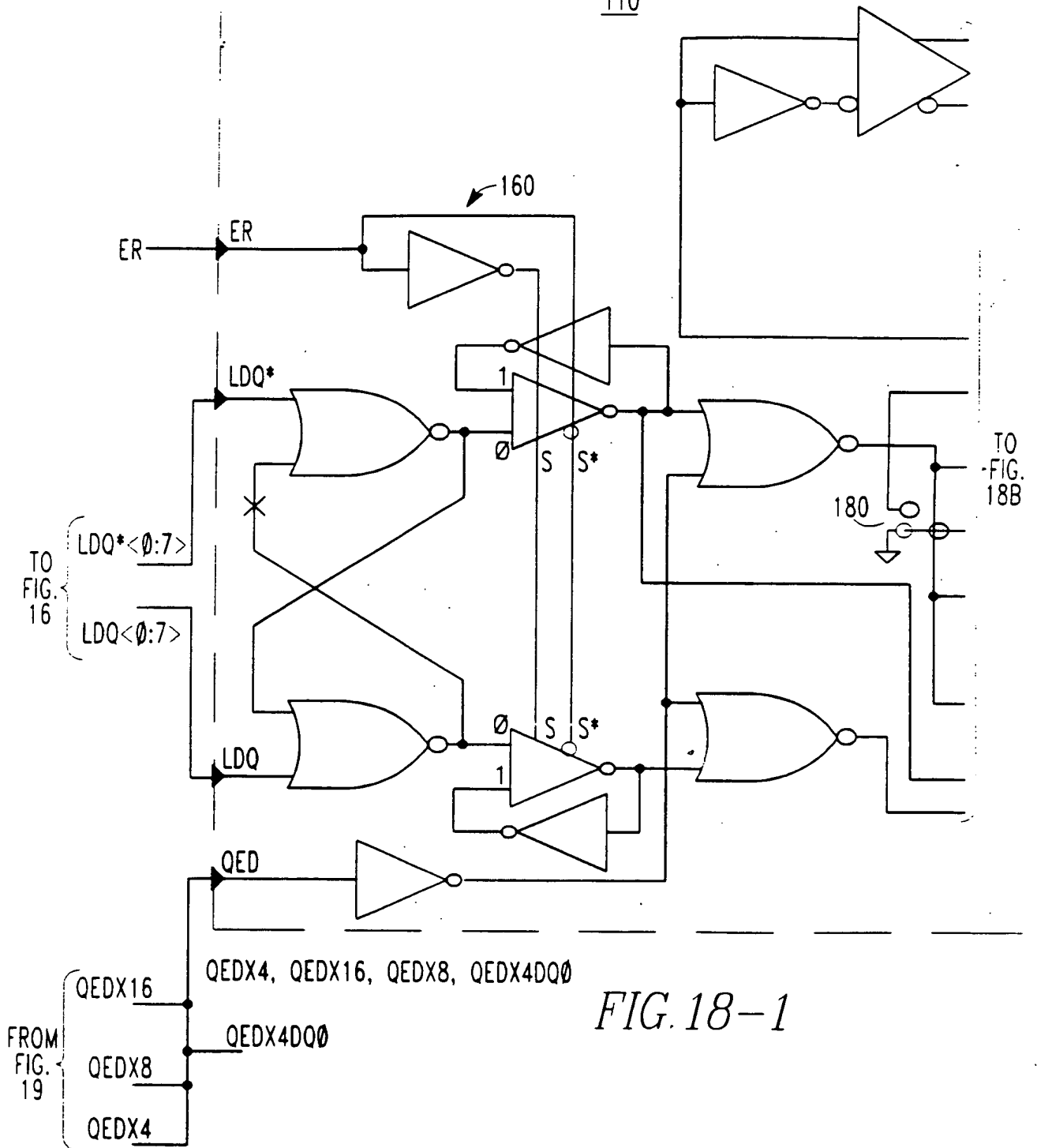
pullup <0:7>

dataReadMux



DATA OUTPUT BUFFER

110



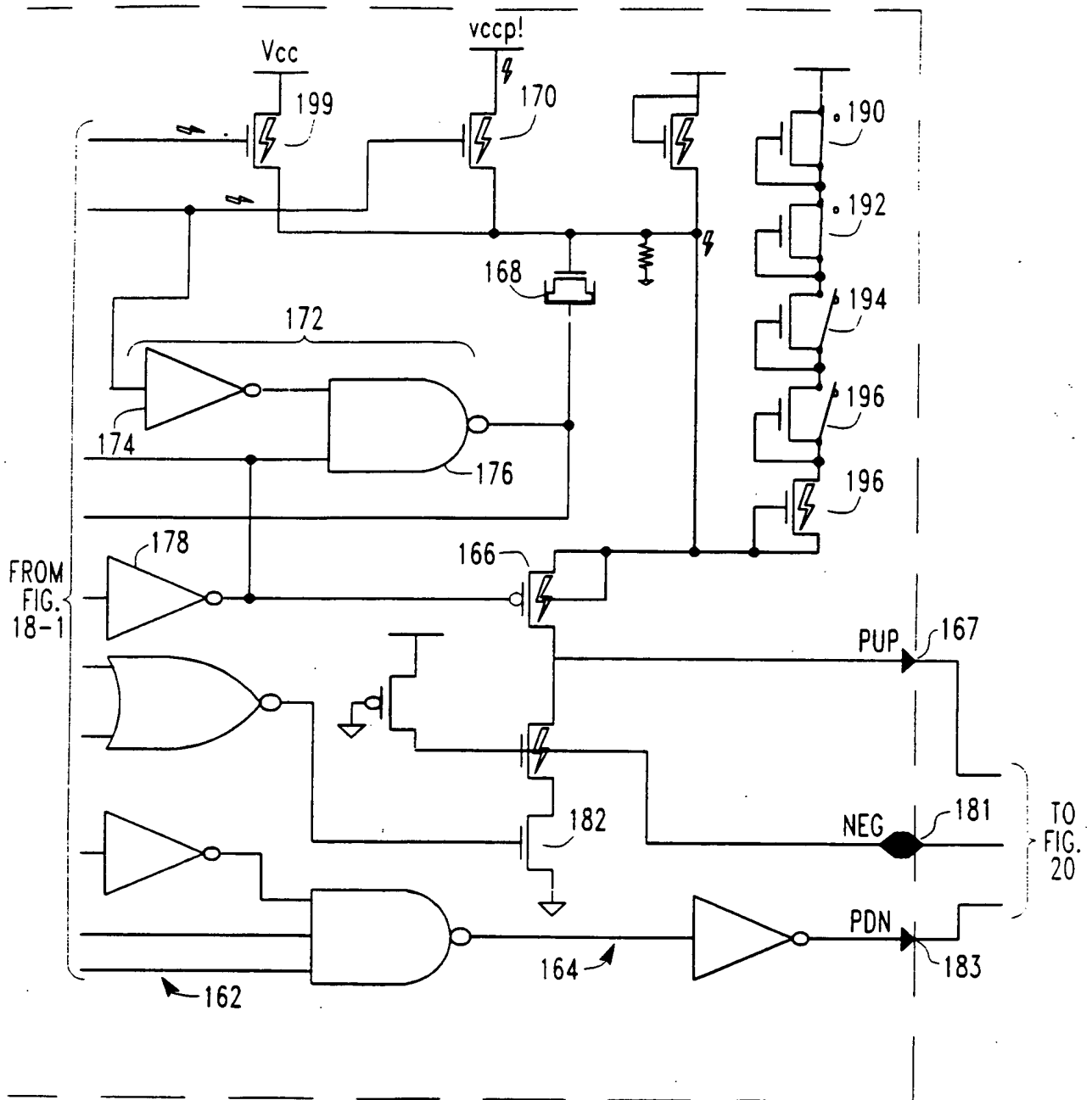
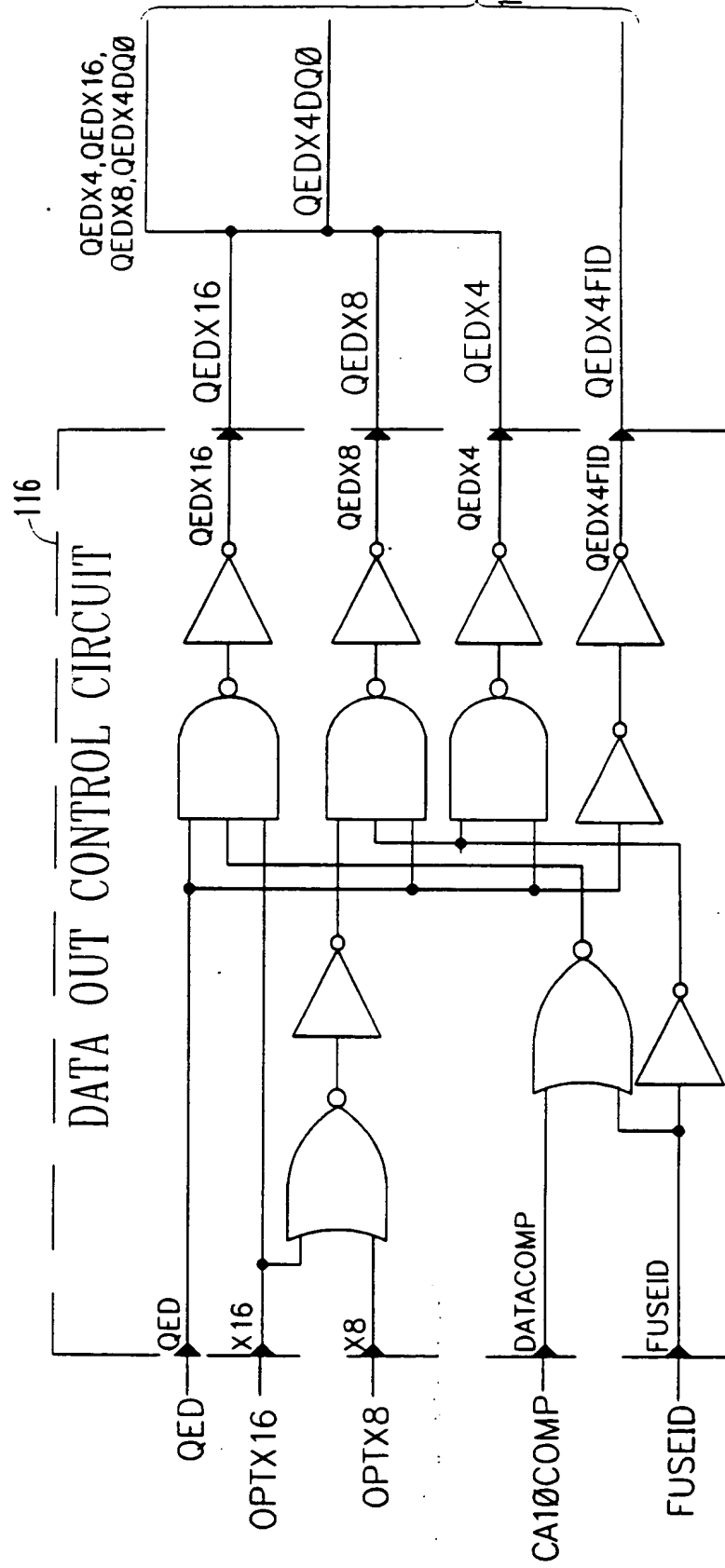


FIG. 18-2



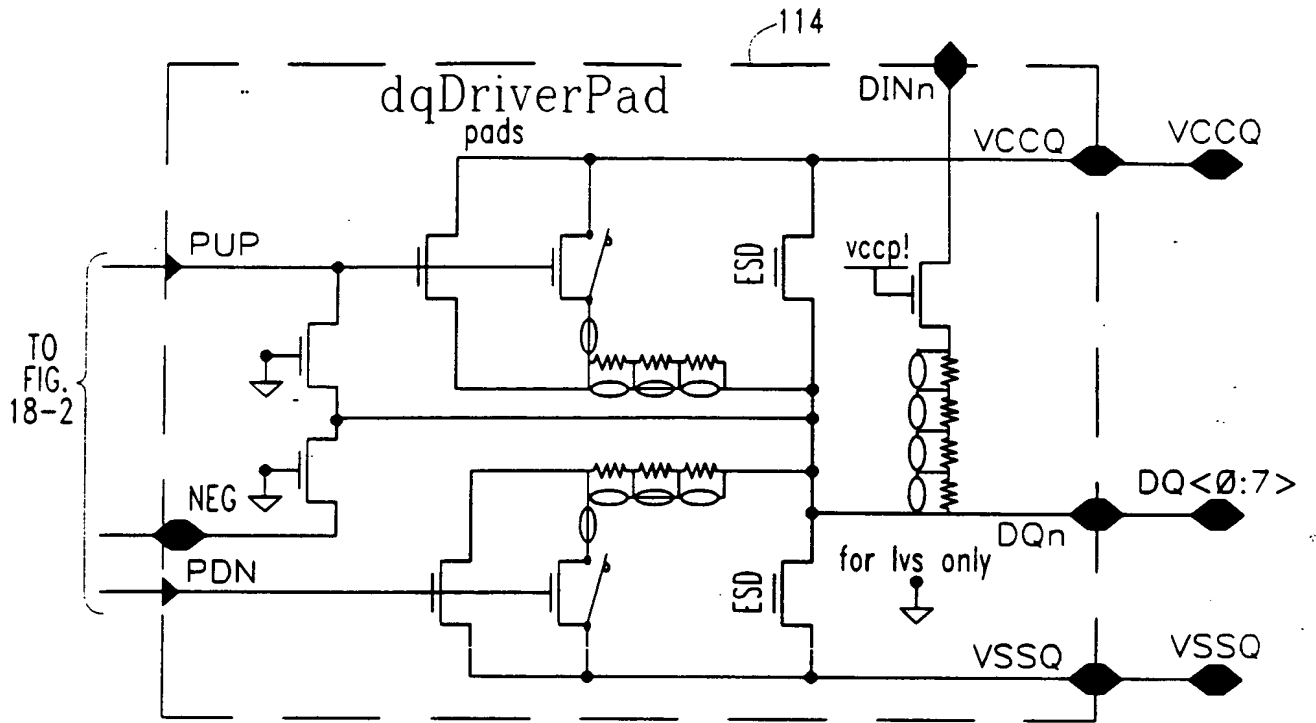


FIG. 20

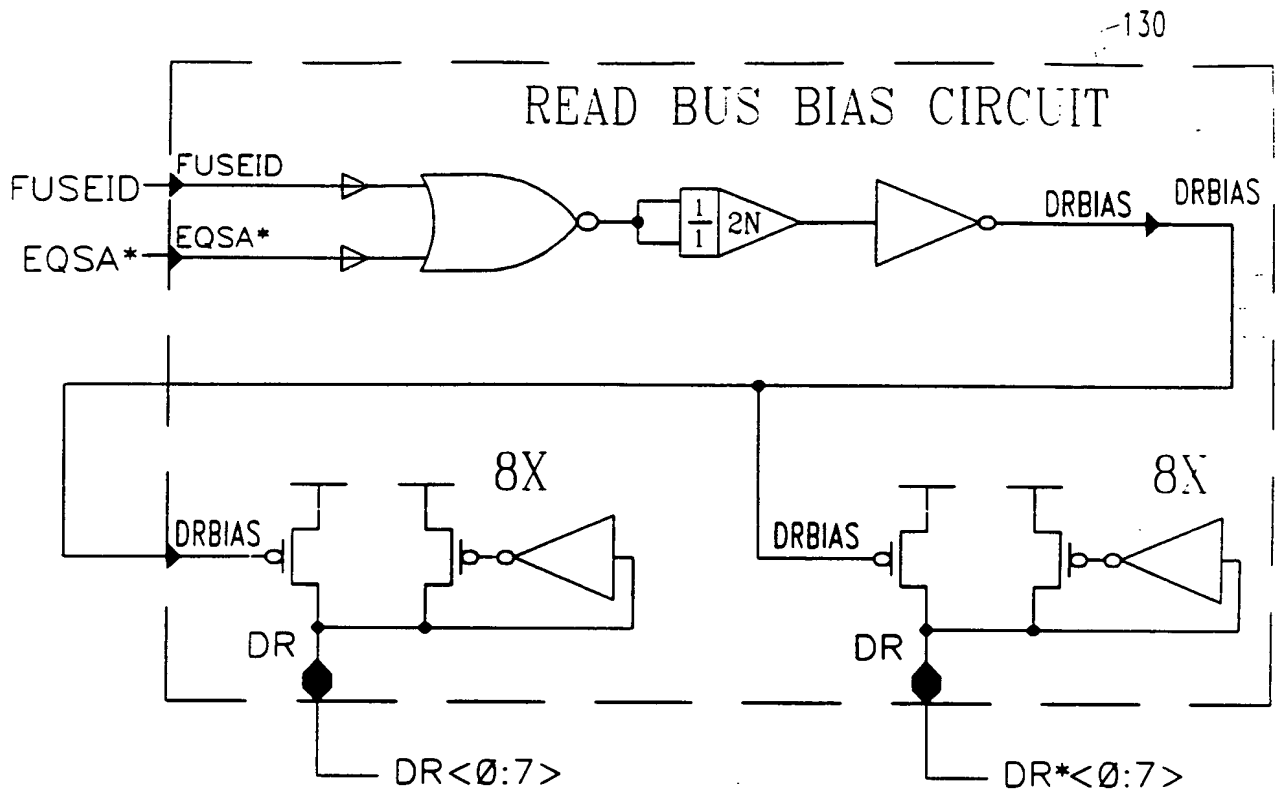


FIG. 21

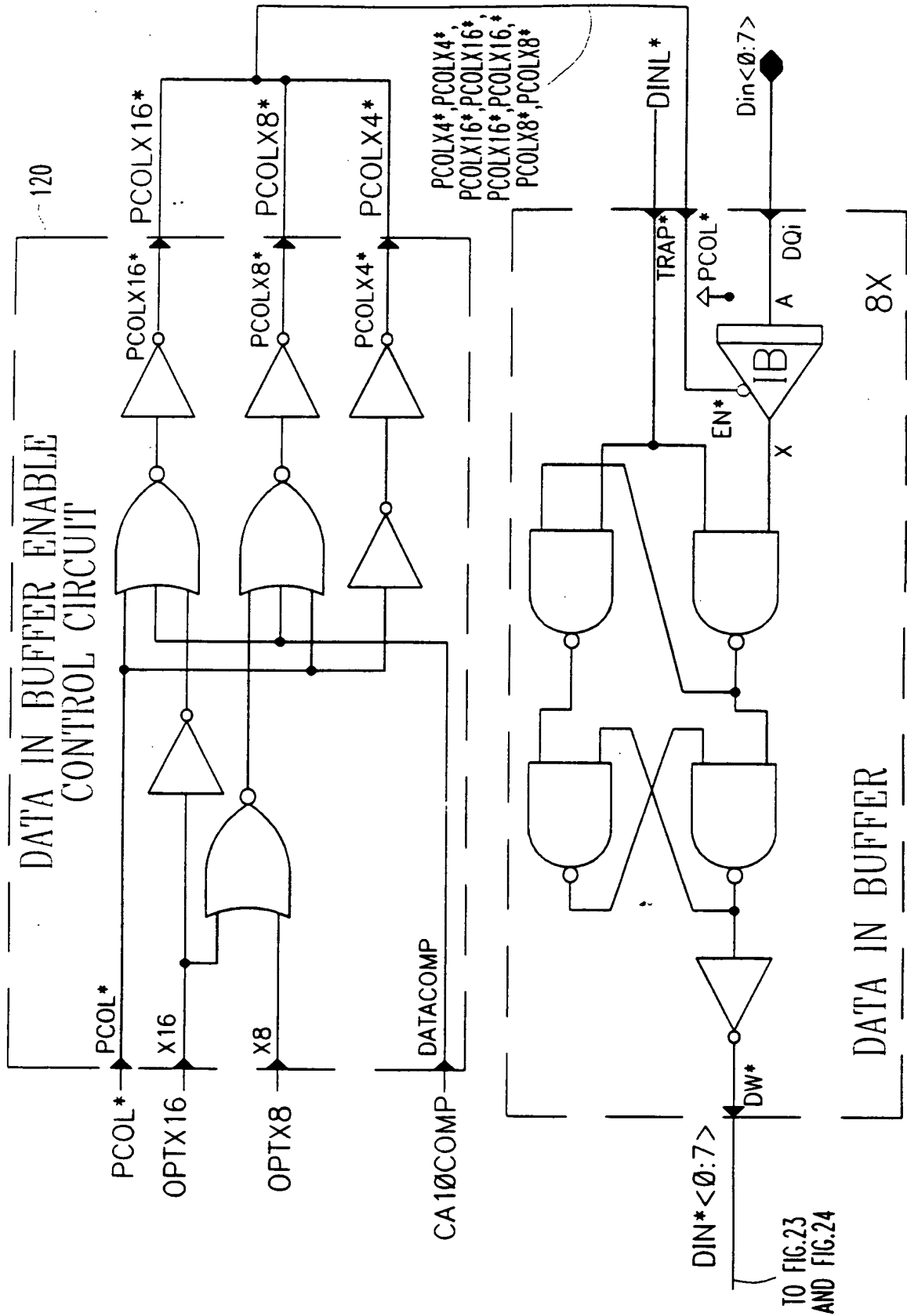
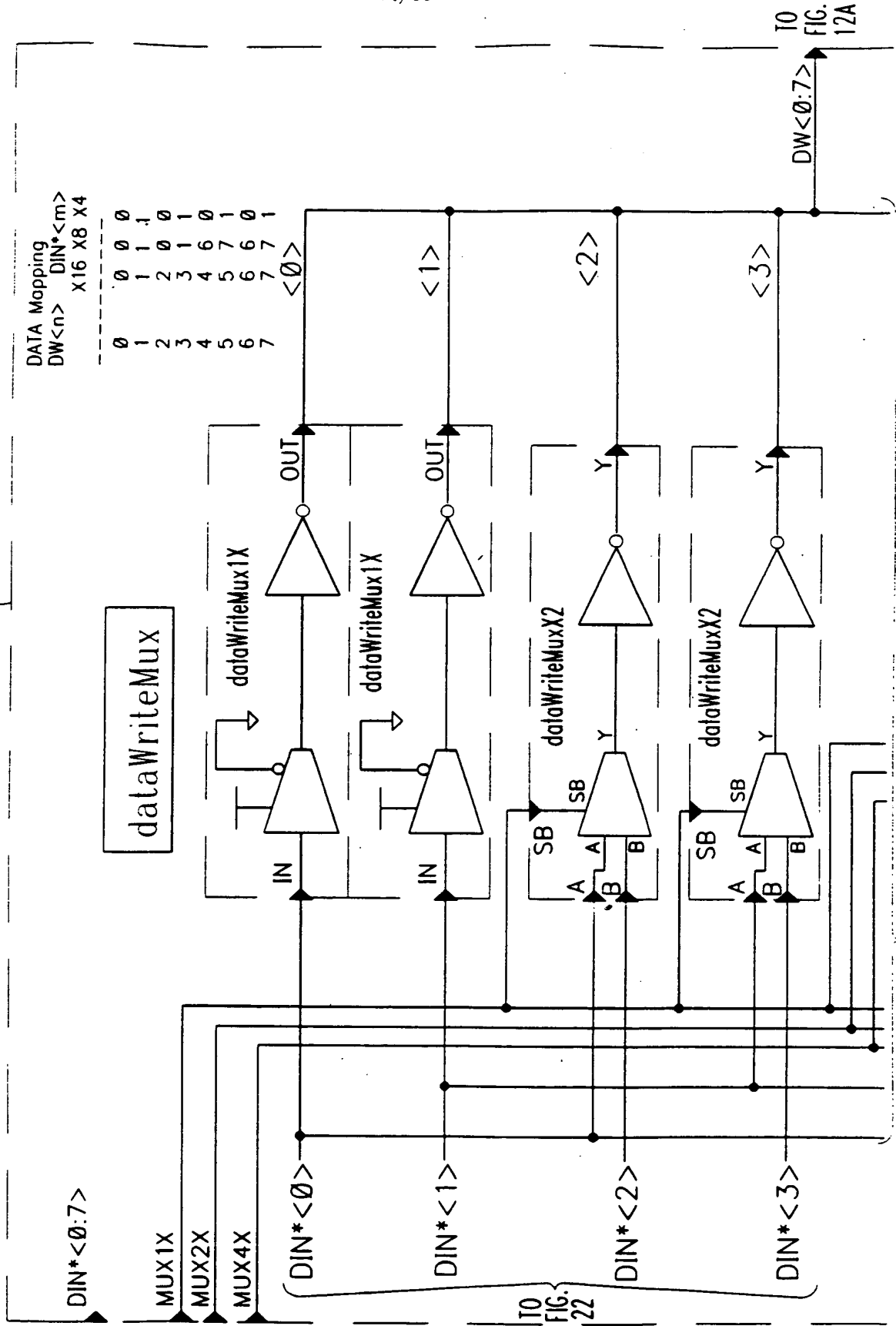


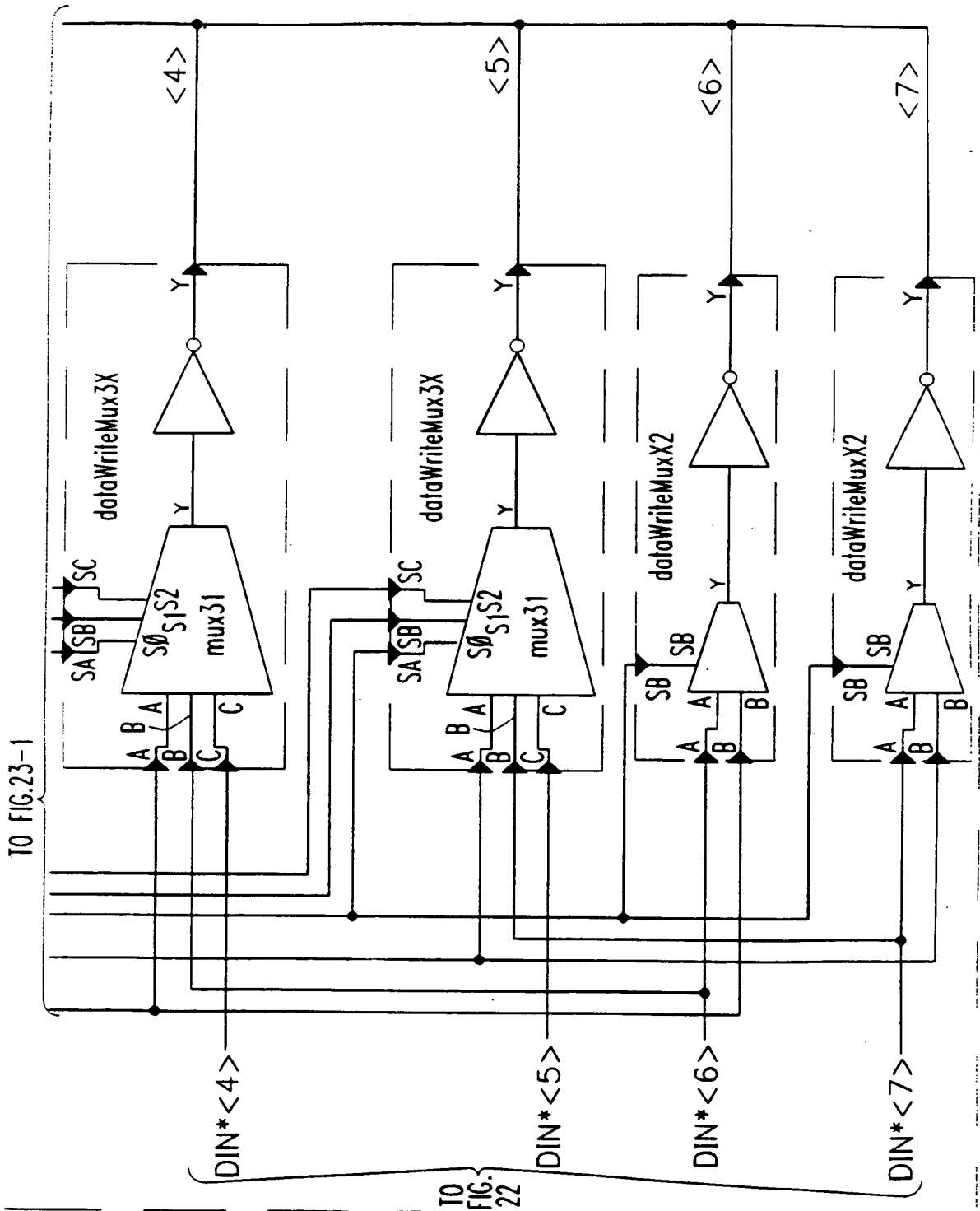
FIG. 22

FIG. 23-1

122



TO FIG. 23-2



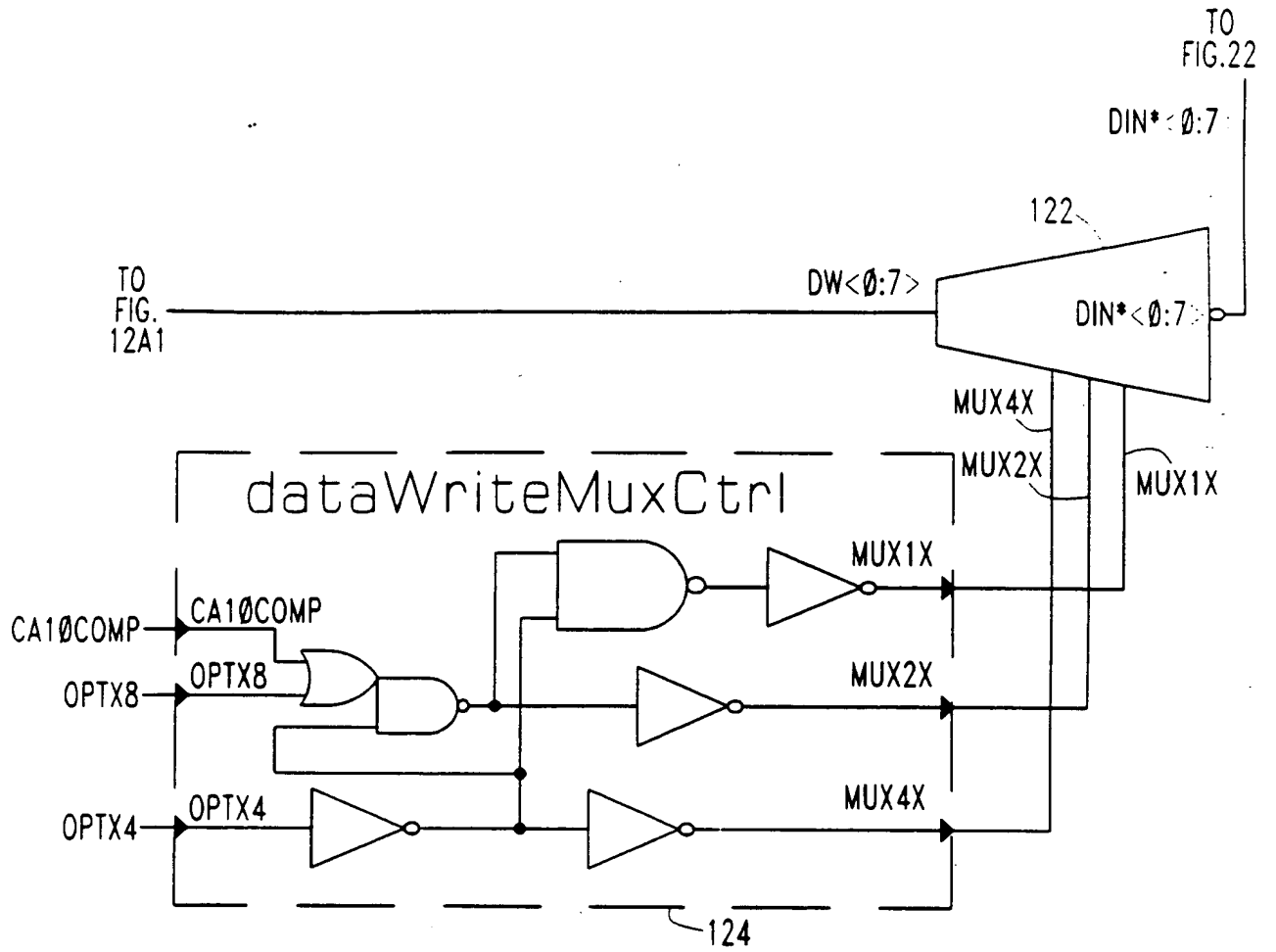


FIG. 24

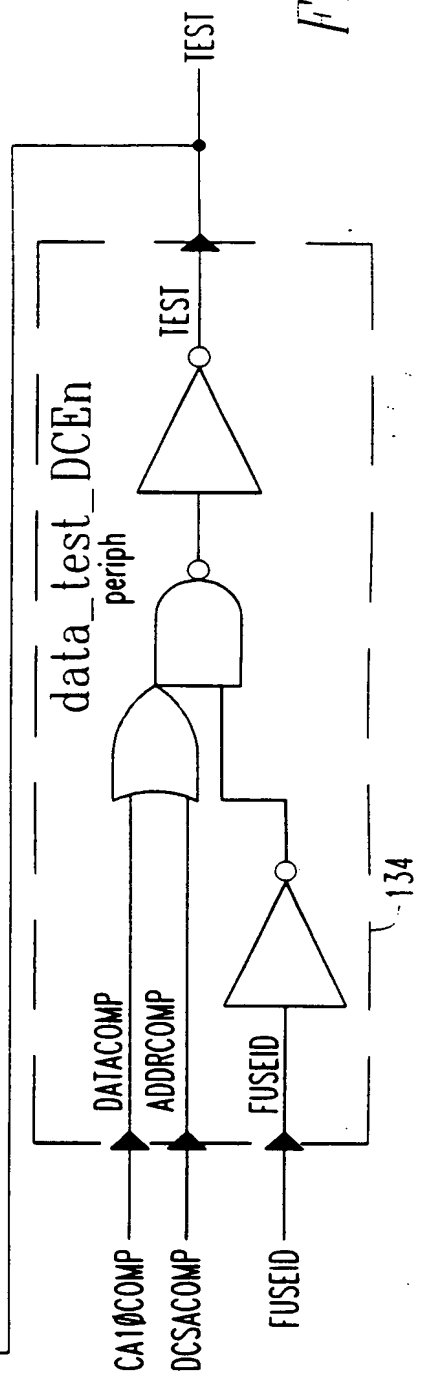
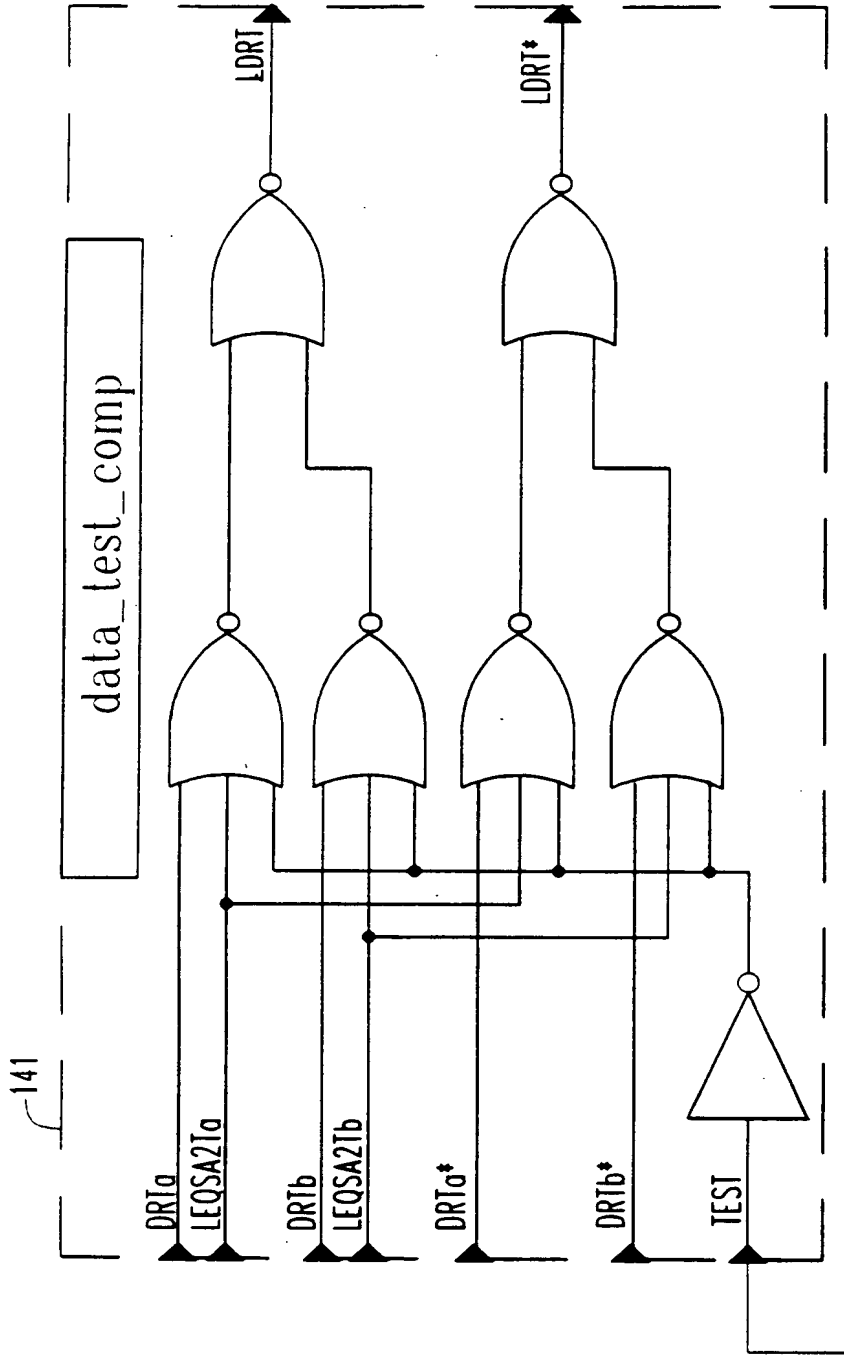


FIG. 25

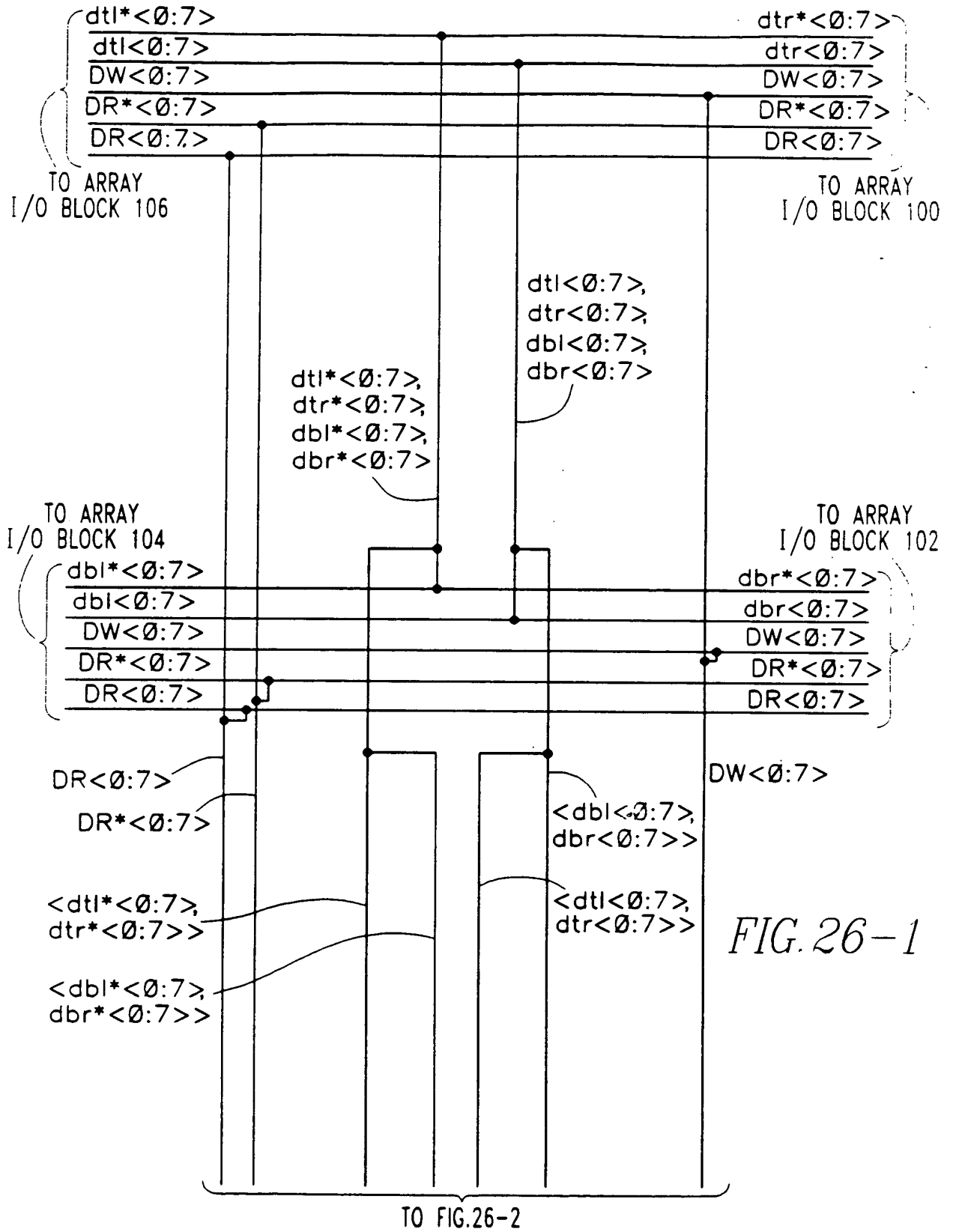


FIG. 26-1

FROM FIG.26-1

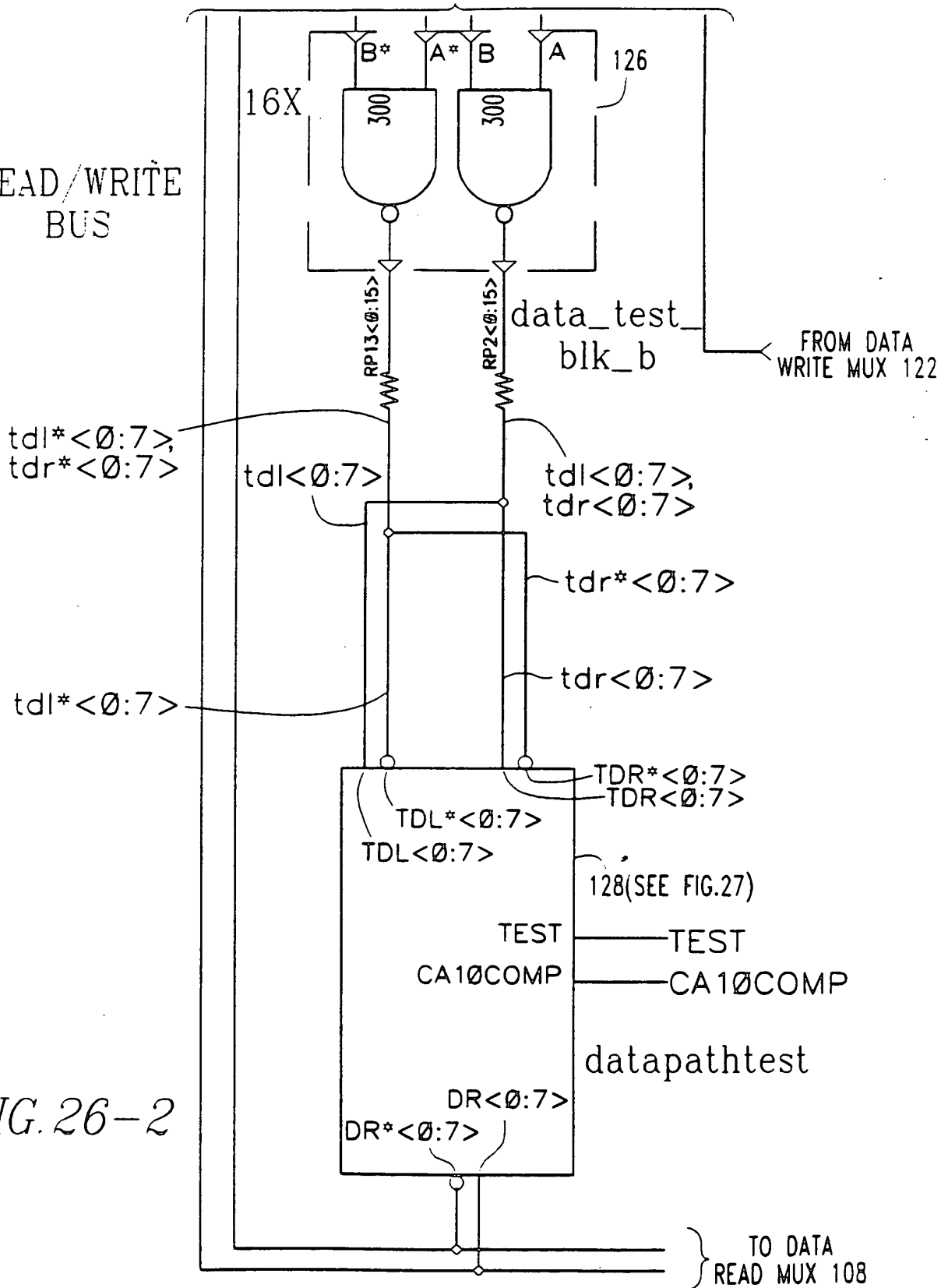
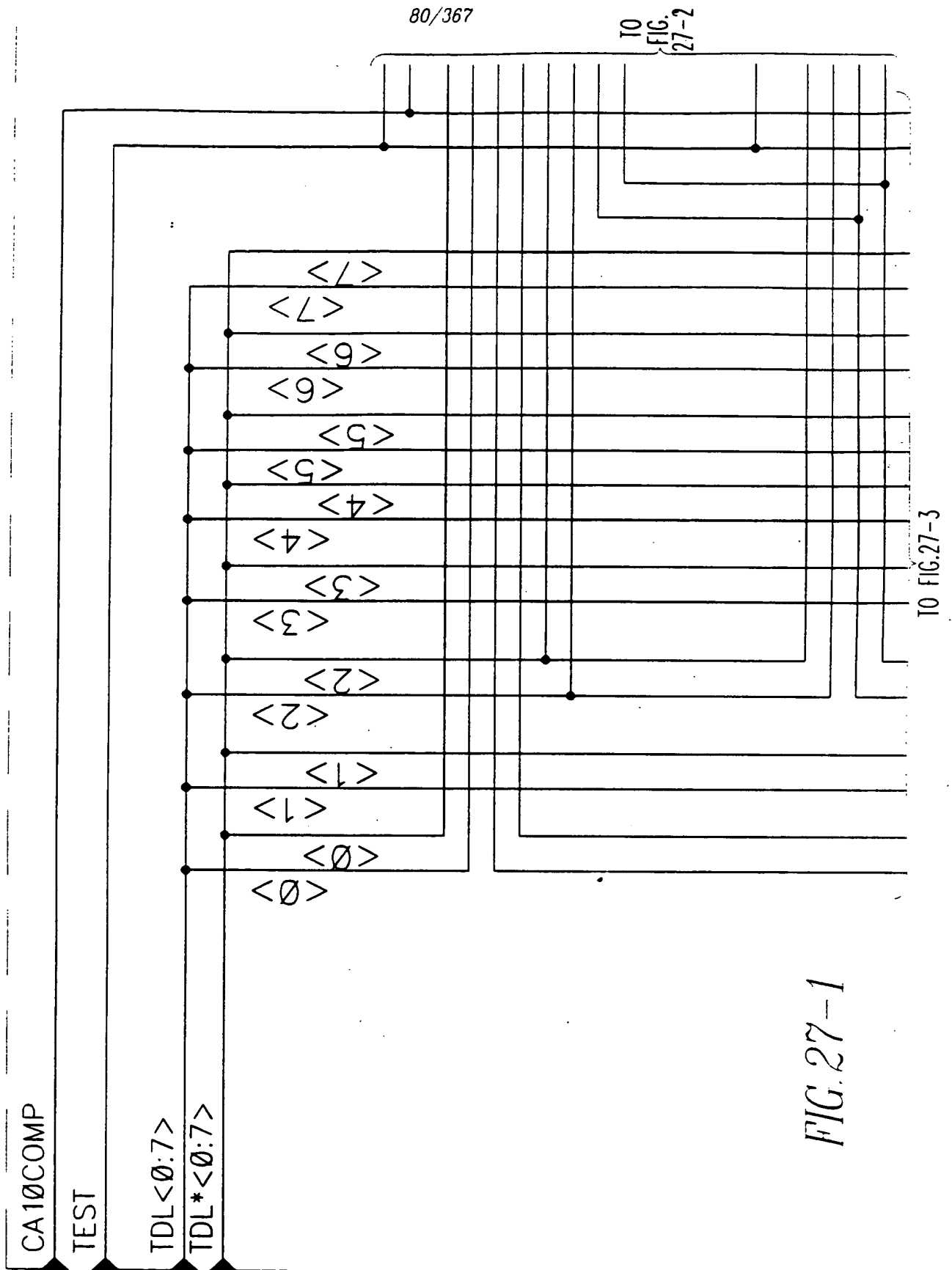
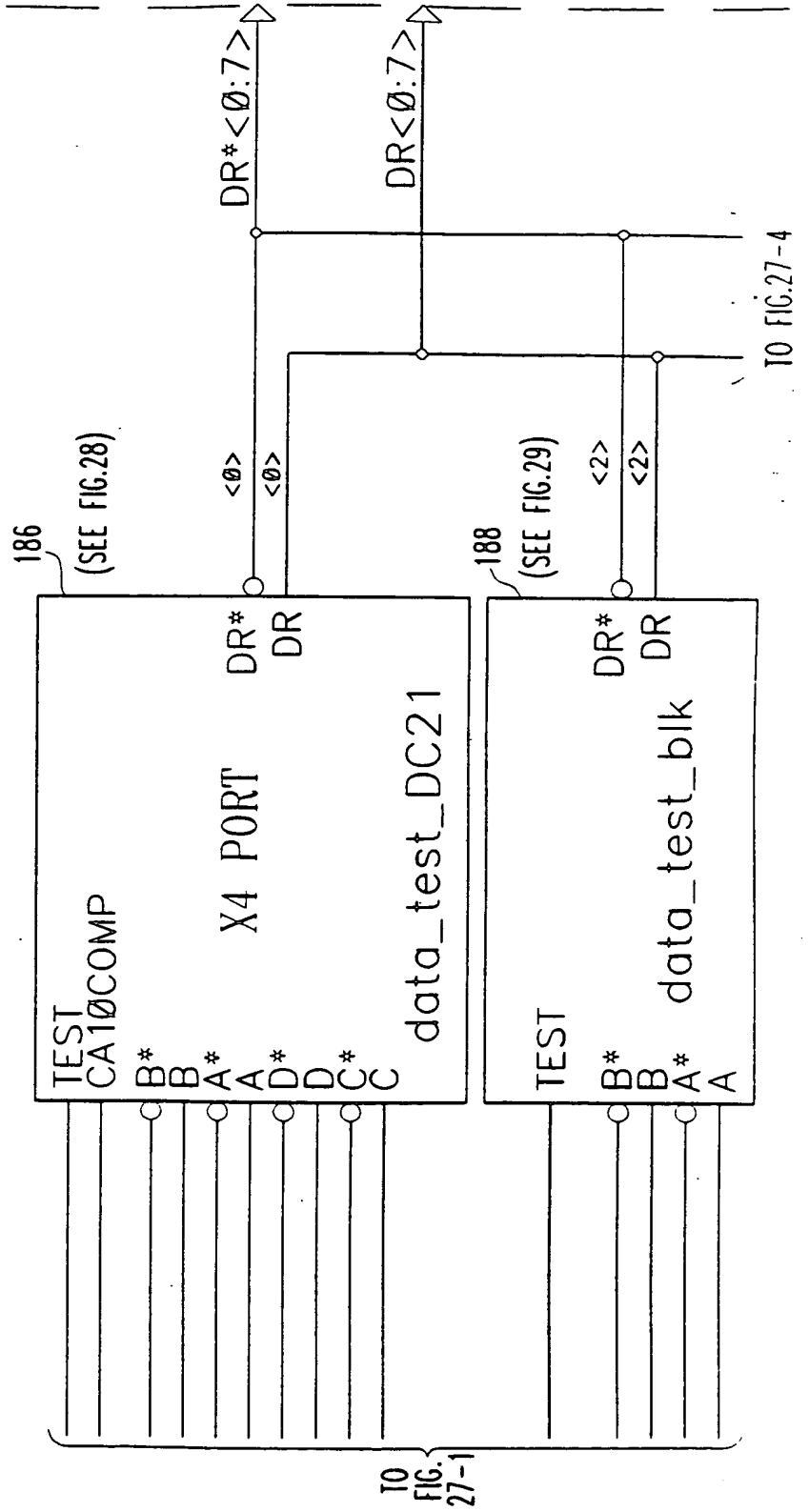
READ/WRITE
BUS

FIG. 26-2



dataPathTest
Block

128



10
FIG.
27-1

10 FIG. 27-4

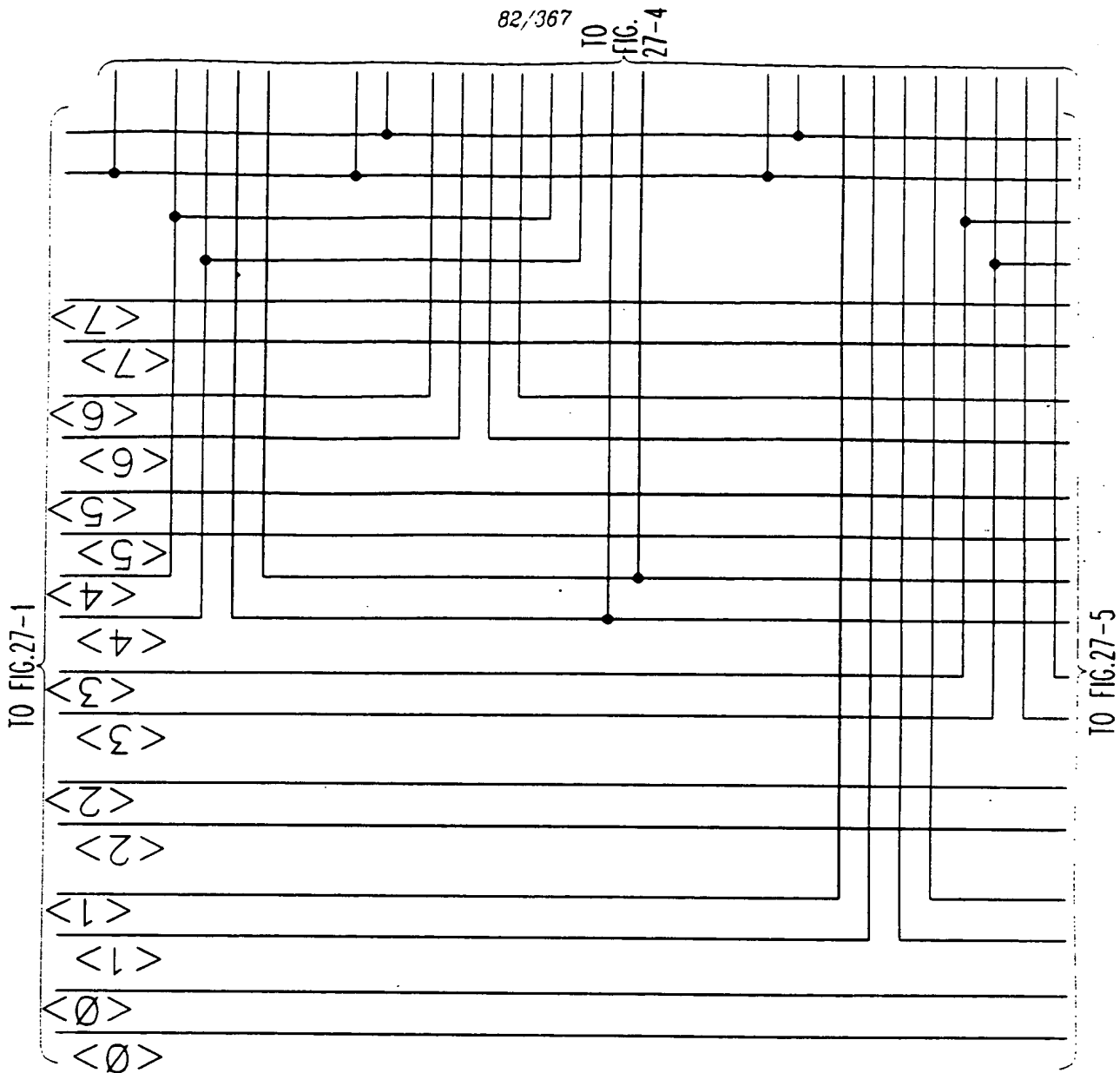


FIG. 27-3

FIG. 27-3

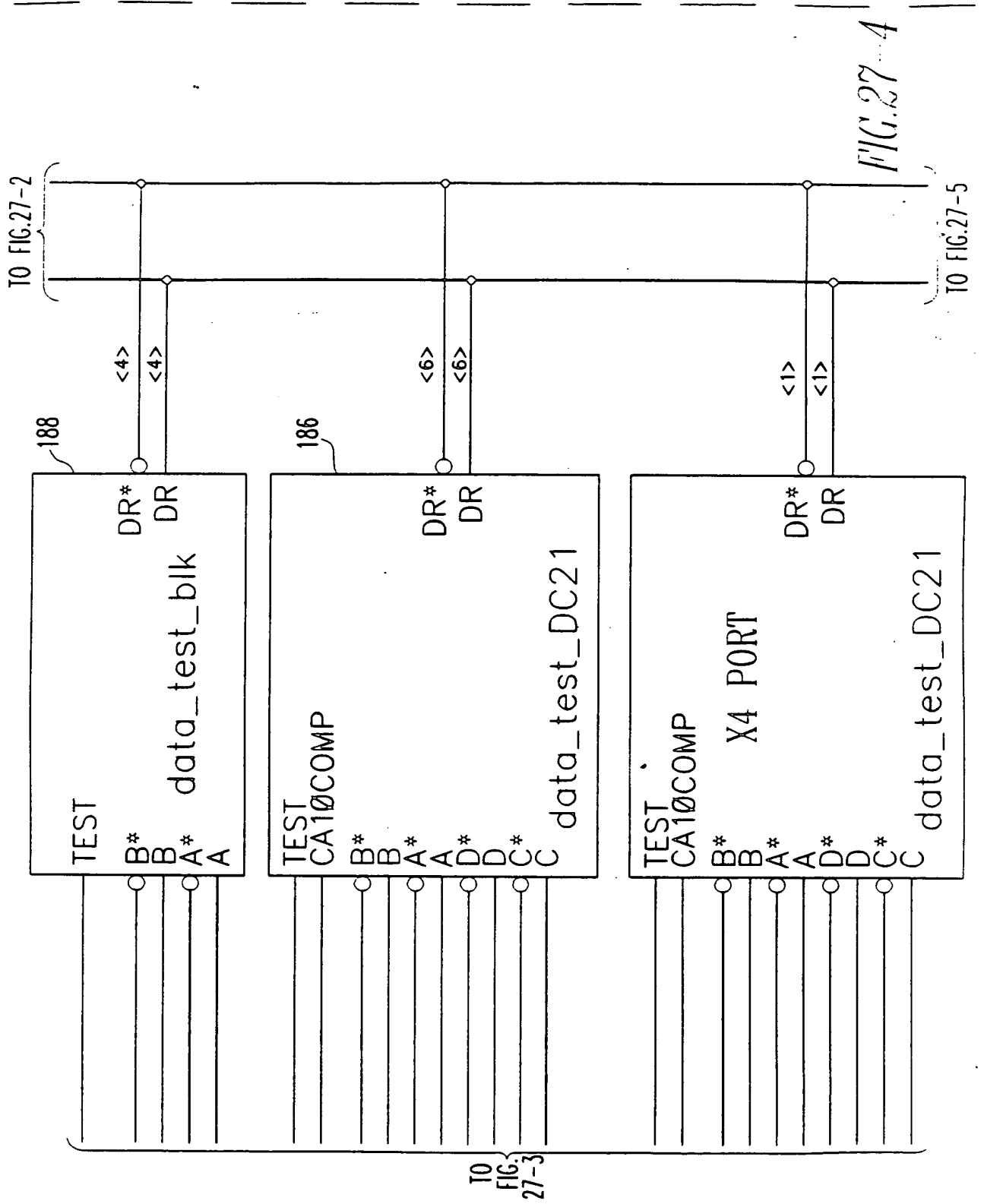
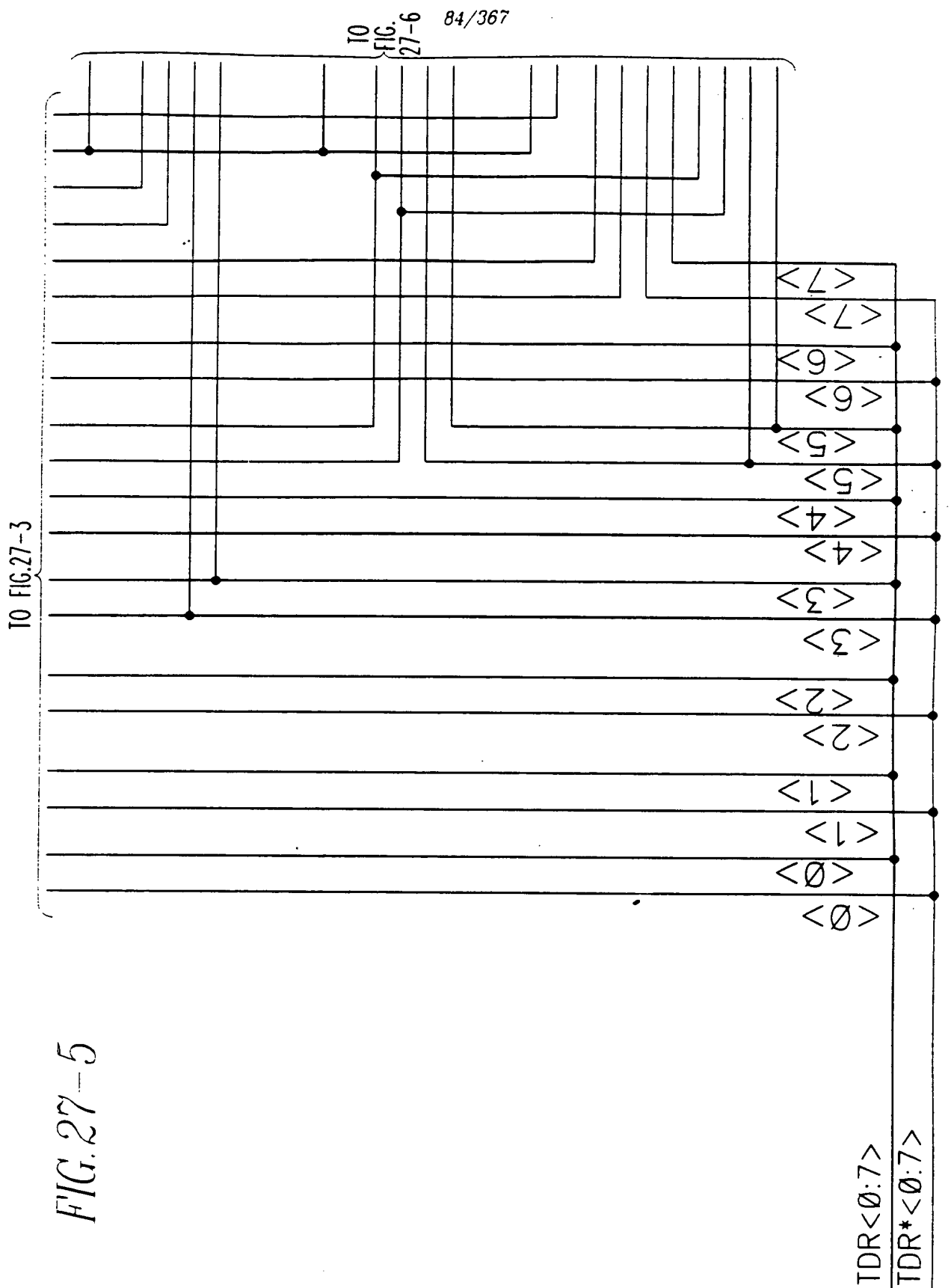


FIG. 27-5



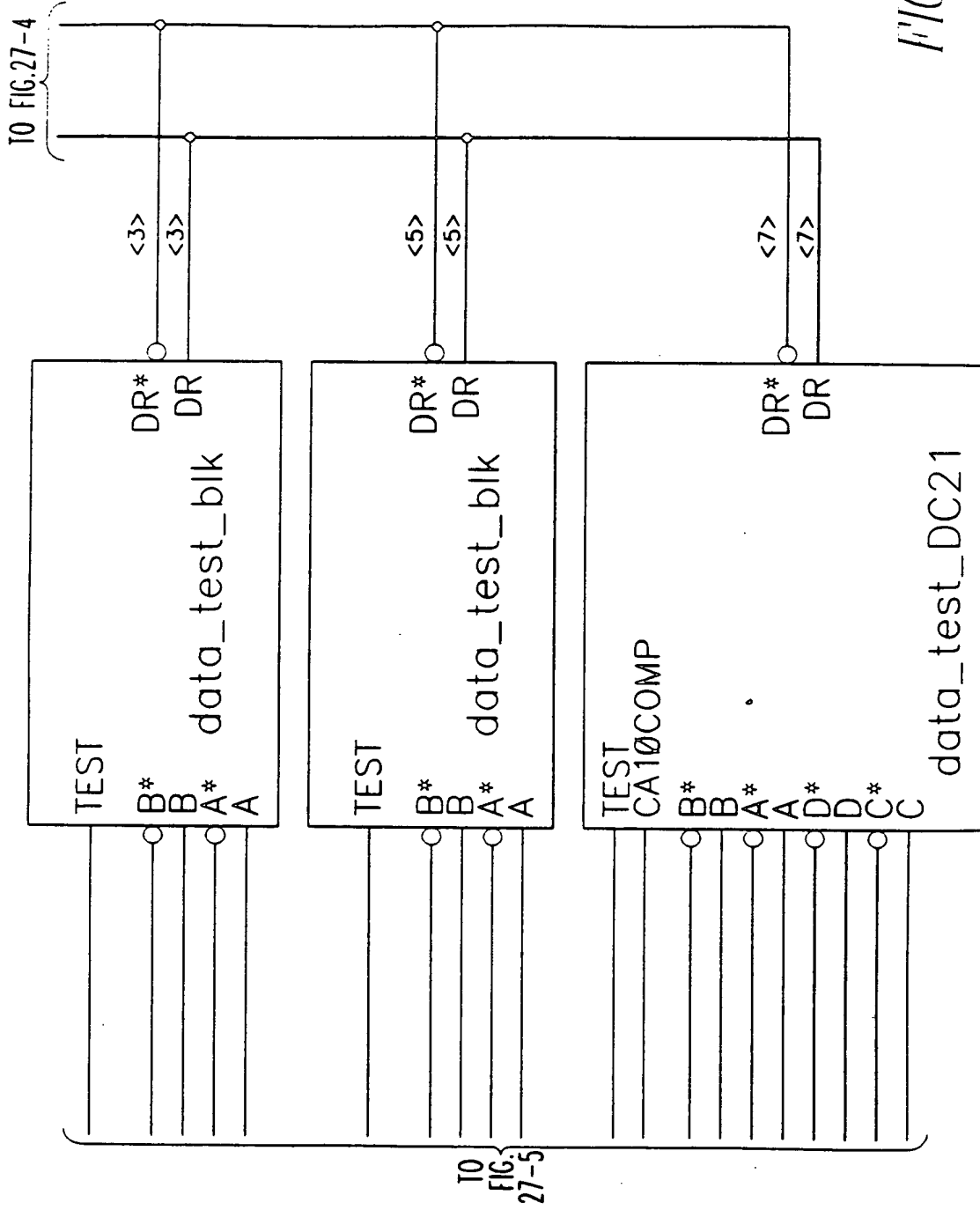


FIG. 27-6

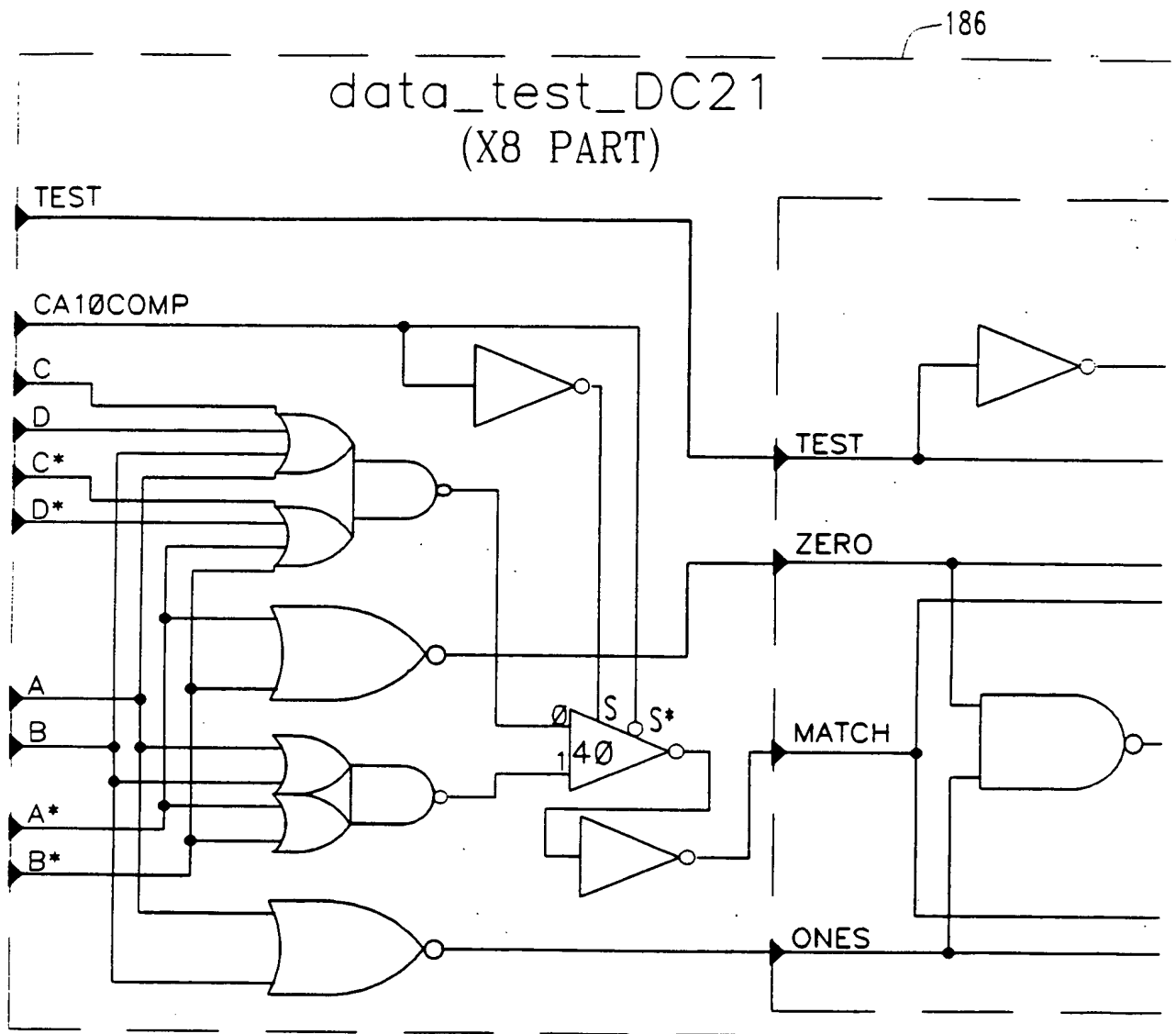


FIG. 28-1

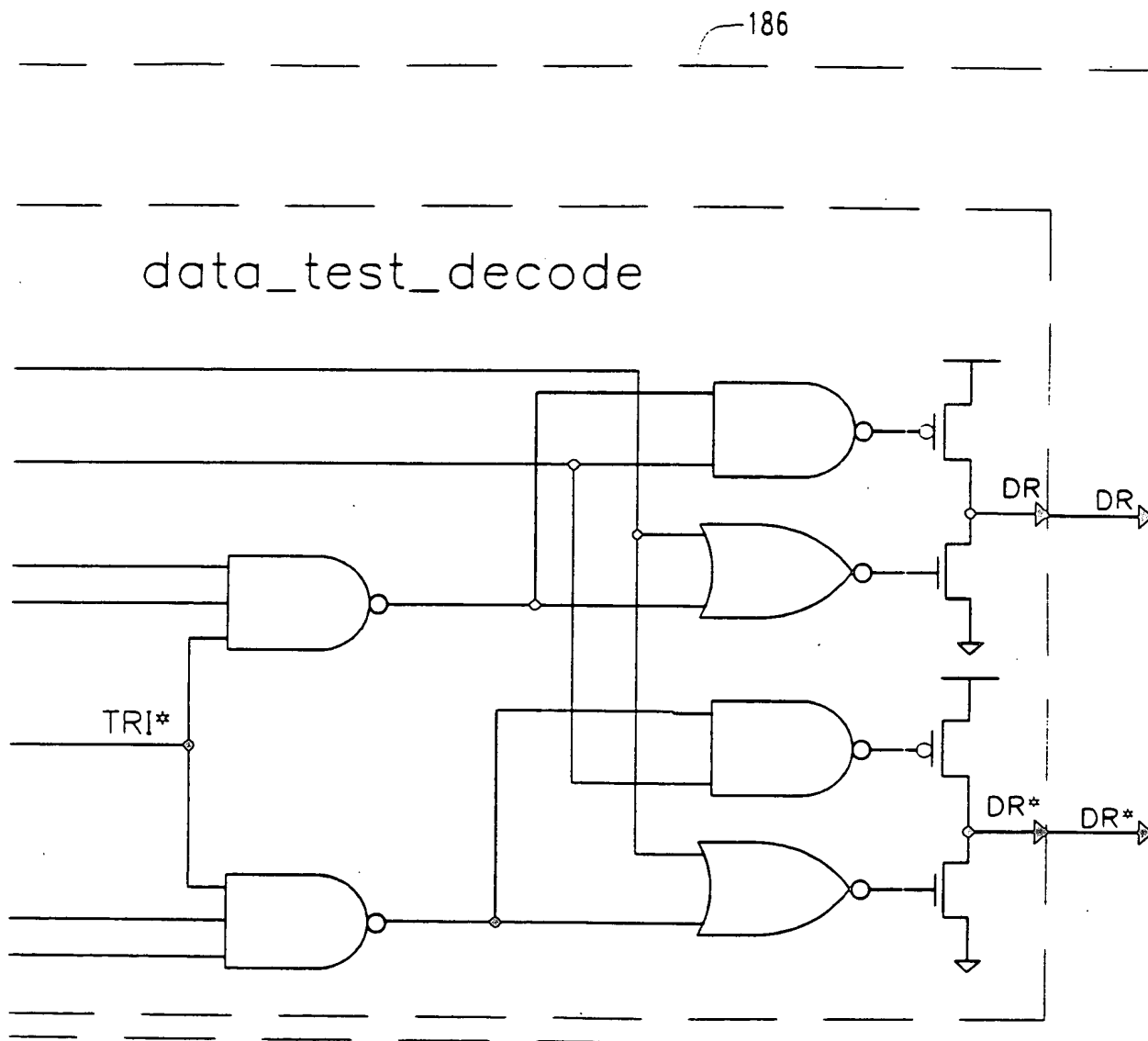


FIG. 28-2

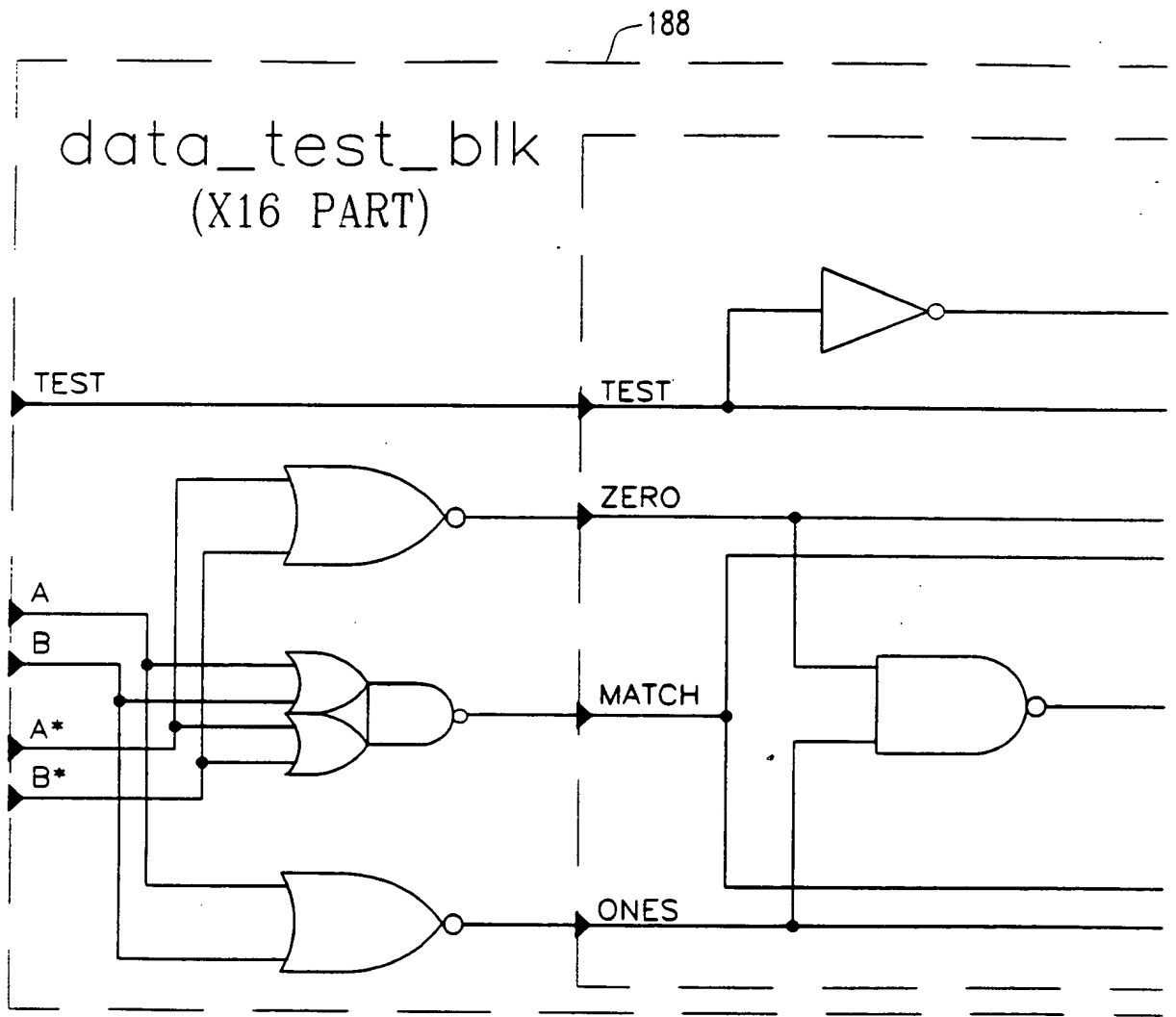


FIG. 29-1

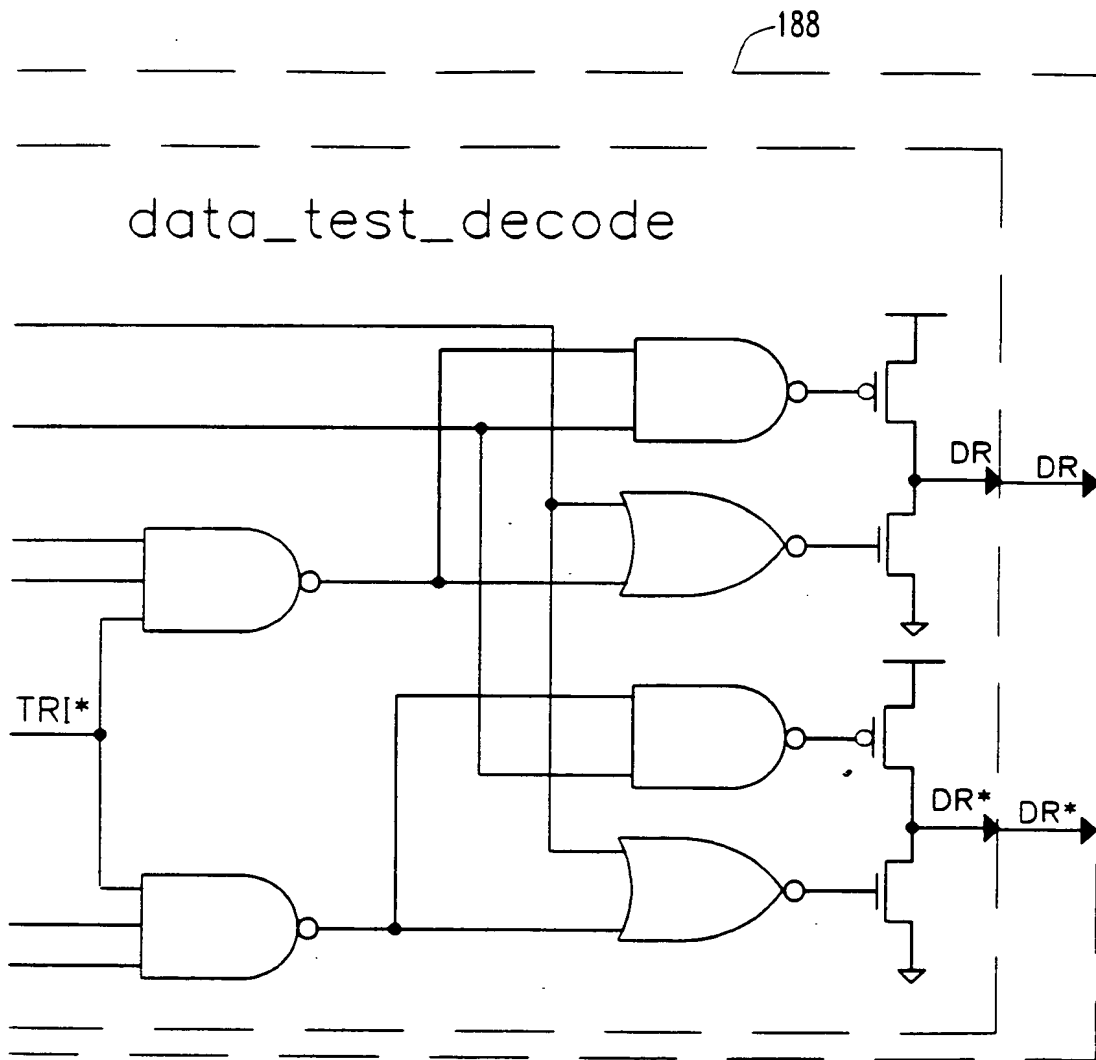
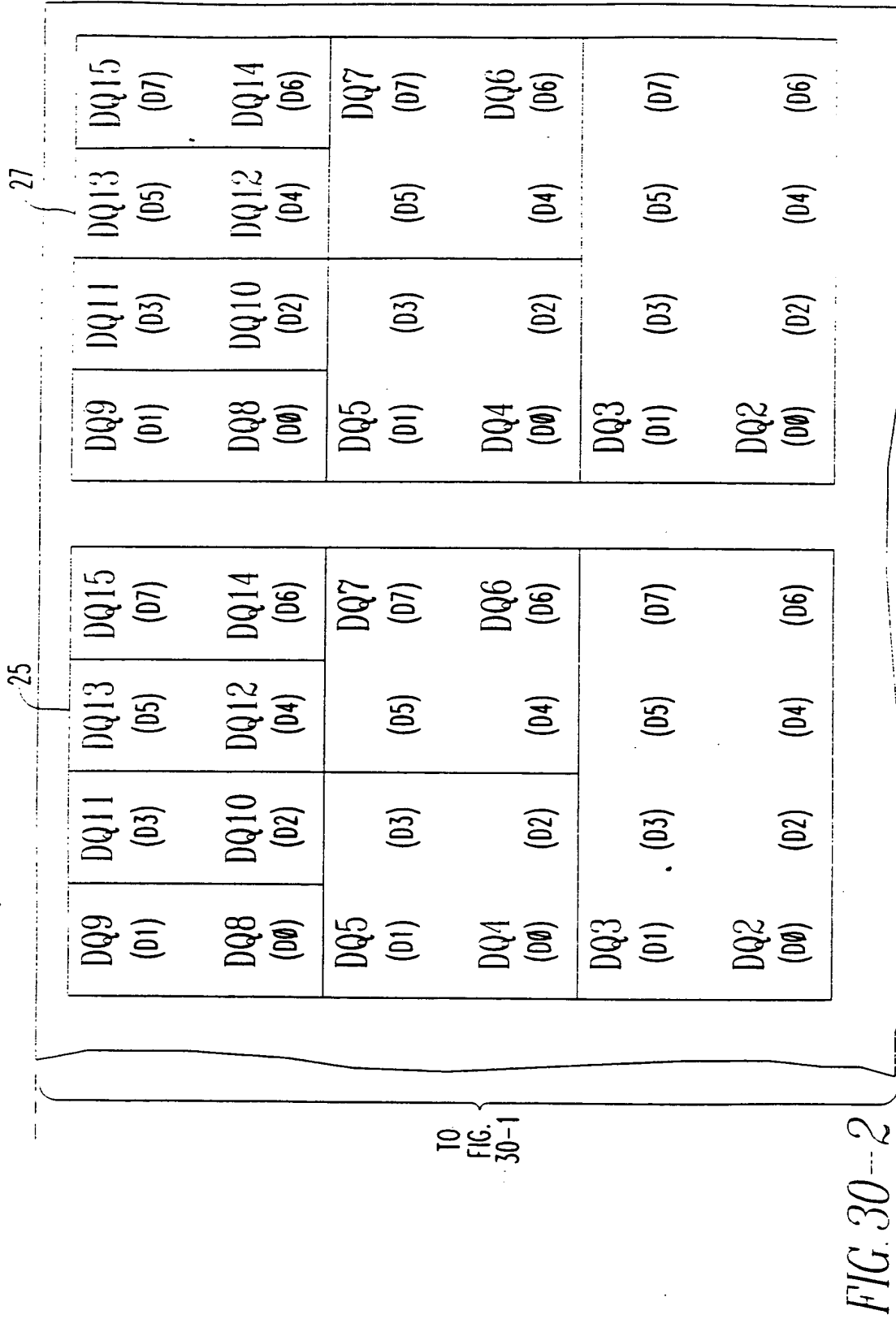


FIG. 29-2

X16	45	DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
		DQ0 (00)	DQ2 (02)	DQ4 (04)	DQ6 (06)
		DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
X8	47	DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
		DQ0 (00)	DQ2 (02)	DQ4 (04)	DQ6 (06)
		DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
X4	10 FIG. 30-2	DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)
		DQ0 (00)	DQ2 (02)	DQ4 (04)	DQ6 (06)
		DQ1 (01)	DQ3 (03)	DQ5 (05)	DQ7 (07)

TO FIG. 30-3

TO
FIG.
30-1



10 FIG. 30-4

FIG. 30-1

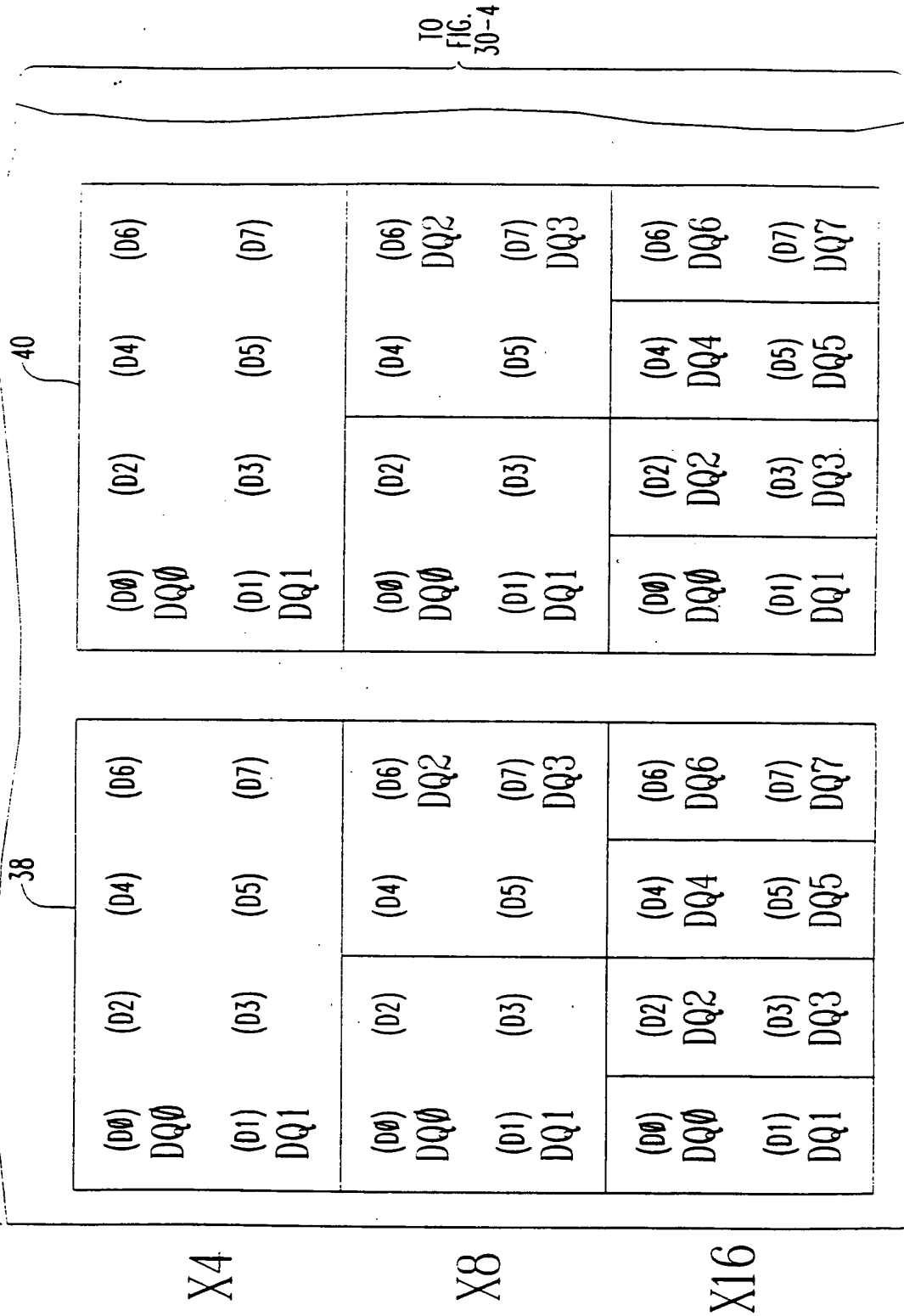


FIG. 30-3

TO FIG. 30-2

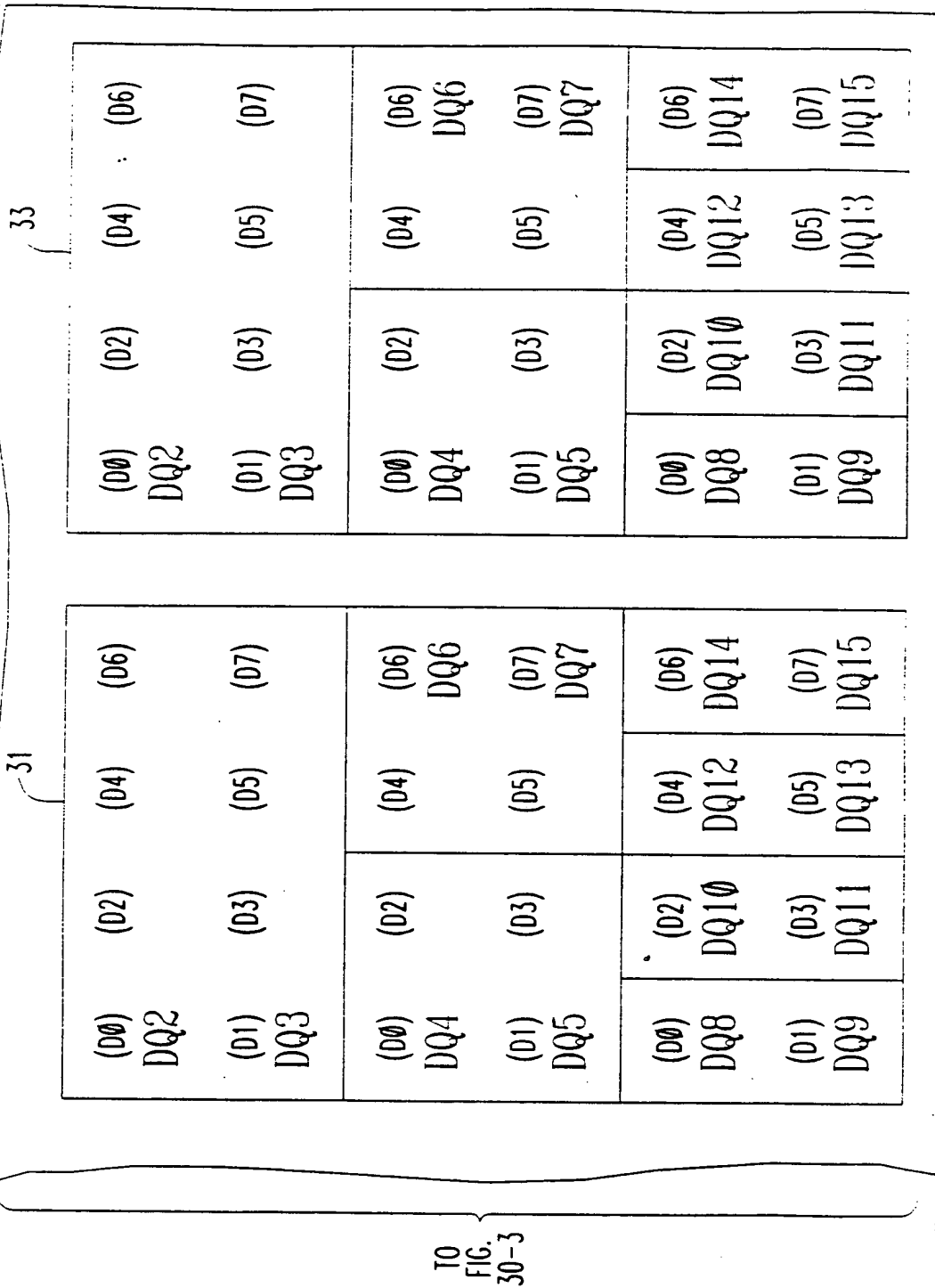


FIG. 30-4

X4	VSSQ	N/C	VSSQ	N/C	N/C	
X8	VSSQ	DQ3	VSSQ	DQ2	N/C	
X16	VSSQ	DQ7	VSSQ	DQ6	DQ5	
	1	2	3	4	5	6
X16	VCCQ	DQ0	VCCQ	DQ1	DQ2	VCCQ
X8	VCCQ	DQ0	VCCQ	DQ1	N/C	N/C
X4	VCCQ	DQ0	VCCQ	DQ1	N/C	N/C

TO FIG. 31A2

FIG. 31A1

[illegible]

FIG. 31A2

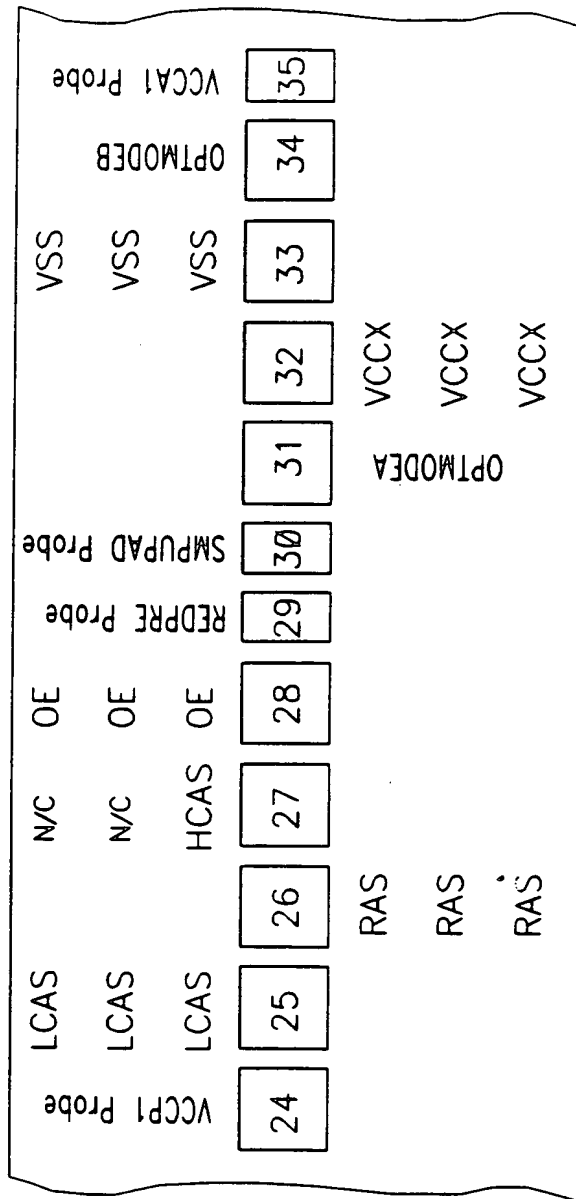


FIG. 31B1

TO FIG. 31B2

TO FIG. 31B1
↓

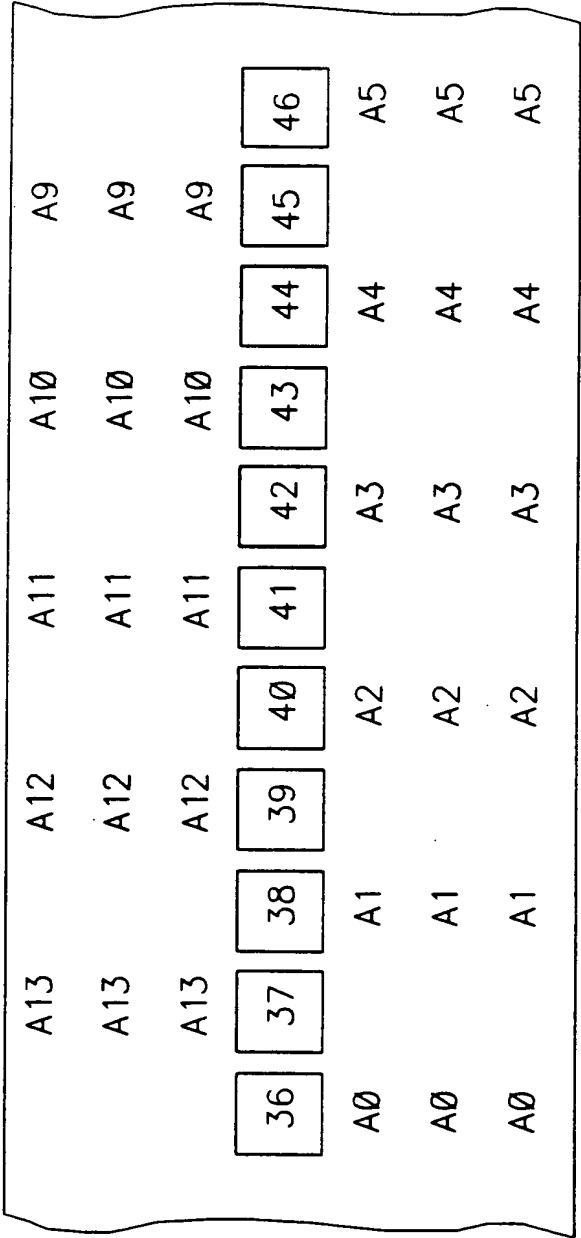


FIG. 31B2

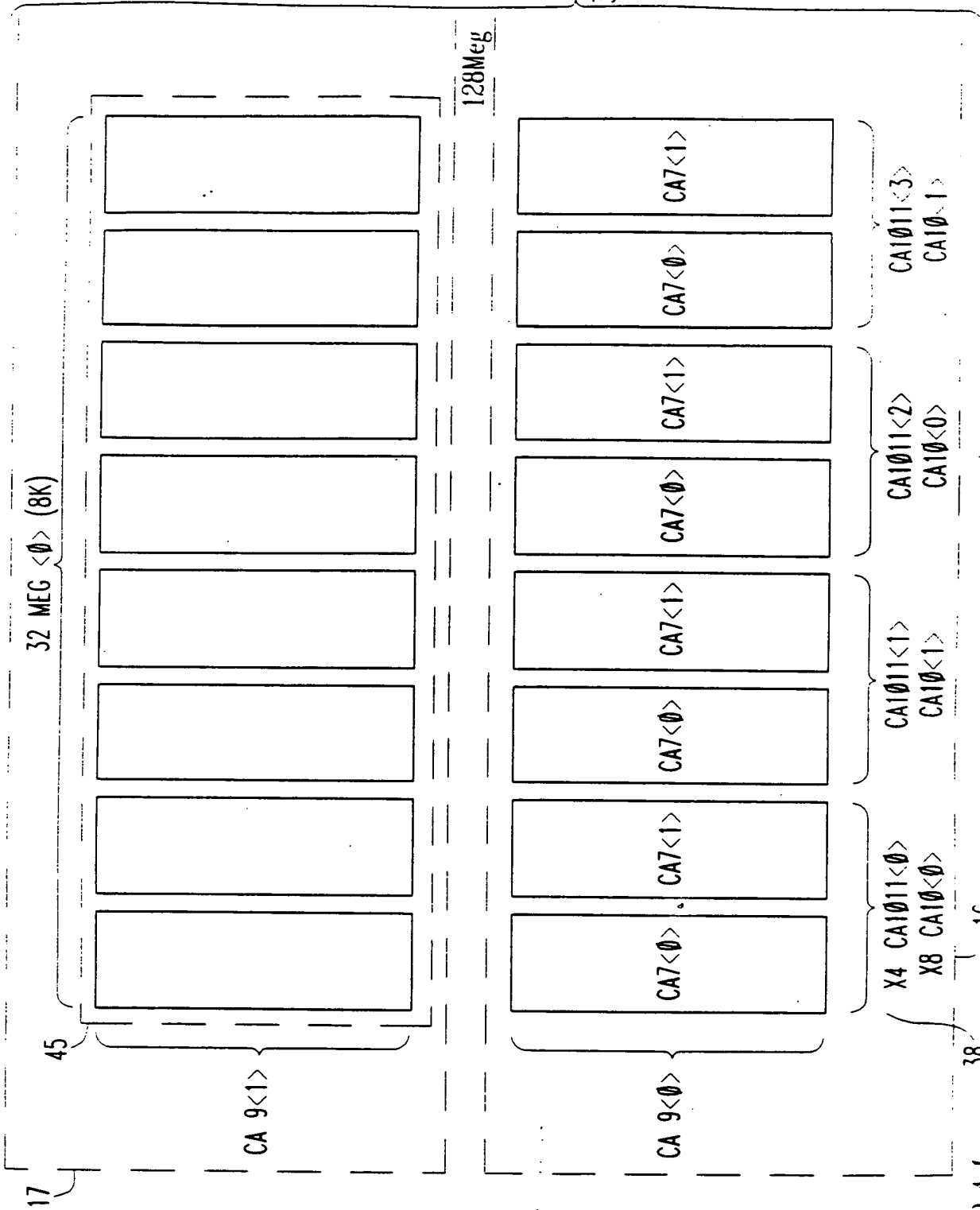
TO FIG. 31C2

FIG. 31C1

TO FIG. 31C1

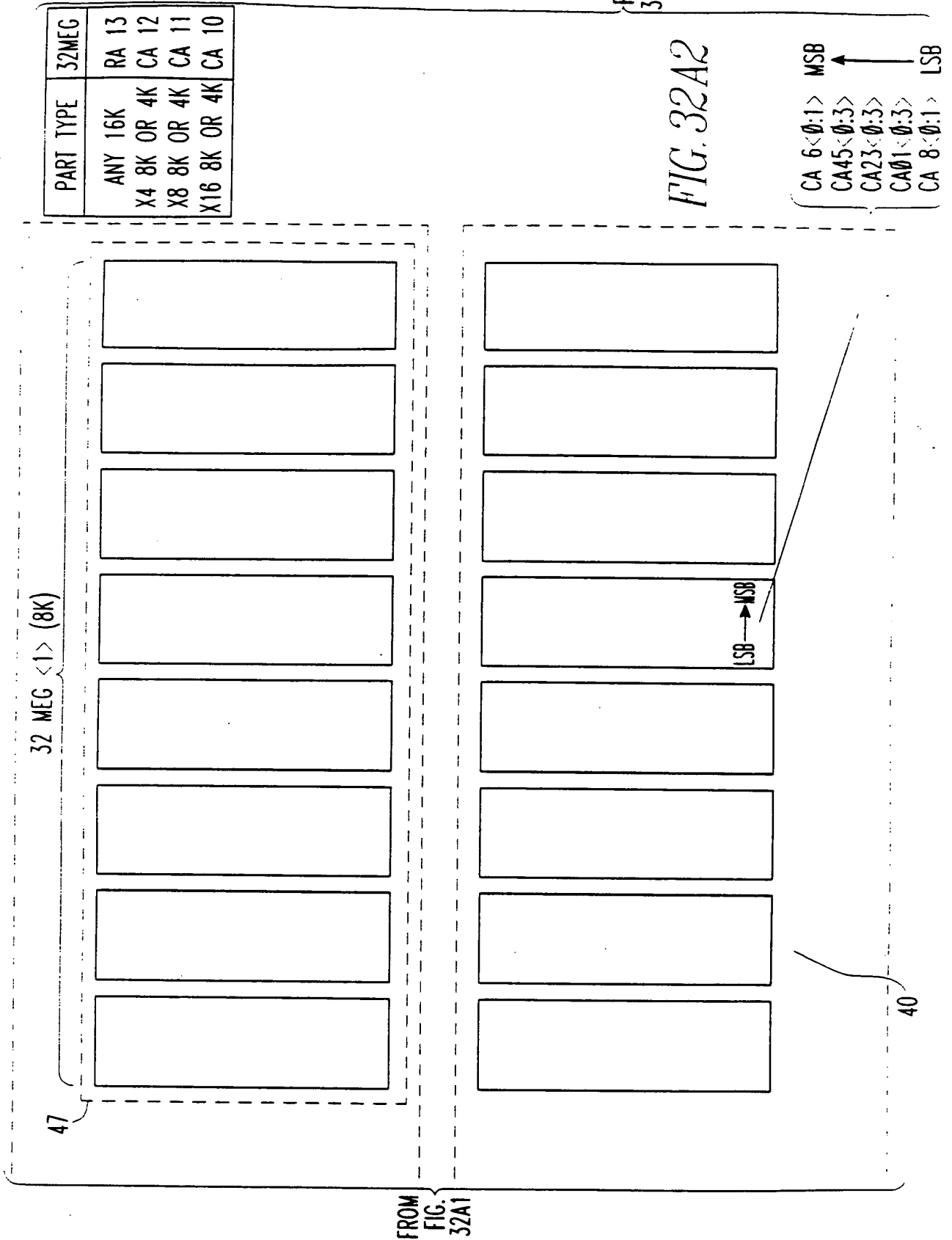
N/C	N/C	N/C	N/C	VSSQ	N/C	VSSQ	VBB
N/C	DQ6	N/C	N/C	VSSQ	N/C	VSSQ	VBB
VSSQ	DQ14	DQ13	DQ12	VSSQ	VSSQ	VSSQ	VBB
60	61	62	63	64	65	66	67
DQ9	DQ10	VCCQ	DQ11	VCCQ	VCCQ	VCCQ	VBB
DQ5	N/C	VCCQ	N/C	VCCQ	VCCQ	VCCQ	VBB
DQ3	N/C	VCCQ	N/C	VCCQ	VCCQ	VCCQ	VBB

FIG. 31C2



100/367
FIG. 32A2

FIG. 32A1



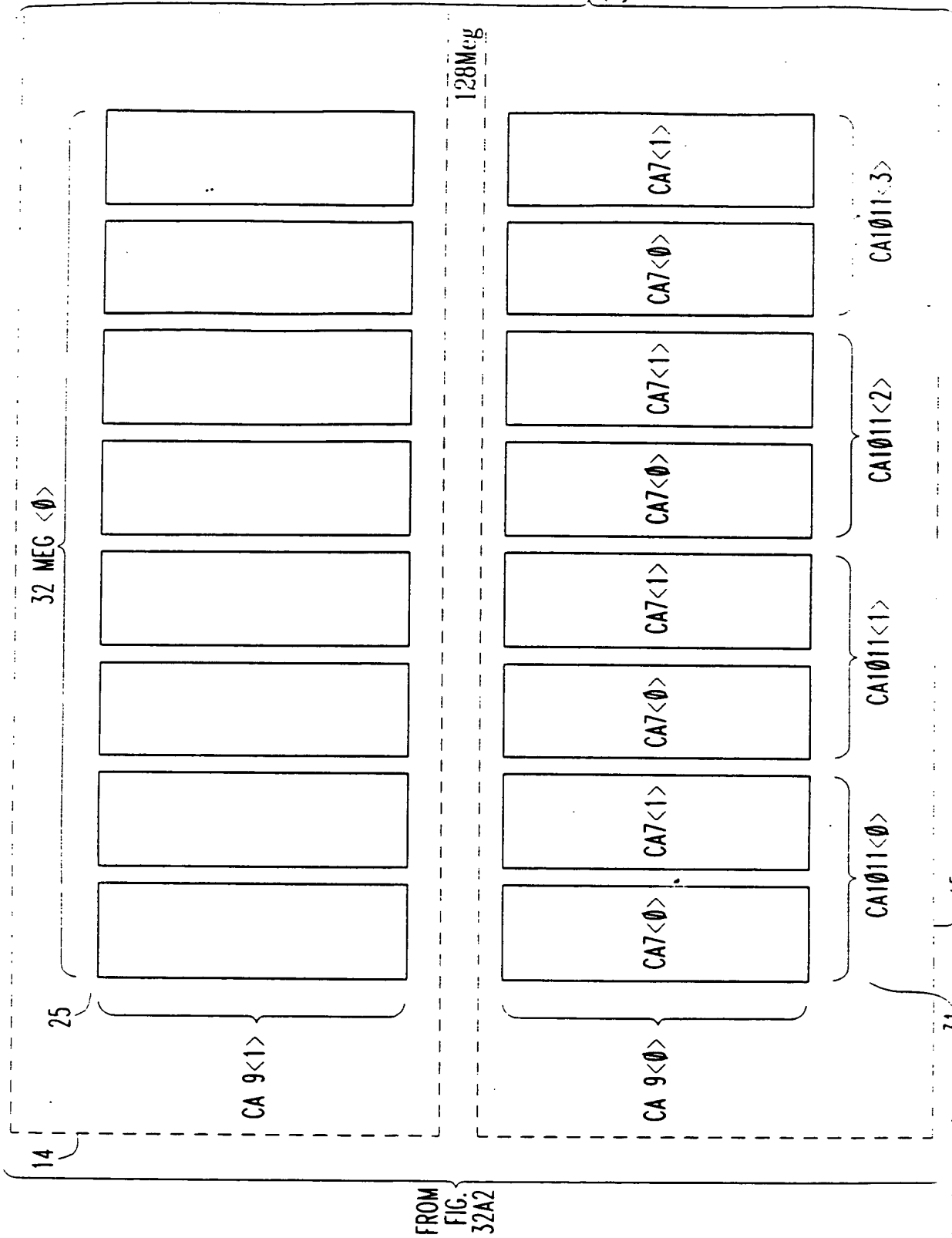
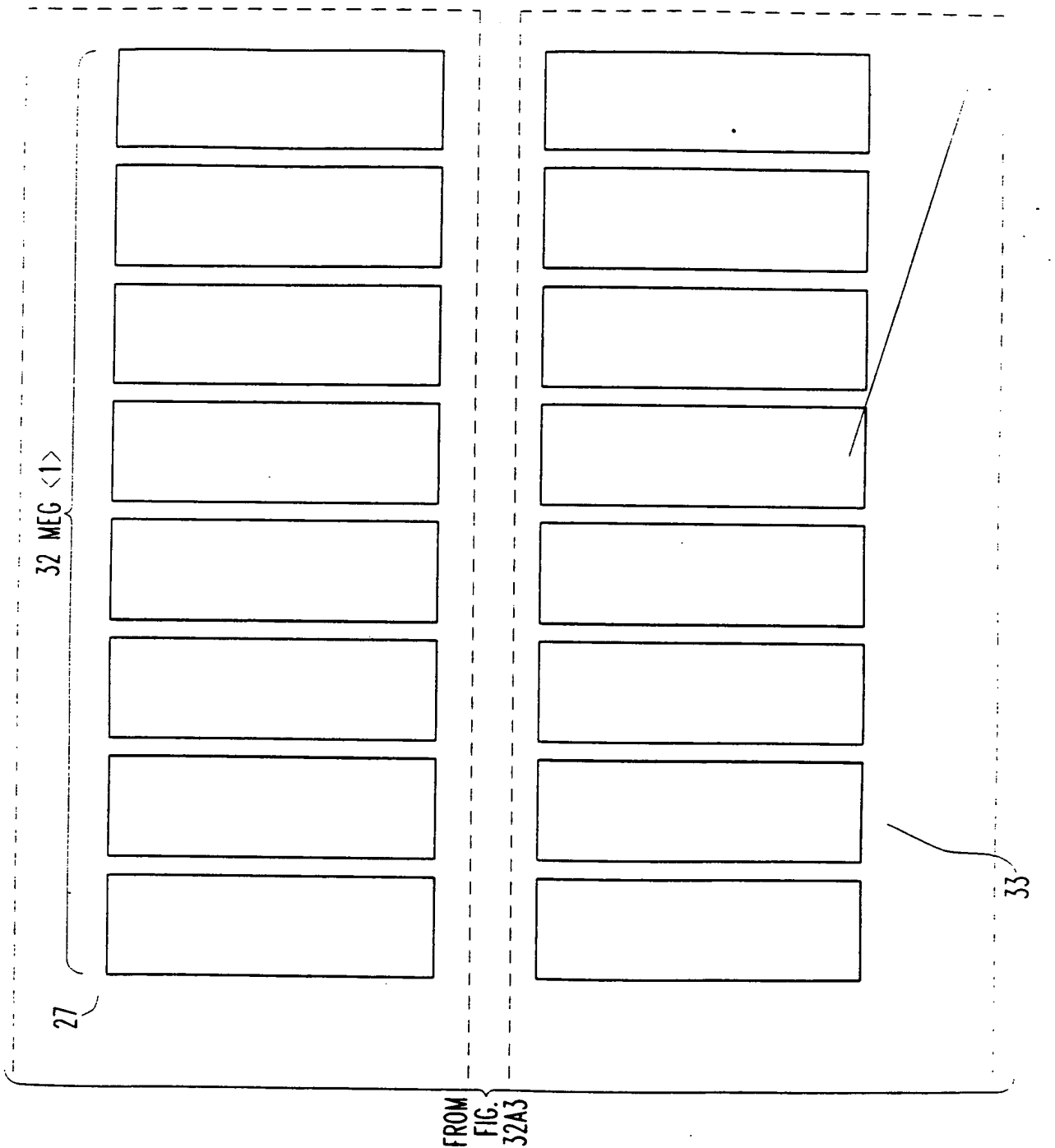


FIG. 32A3



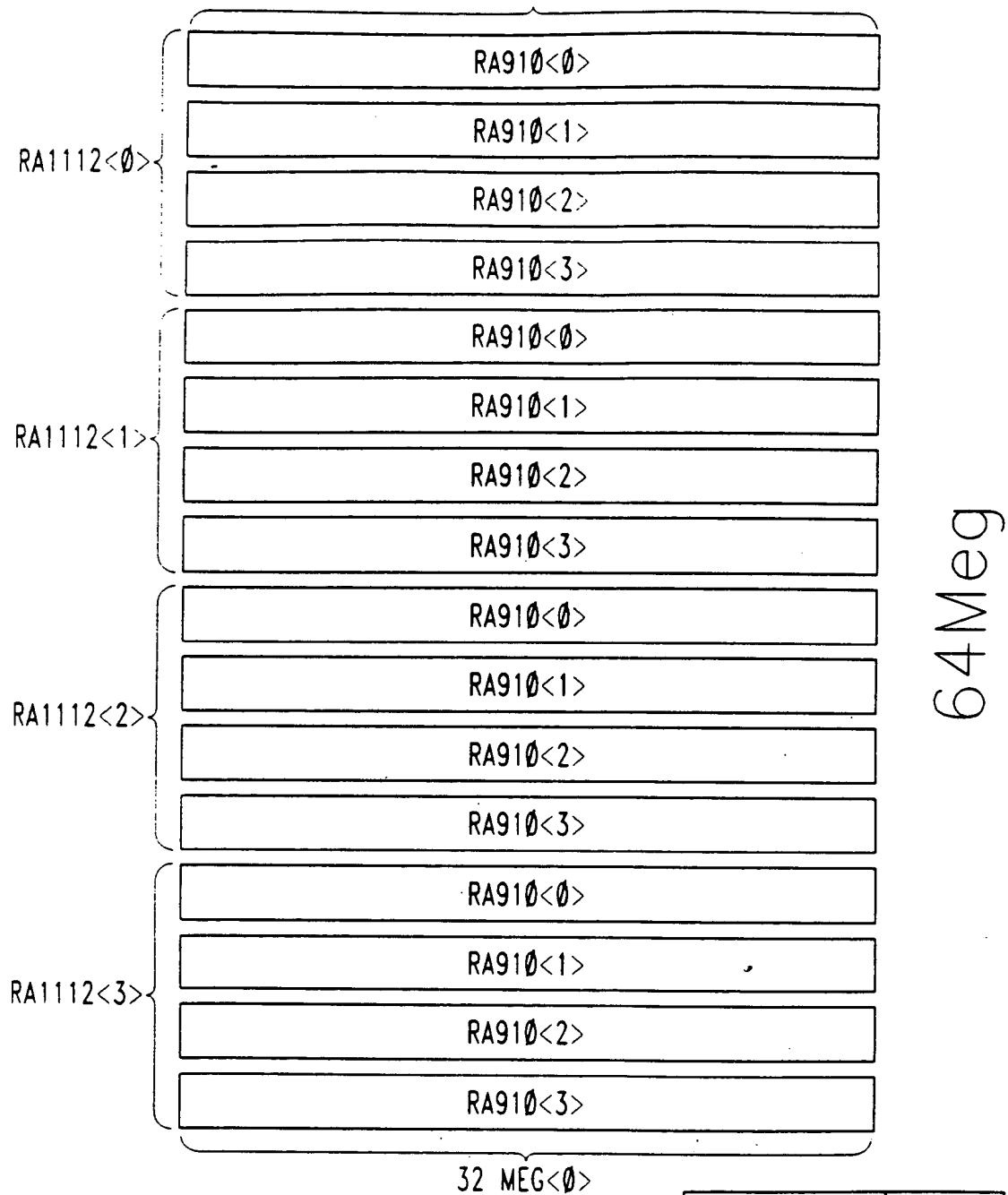


FIG. 32B1

PART TYPE	32MEG
ANY 16K	RA_13
X4 8K OR 4K	CA_12
X8 8K OR 4K	CA_11
X16 8K OR 4K	CA_10

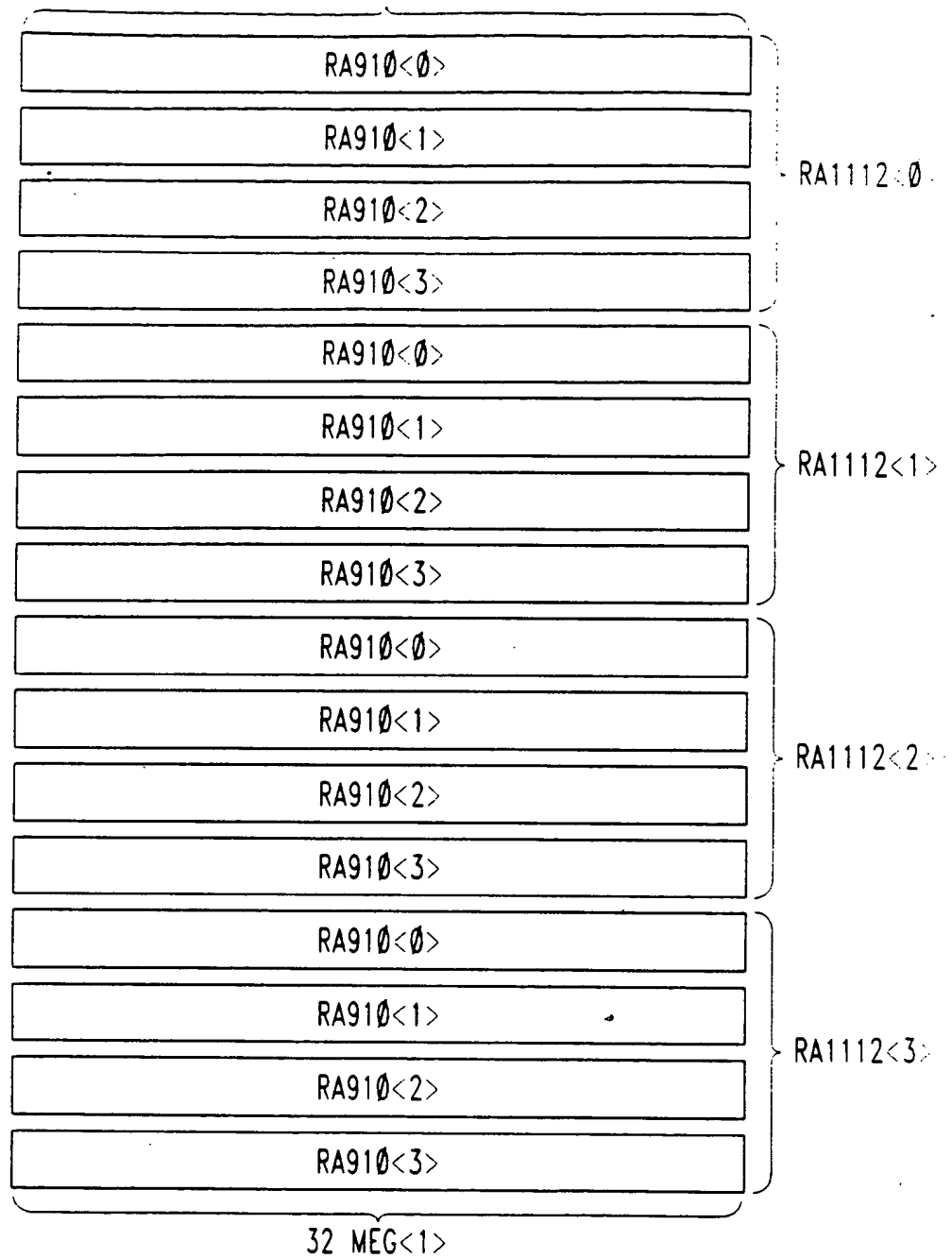
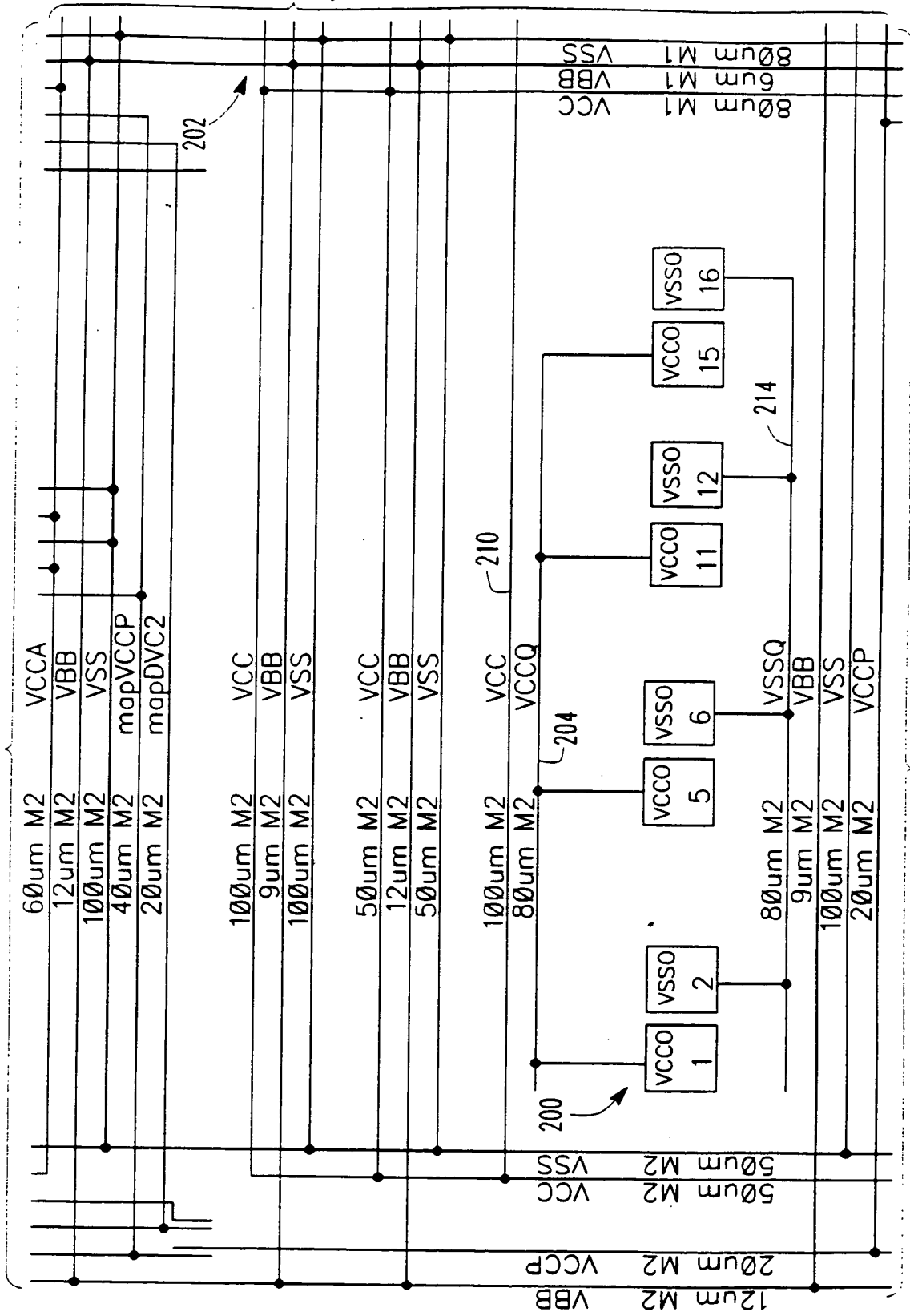


FIG. 32B2



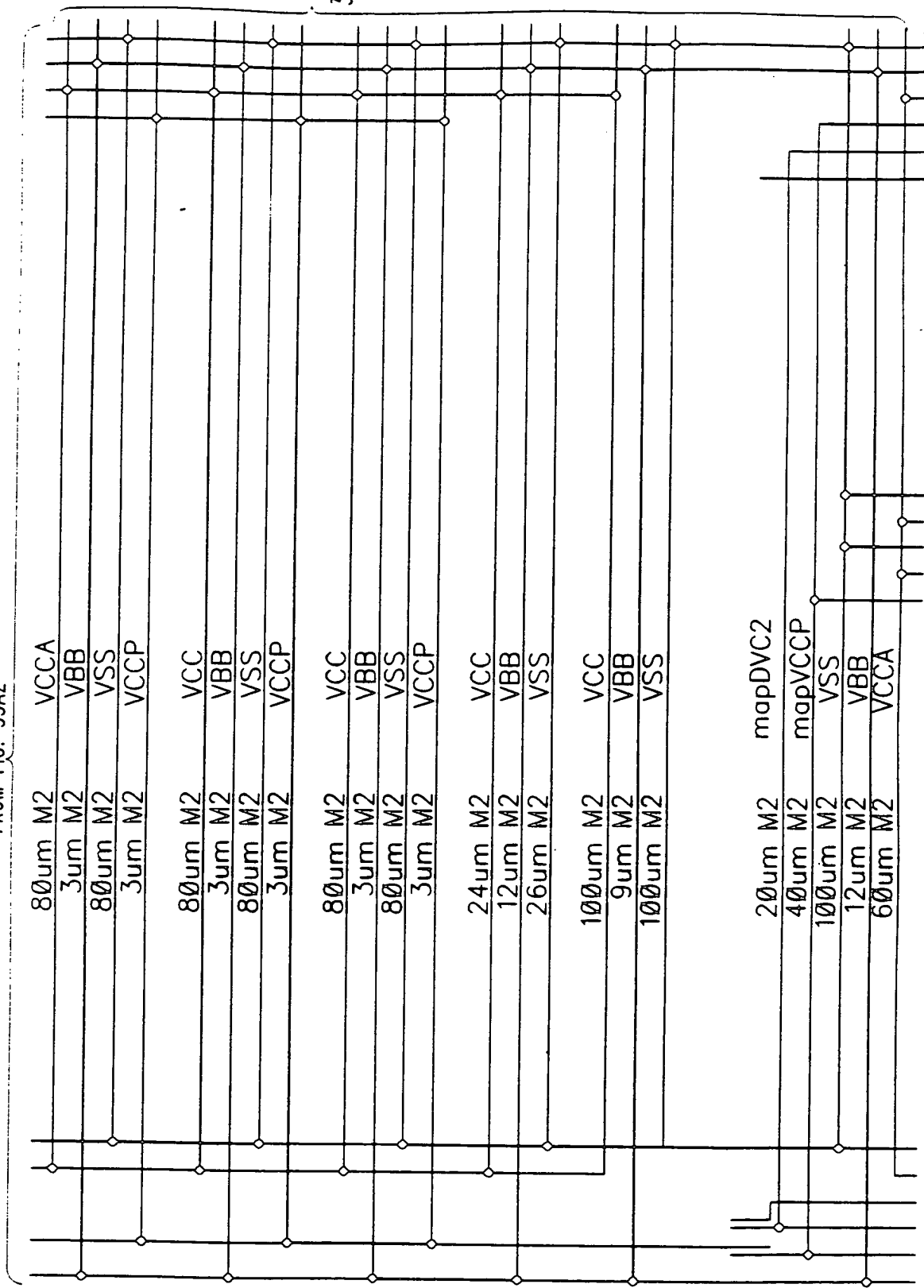
1763311

FROM FIG. 33A1



TO FIG. 33A3

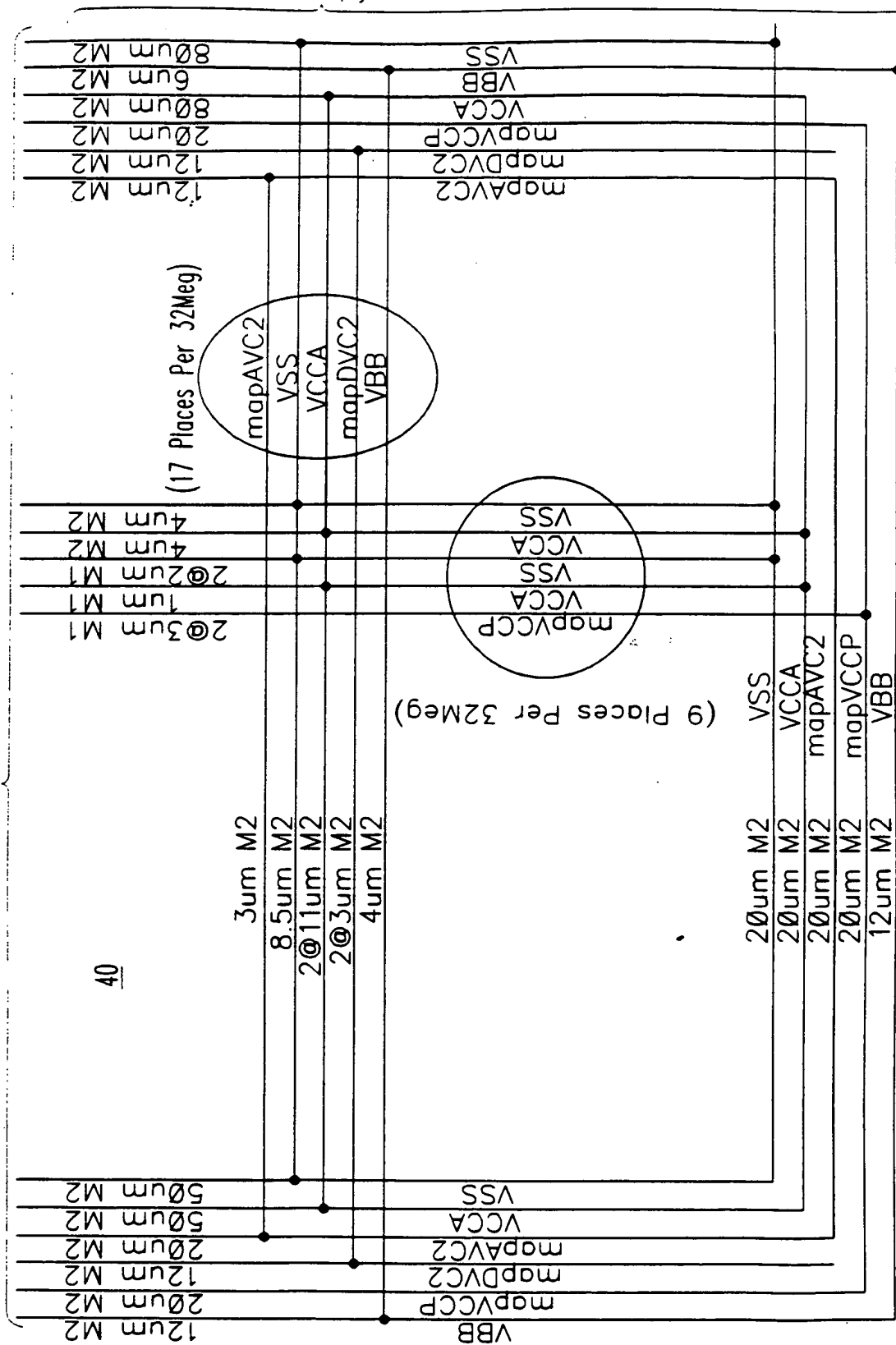
FROM FIG. 33A2



TO FIG. 33A4

1997

FROM FIG. 33A3



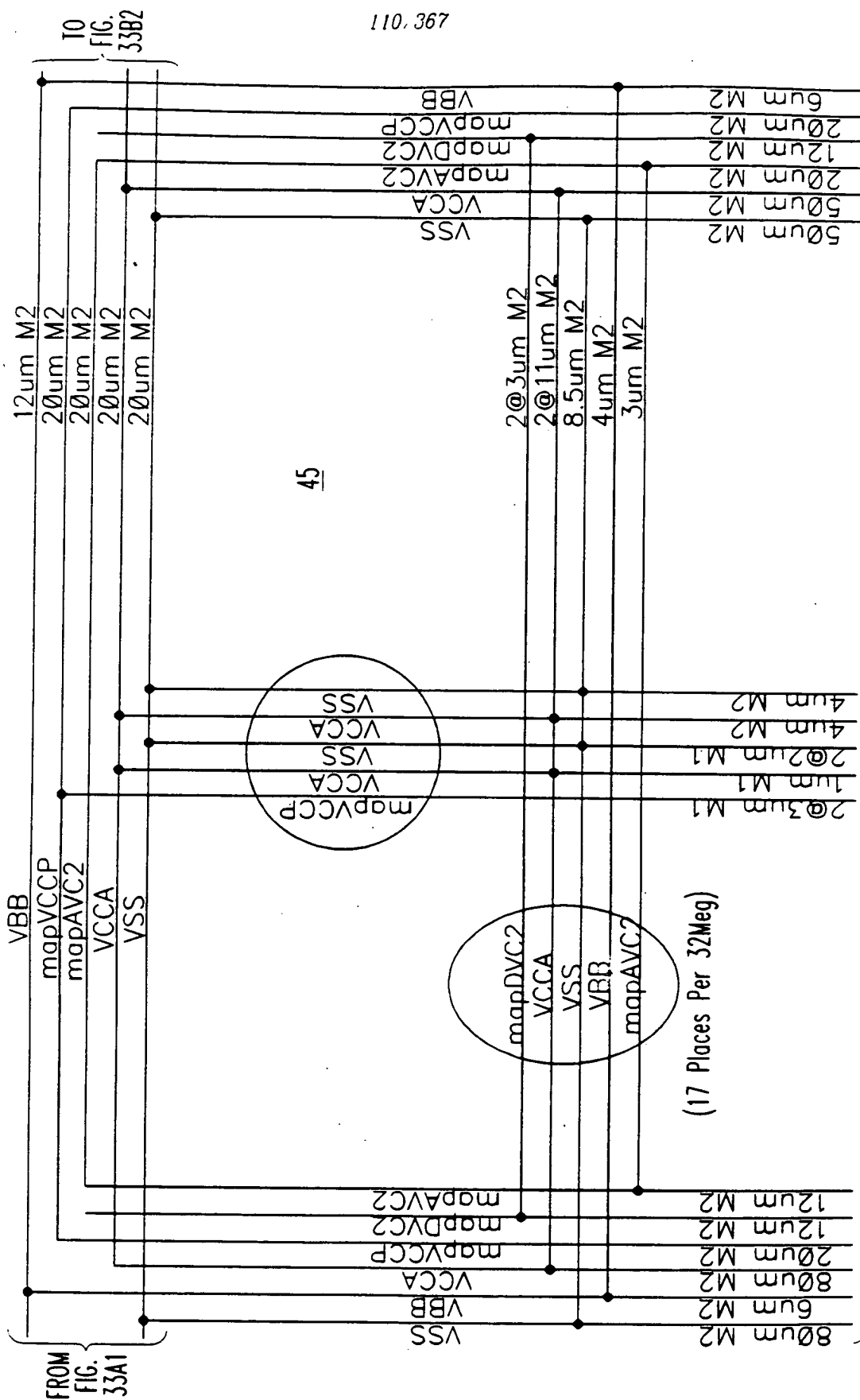
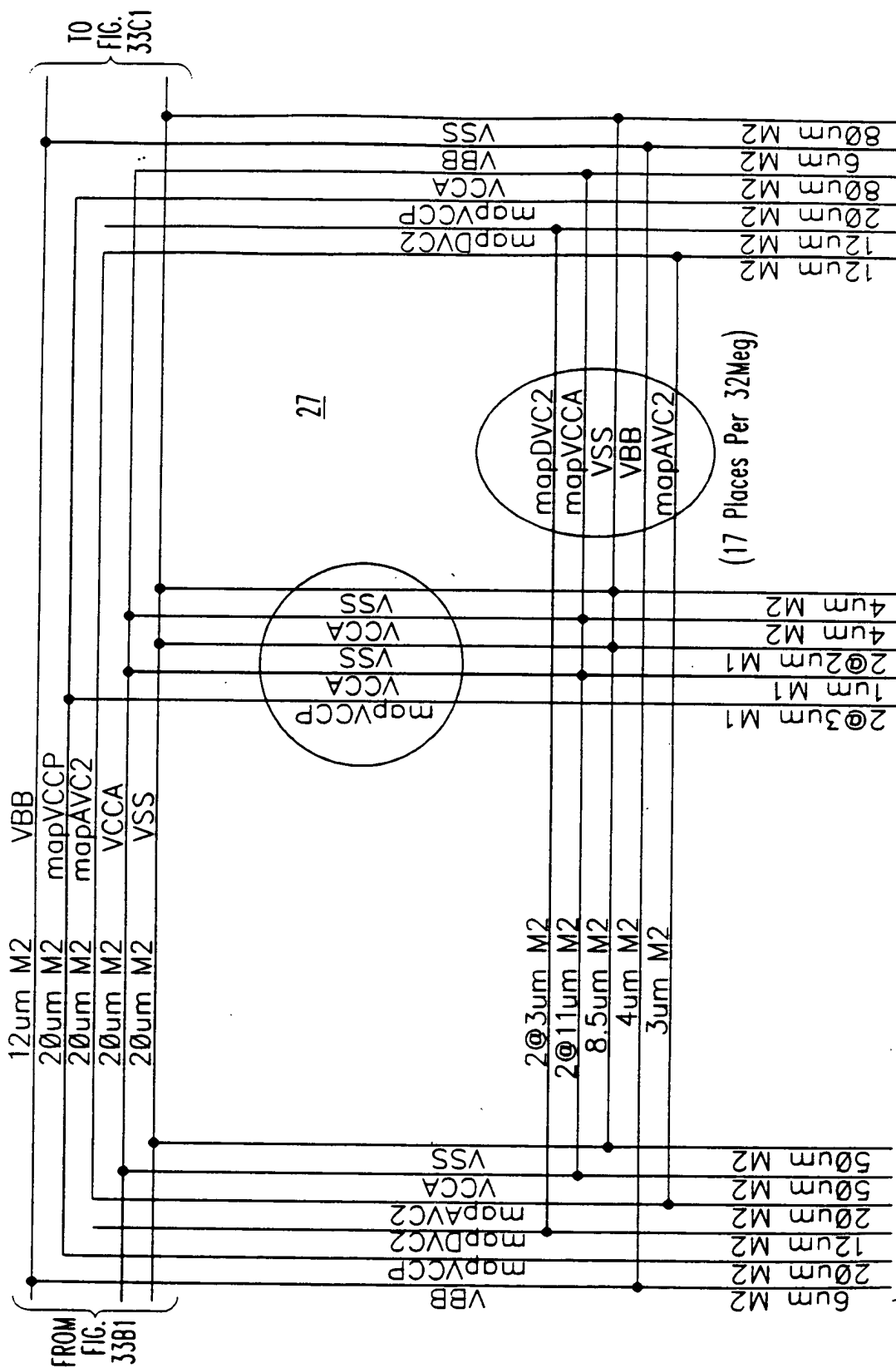


FIG. 33B3

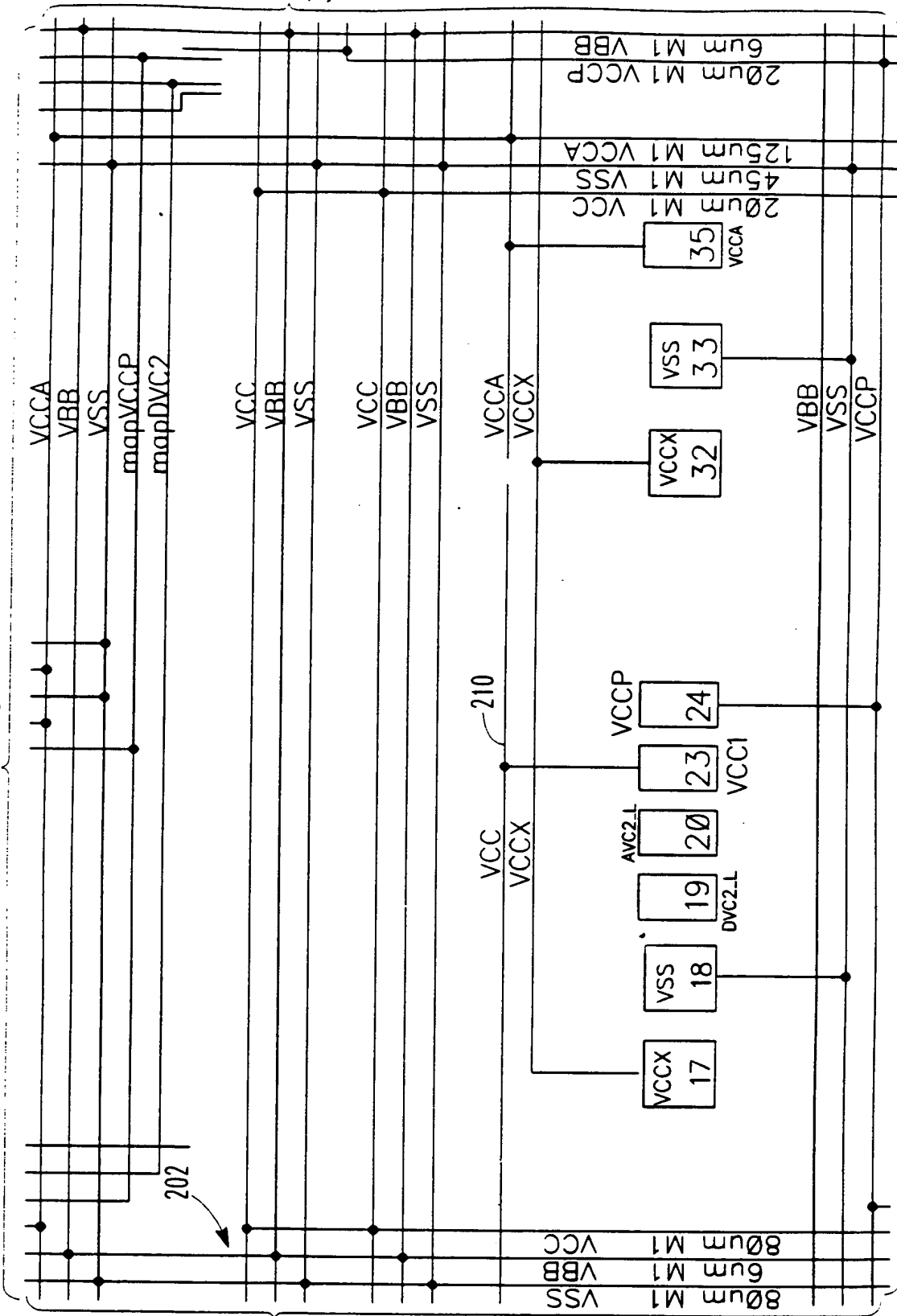
FIG. 33B1



TO FIG. 3384

FIG. 33B2

FROM FIG. 33B1



FROM
FIG.
33A2

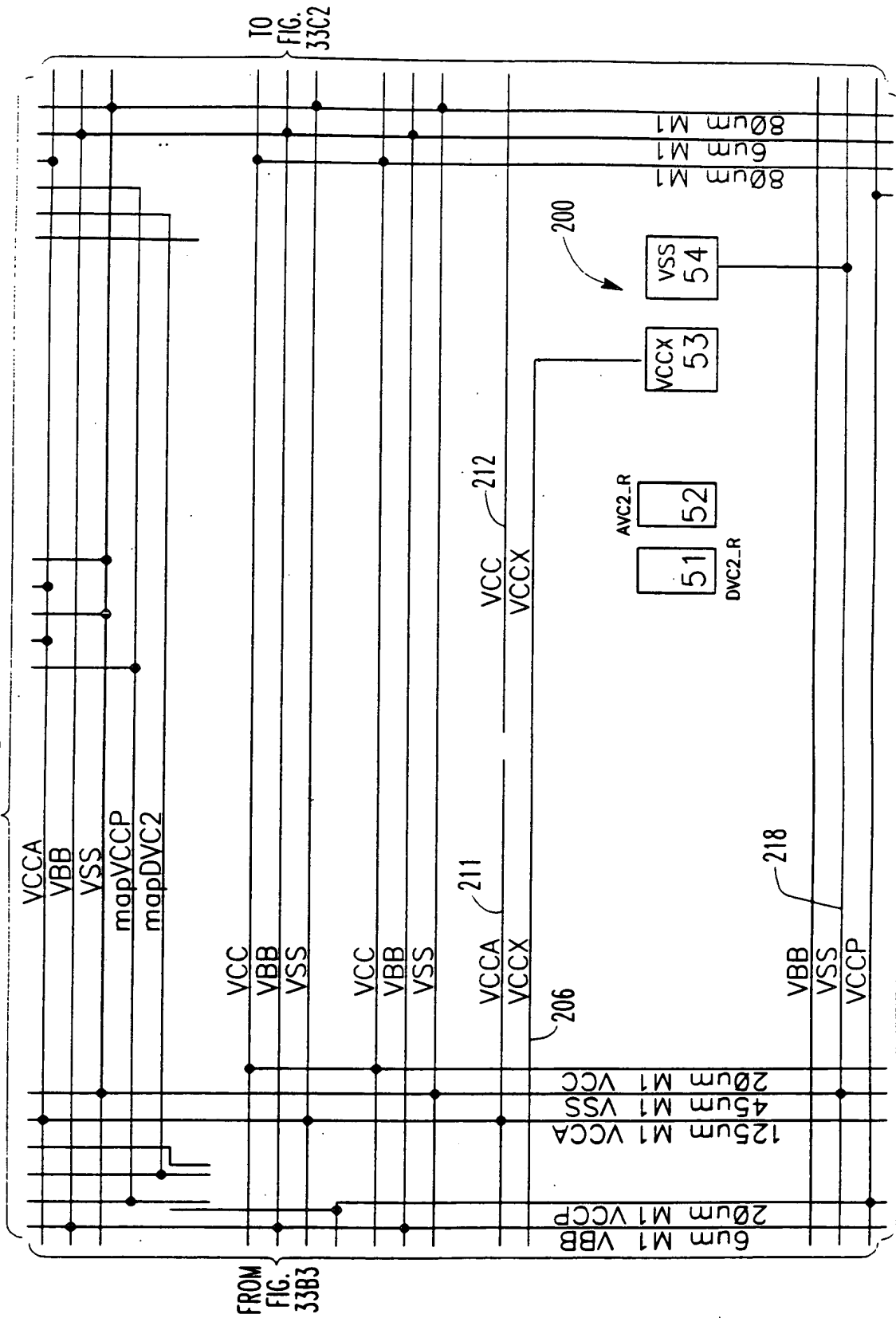
TO
FIG.
33B4

112/367

TO FIG. 33B3

FIG. 33B3

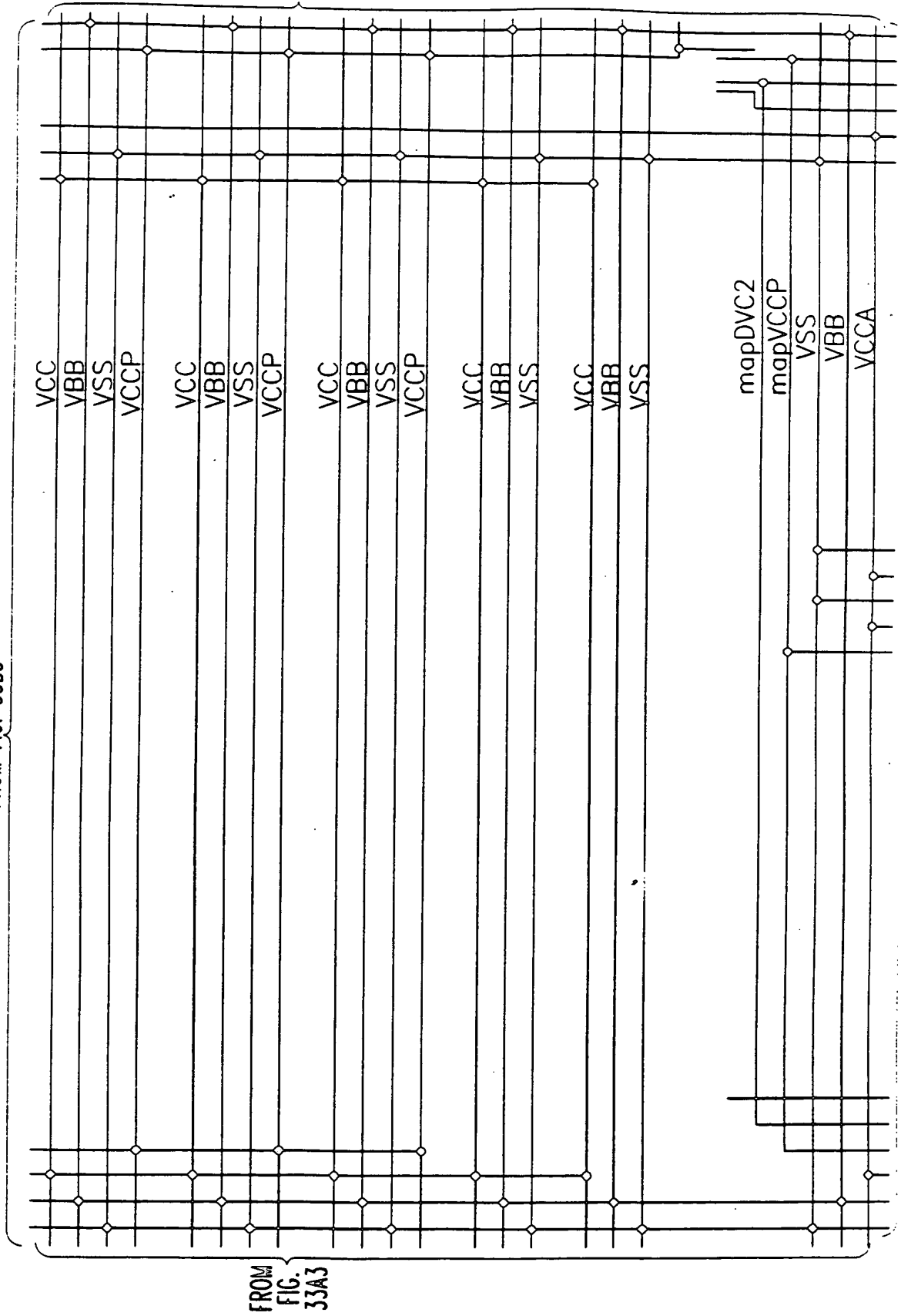
FROM FIG. 33B2



TO FIG. 33B6

FIG. 33B1

FROM FIG. 33B3

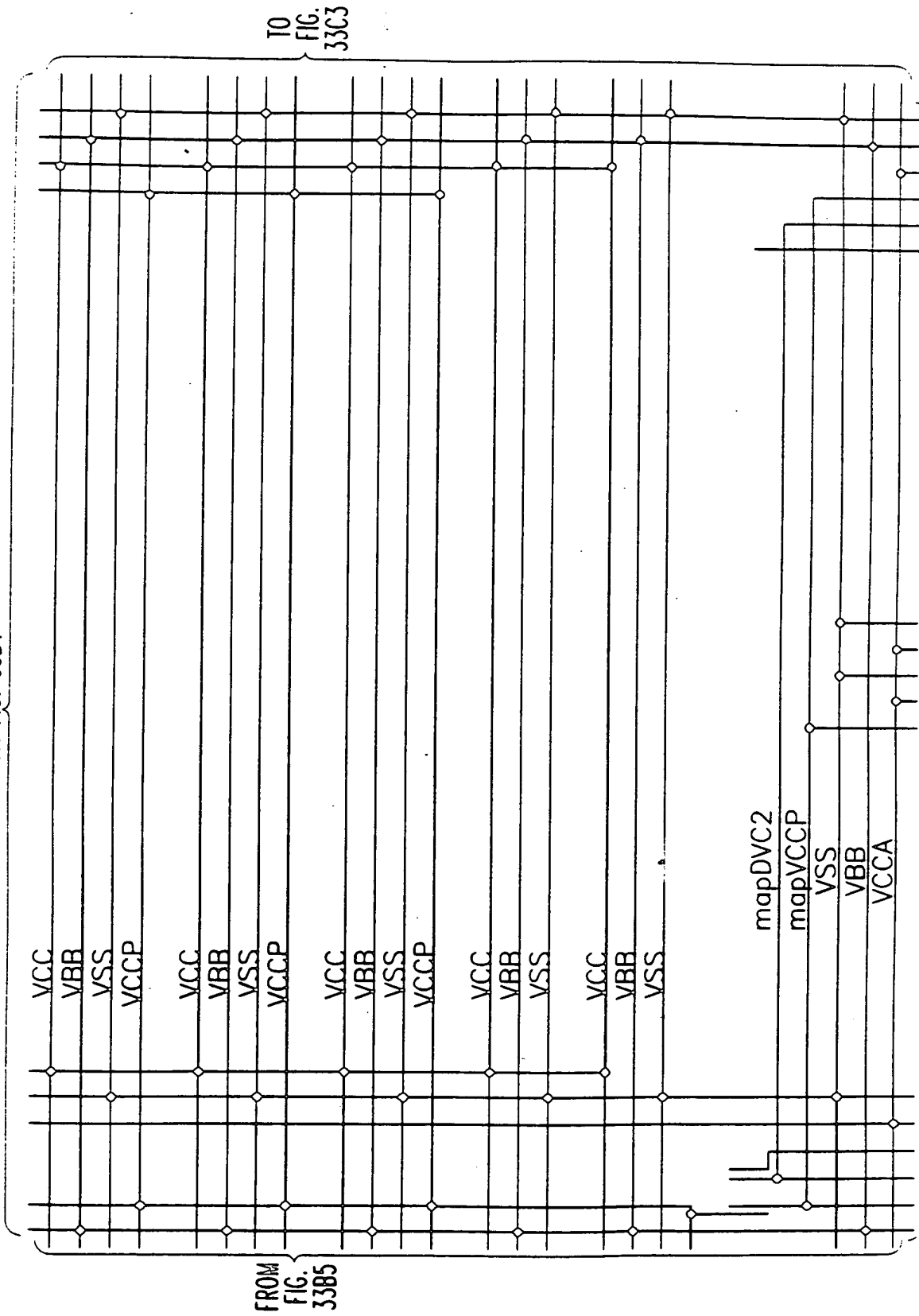


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TO FIG. 33B7

FIG. 33B5

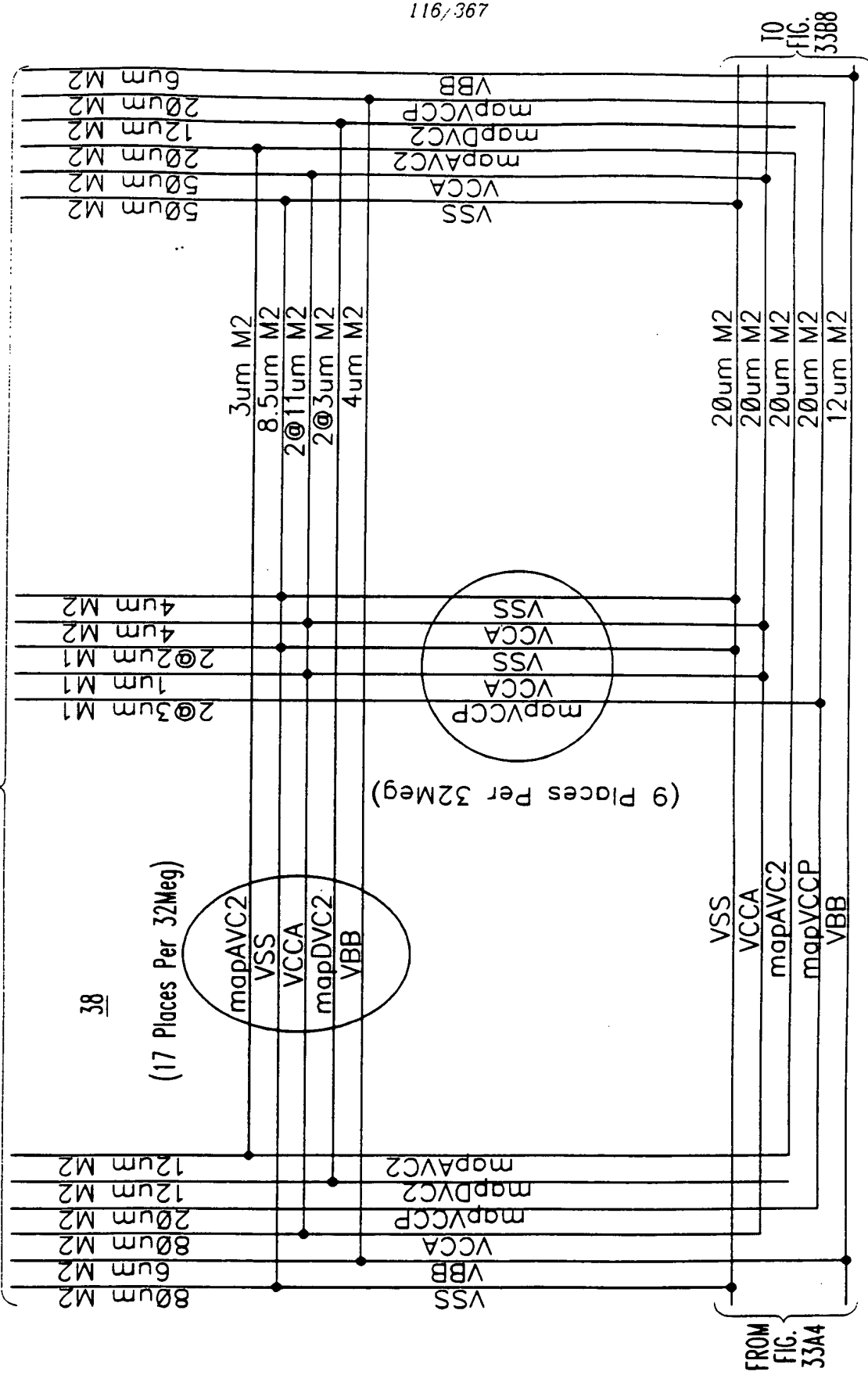
FROM FIG. 33B4



TO FIG. 33B8

FIG. 33B6

FROM FIG. 33B5



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FIG. 33B7

FROM FIG. 33B6

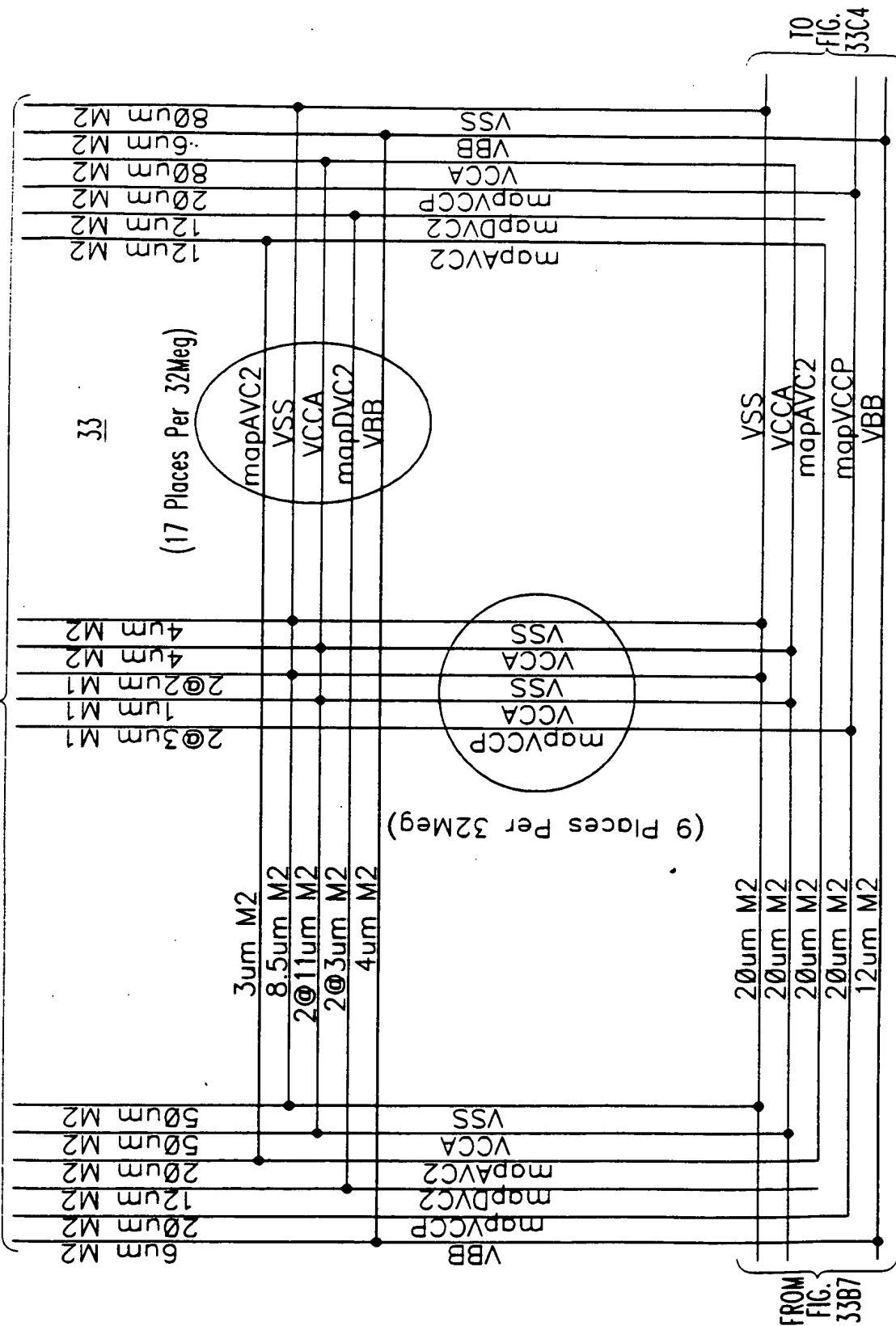
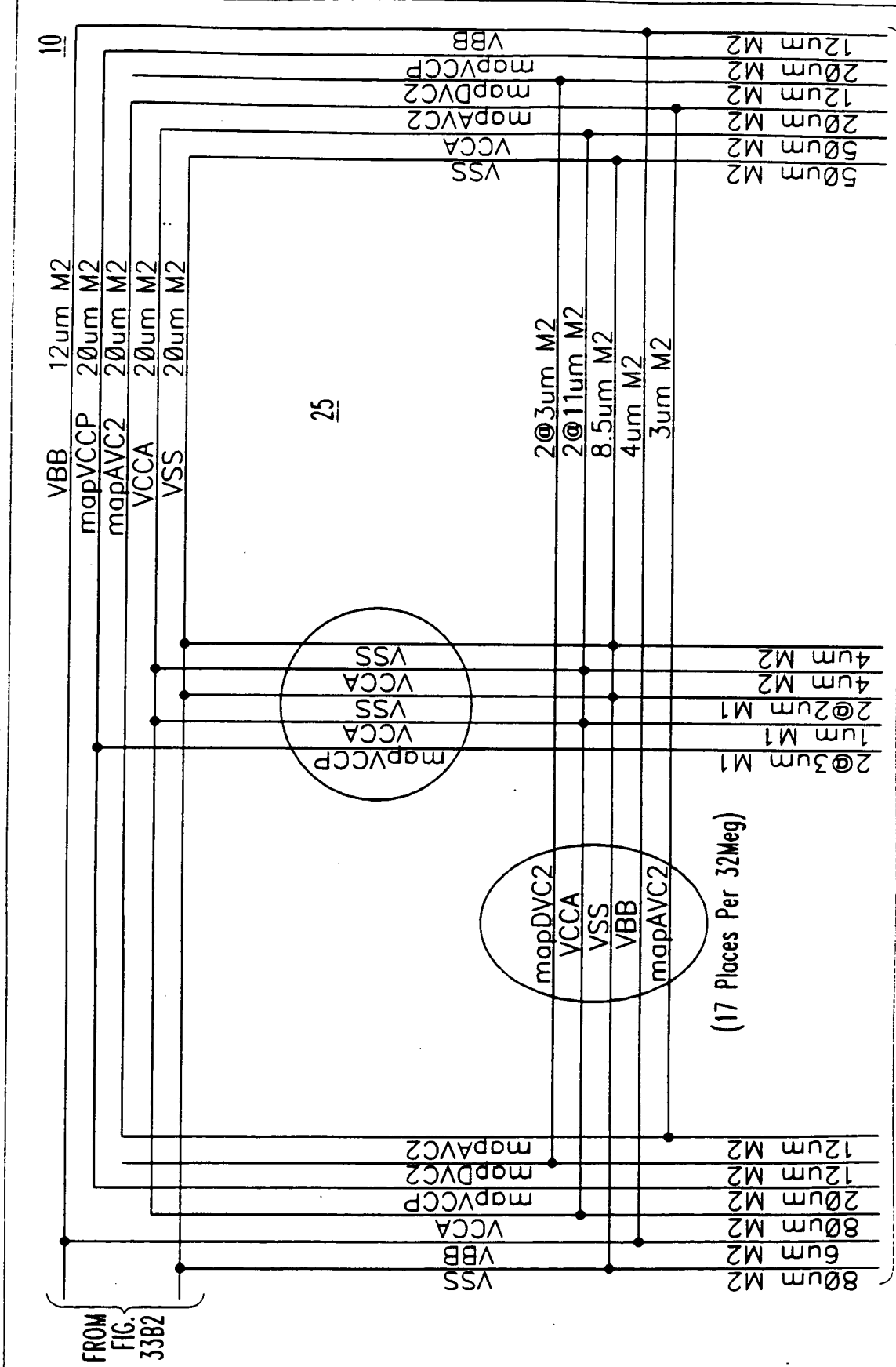


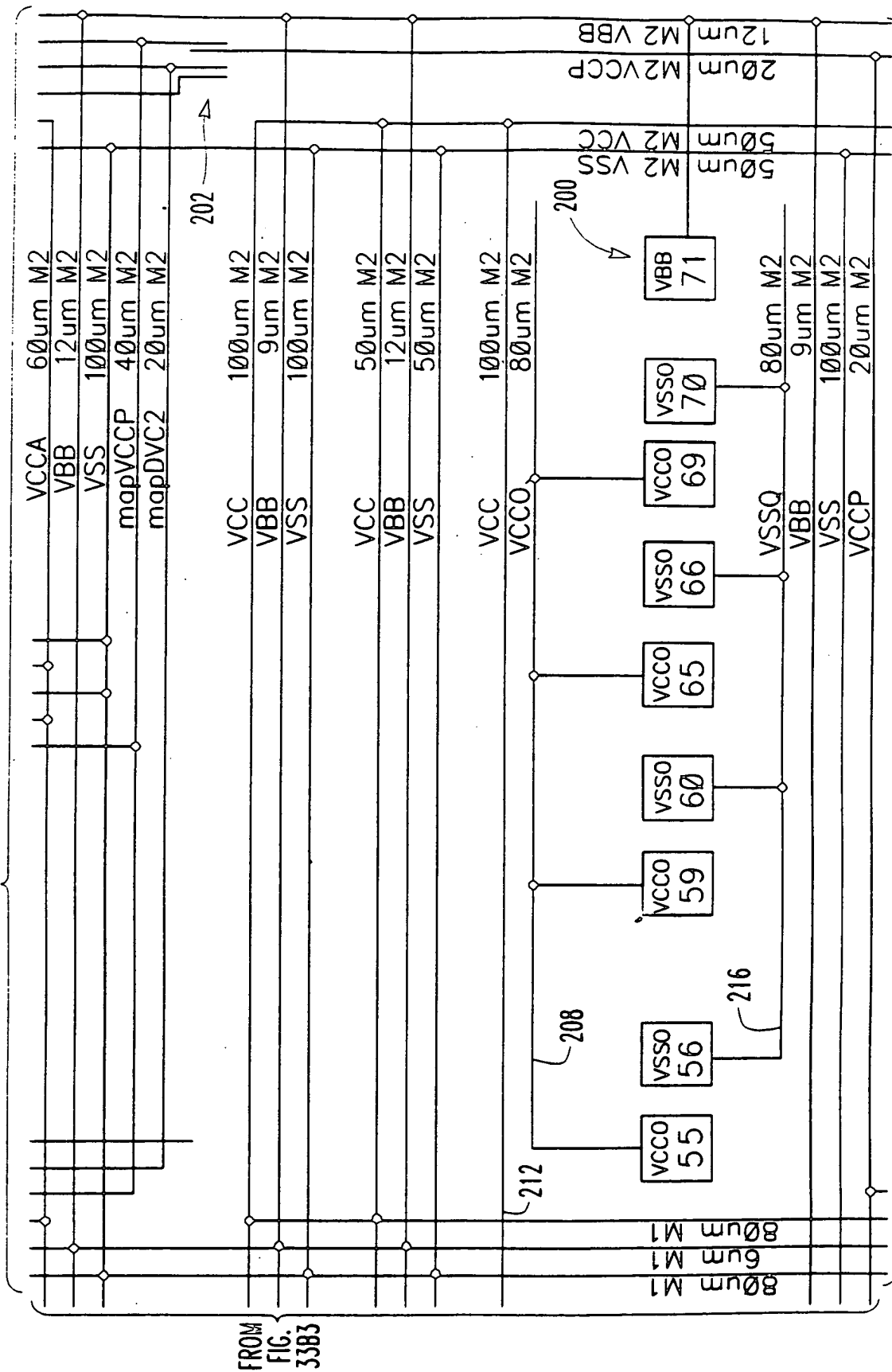
FIG. 33B8



TO FIG. 33C2

FIG. 33C1

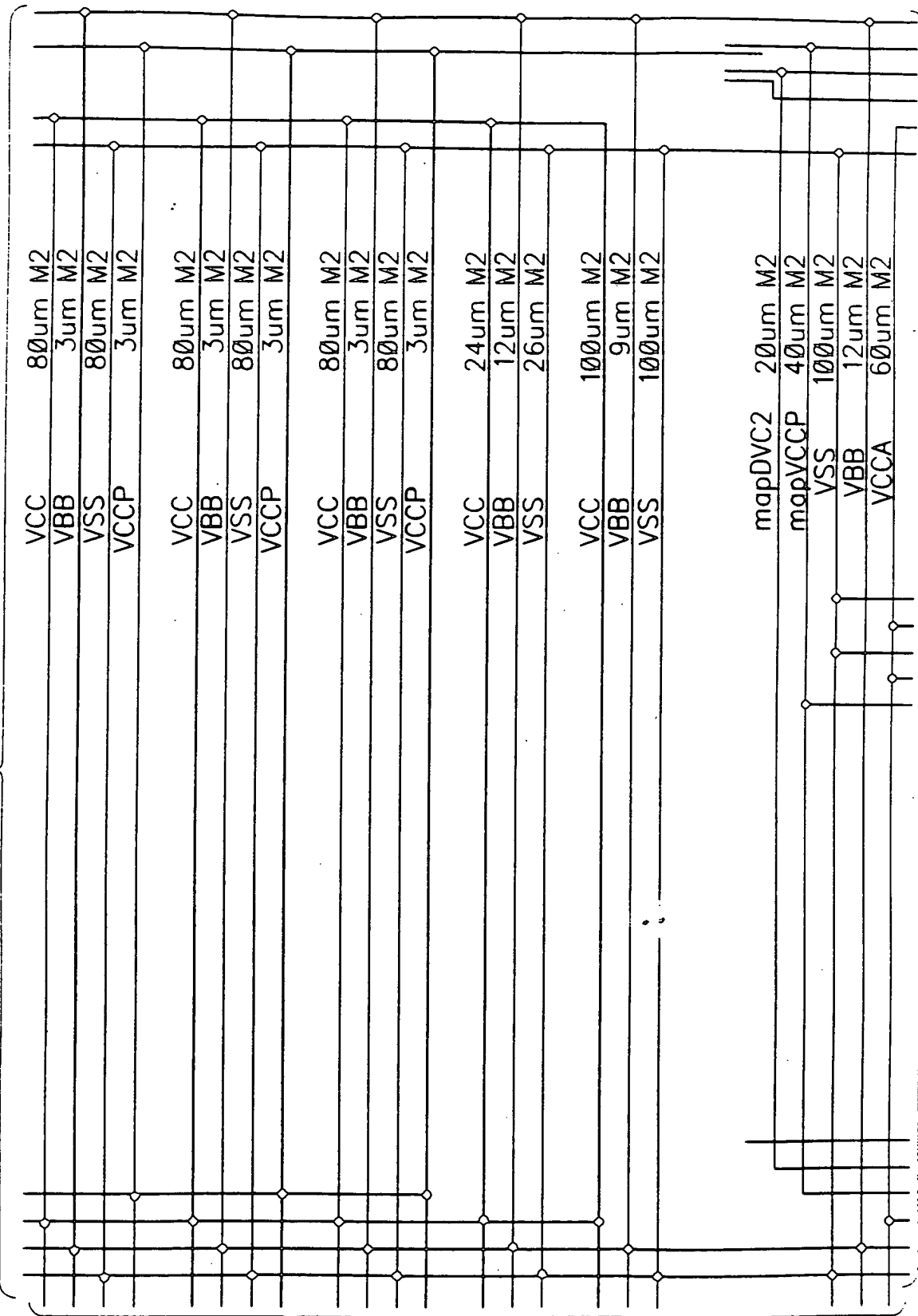
FROM FIG. 33C1



TO FIG. 33C3

FIG. 33C2

FROM FIG. 33C2



FROM
FIG.
33B6

TO FIG. 33C4

FIG. 33C3

THE

10

45

200

DVC2
GENERATOR

500

□ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □

VCCP
PUMP
CIRCUITS

402

DVC2
GENERATOR

501

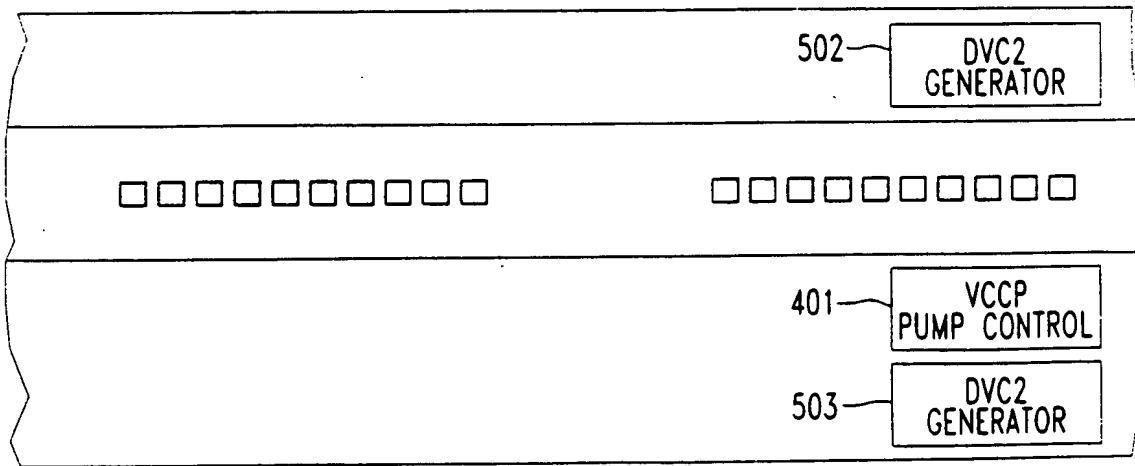
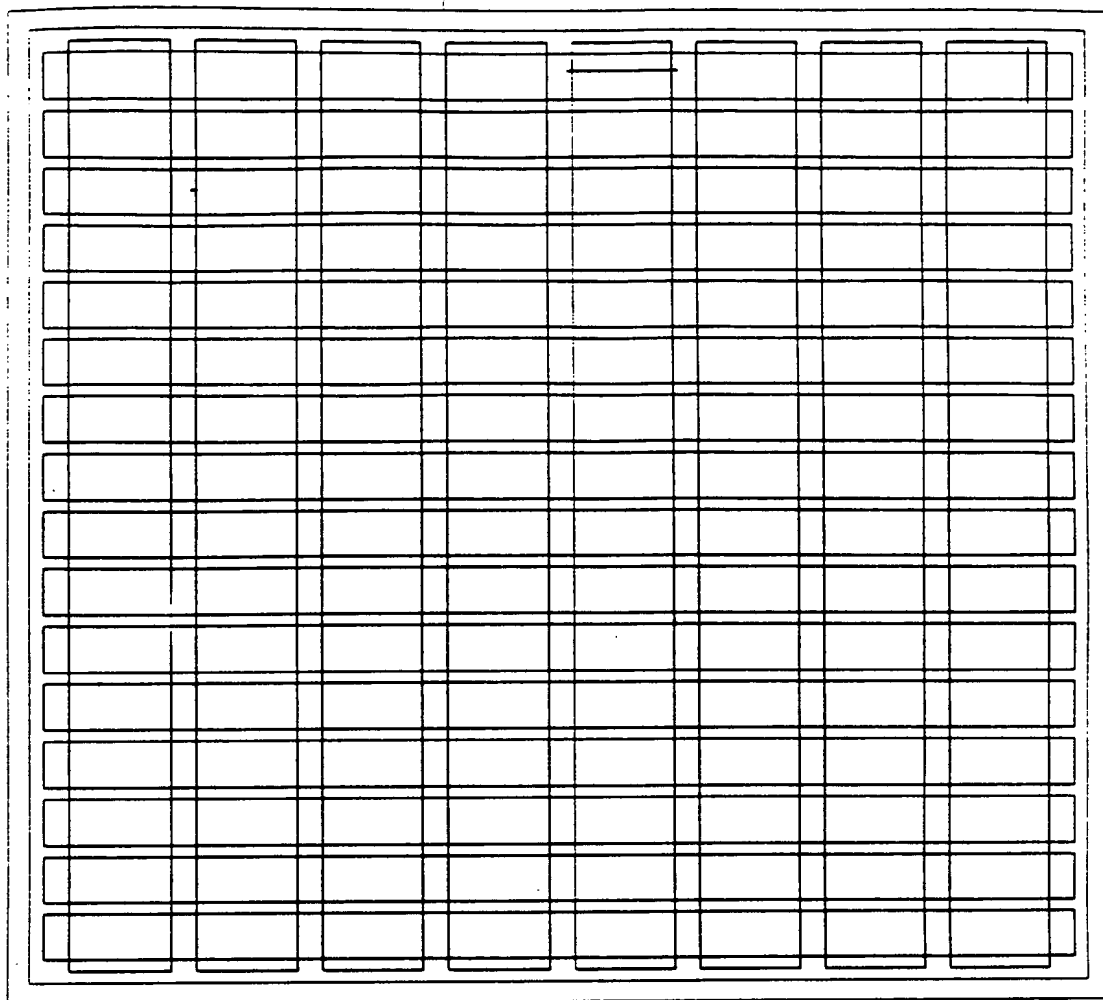
TO
FIG.
33D3

TO FIG. 33D2

FIG. 33D1

TO FIG. 33D1

47

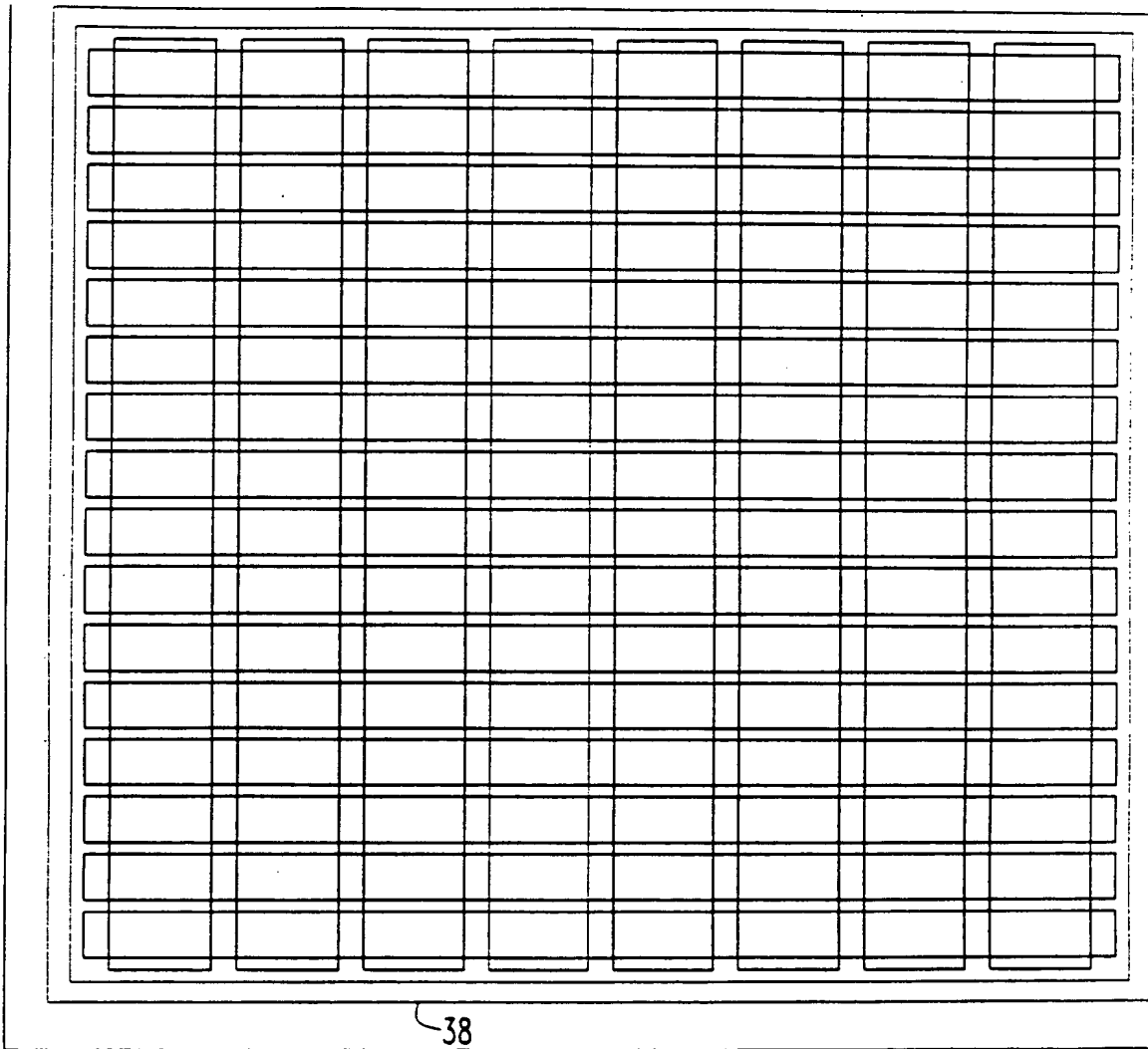


TO
FIG.
▼ 33D4

FIG. 33D2

TO
FIG.
33D1

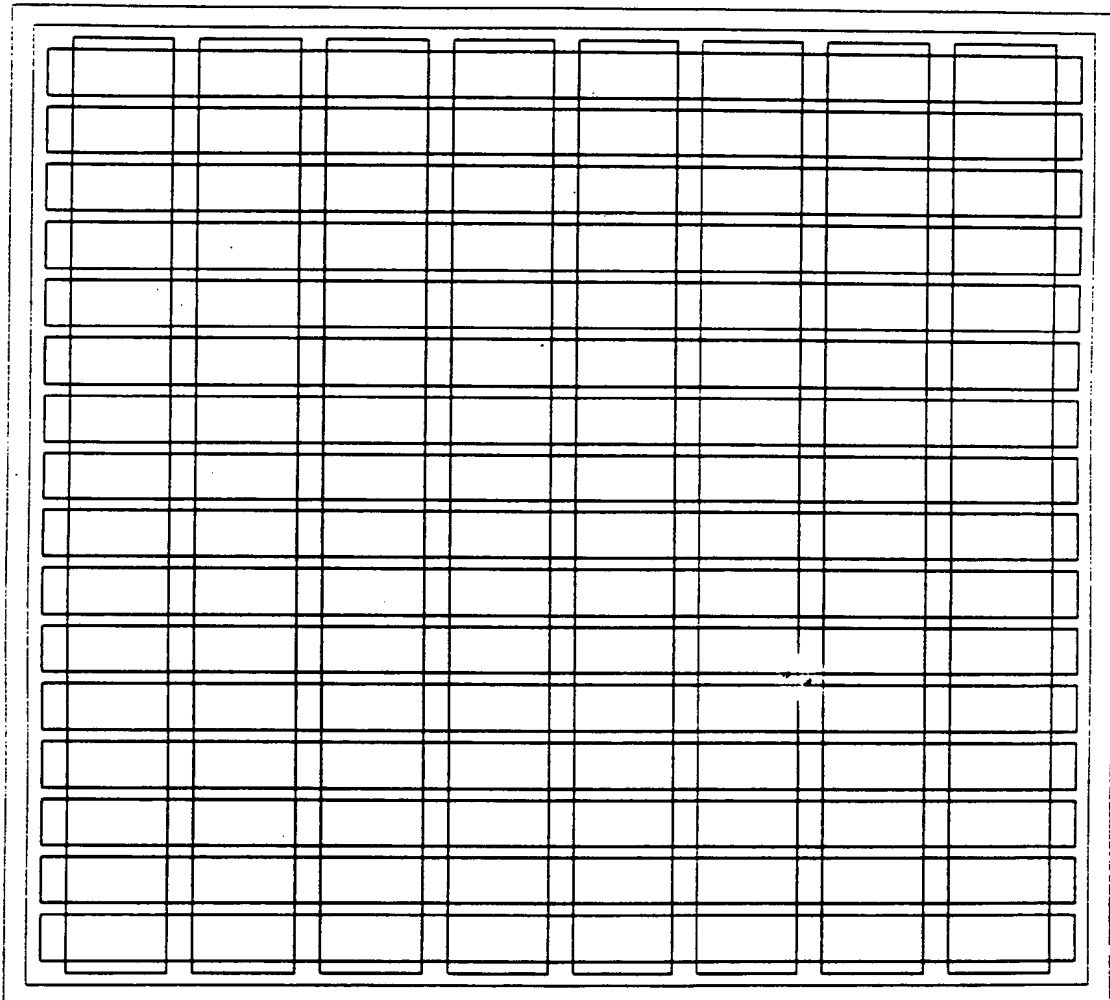
(SEE FIG. 33E1)



TO FIG. 33D4

FIG. 33D3

TO
FIG.
33D2



40

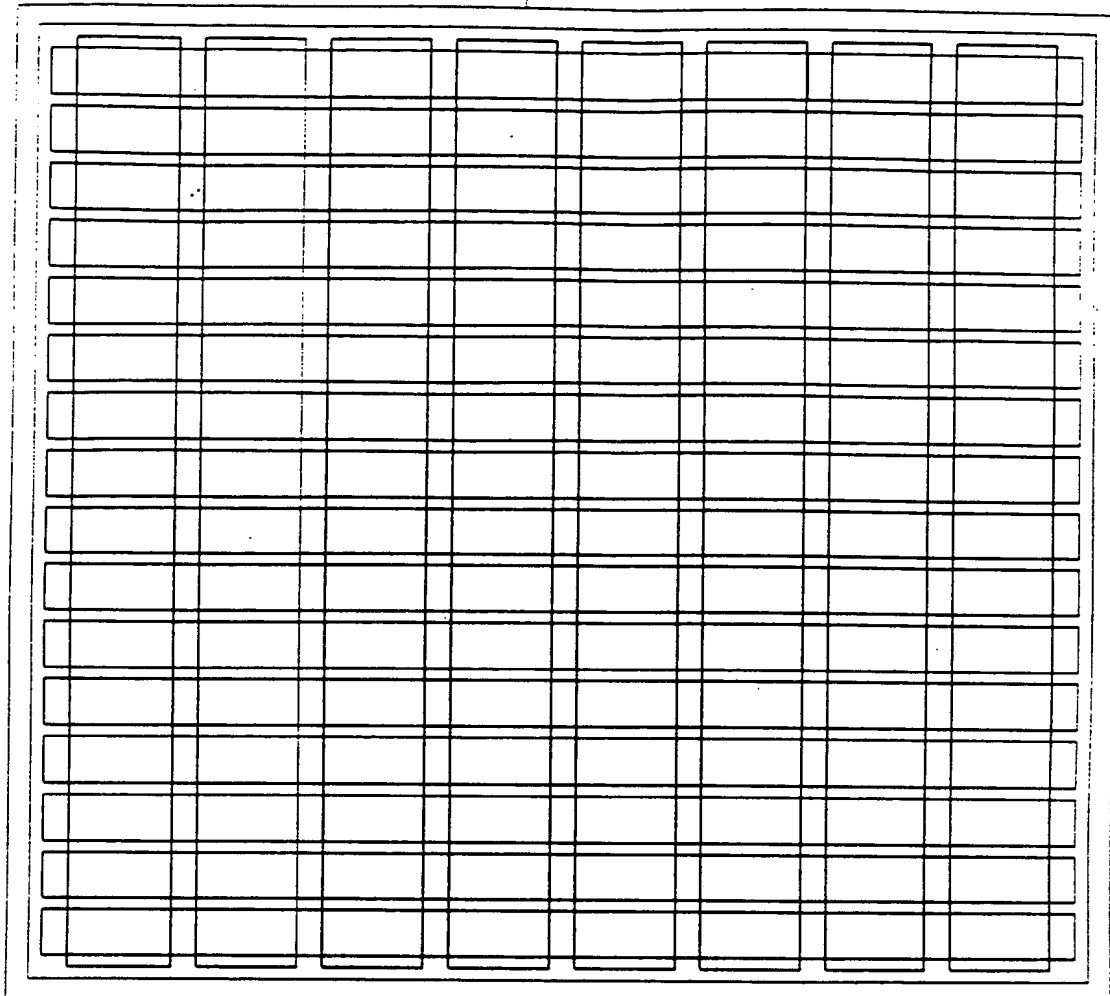
TO FIG. 33D3

FIG. 33D4

(SEE FIG. 33D2)

25

TO FIG. 33E2



DVC2
GENERATOR 504

□ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □

VCCP
REGULATOR 220

DVC2
GENERATOR 505

TO
FIG.
33E3

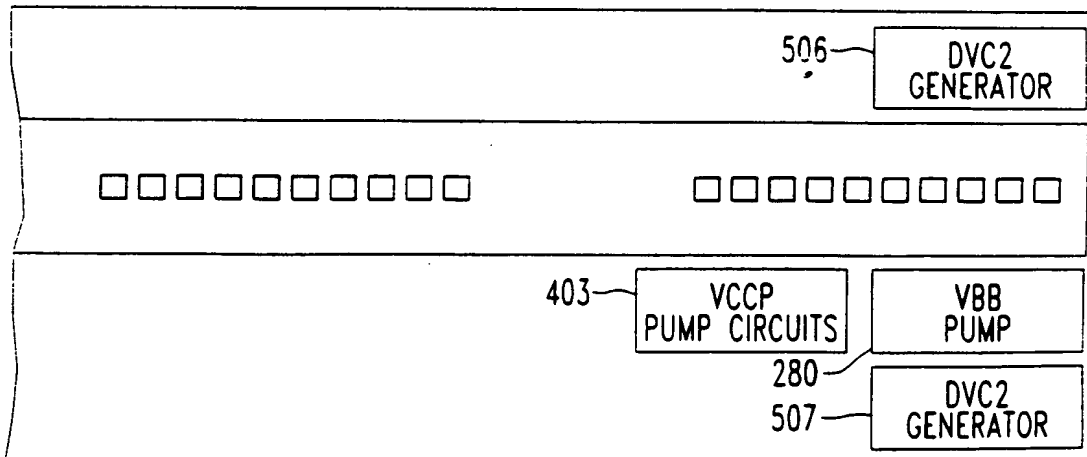
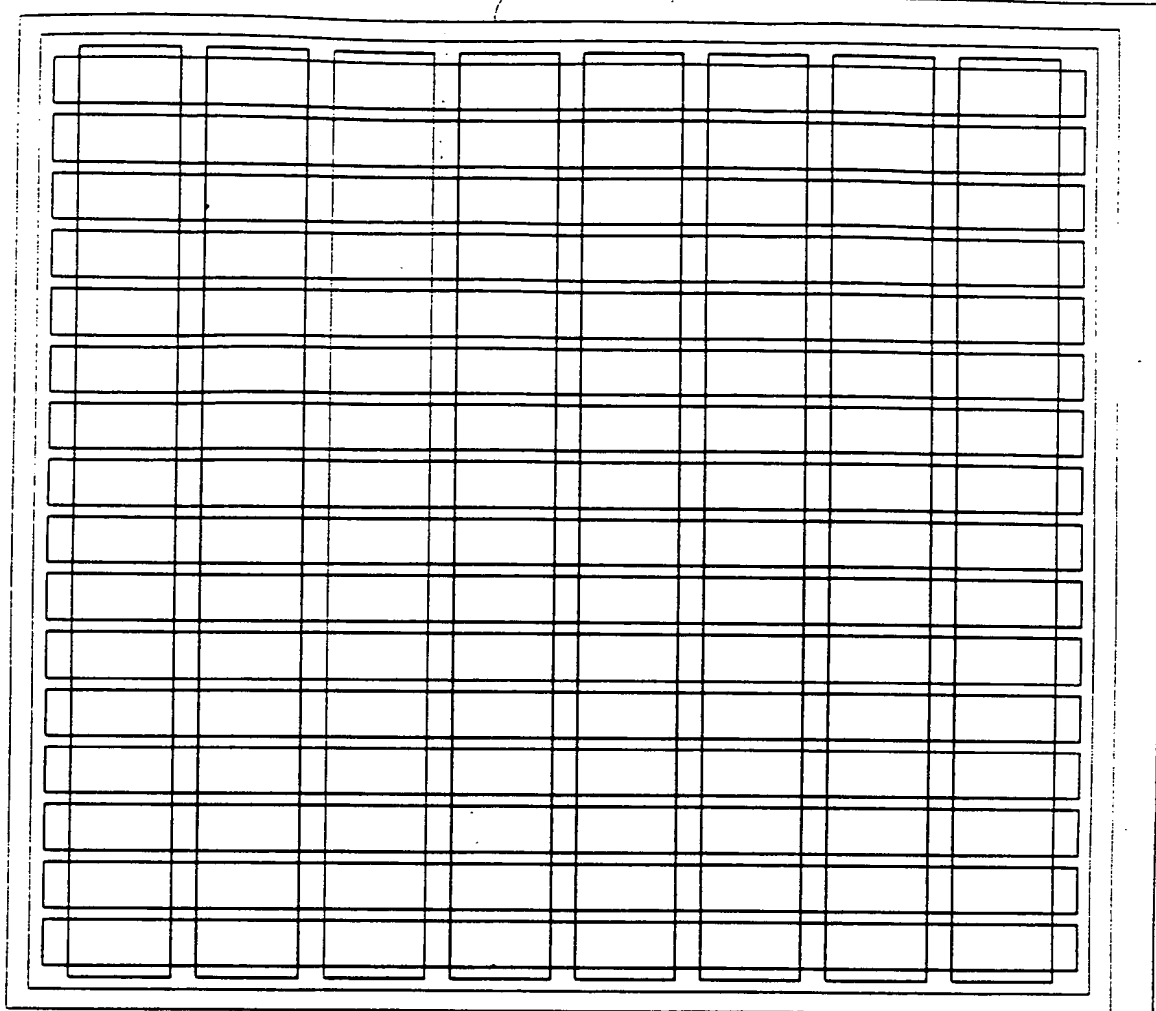
FIG. 33E1

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TO FIG. 33E1

27

10

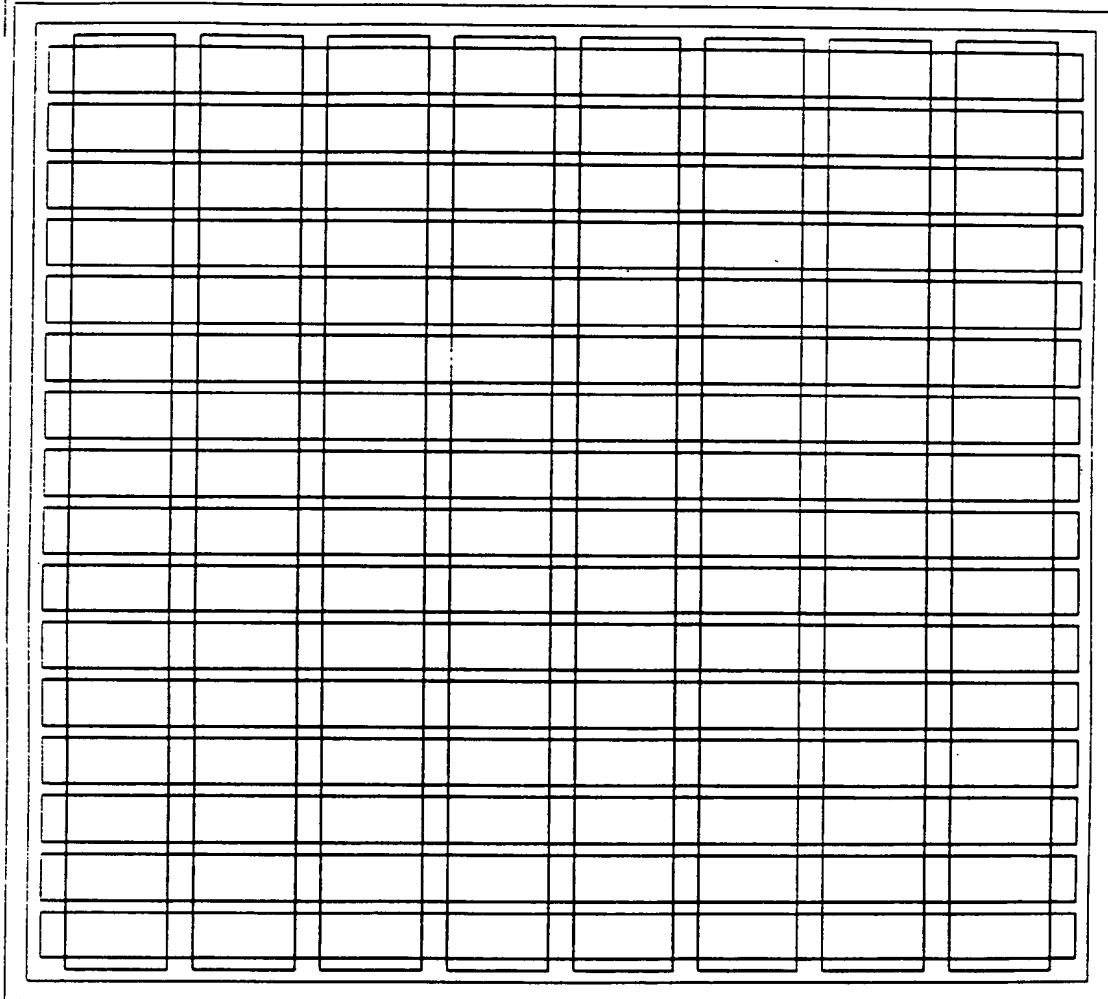


200

FIG. 33E2

TO
FIG.
33E4

TO
FIG.
33E1



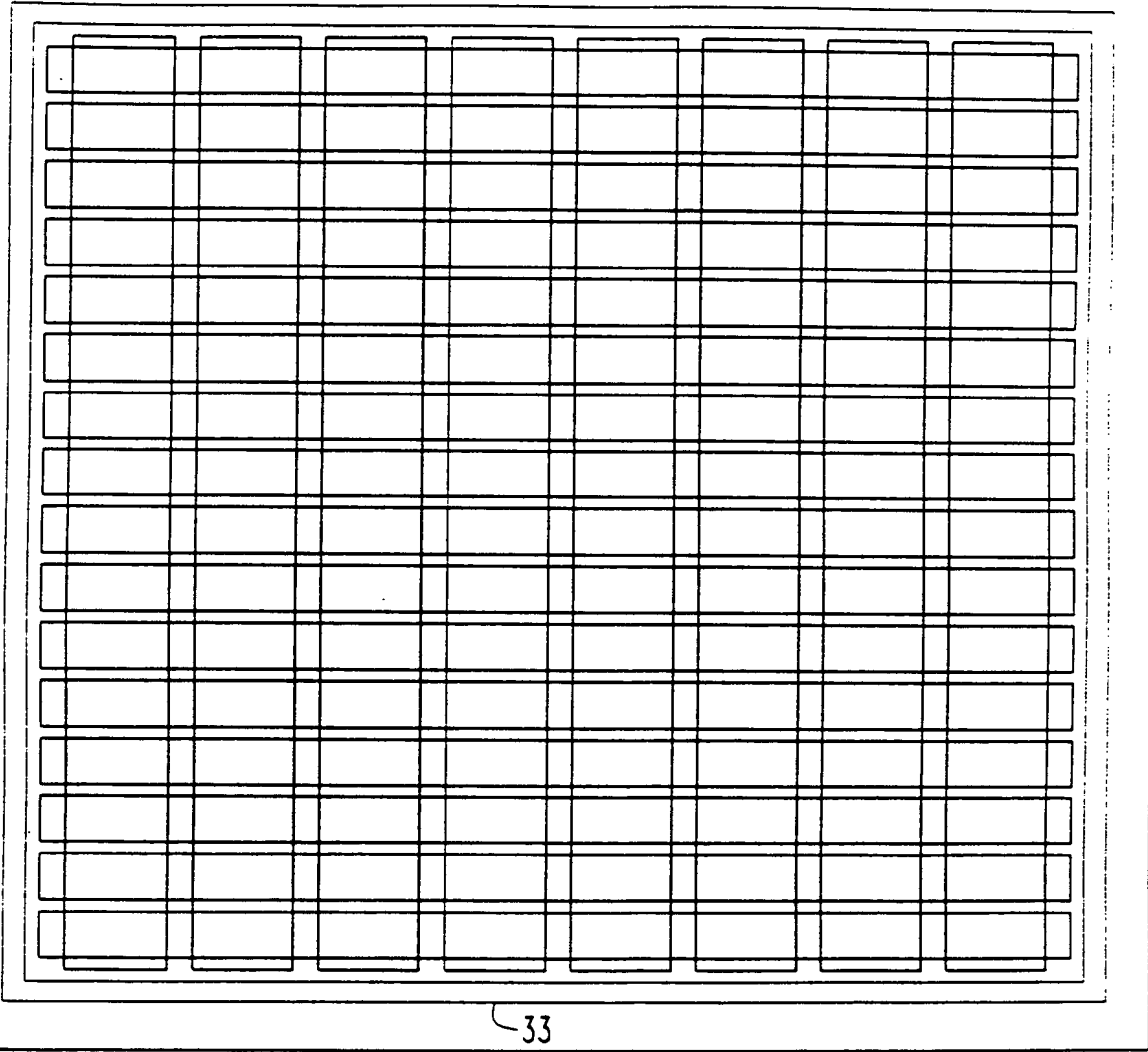
31

TO FIG. 33E4

FIG. 33E3

FIG. 33E4

TO FIG. 33E3



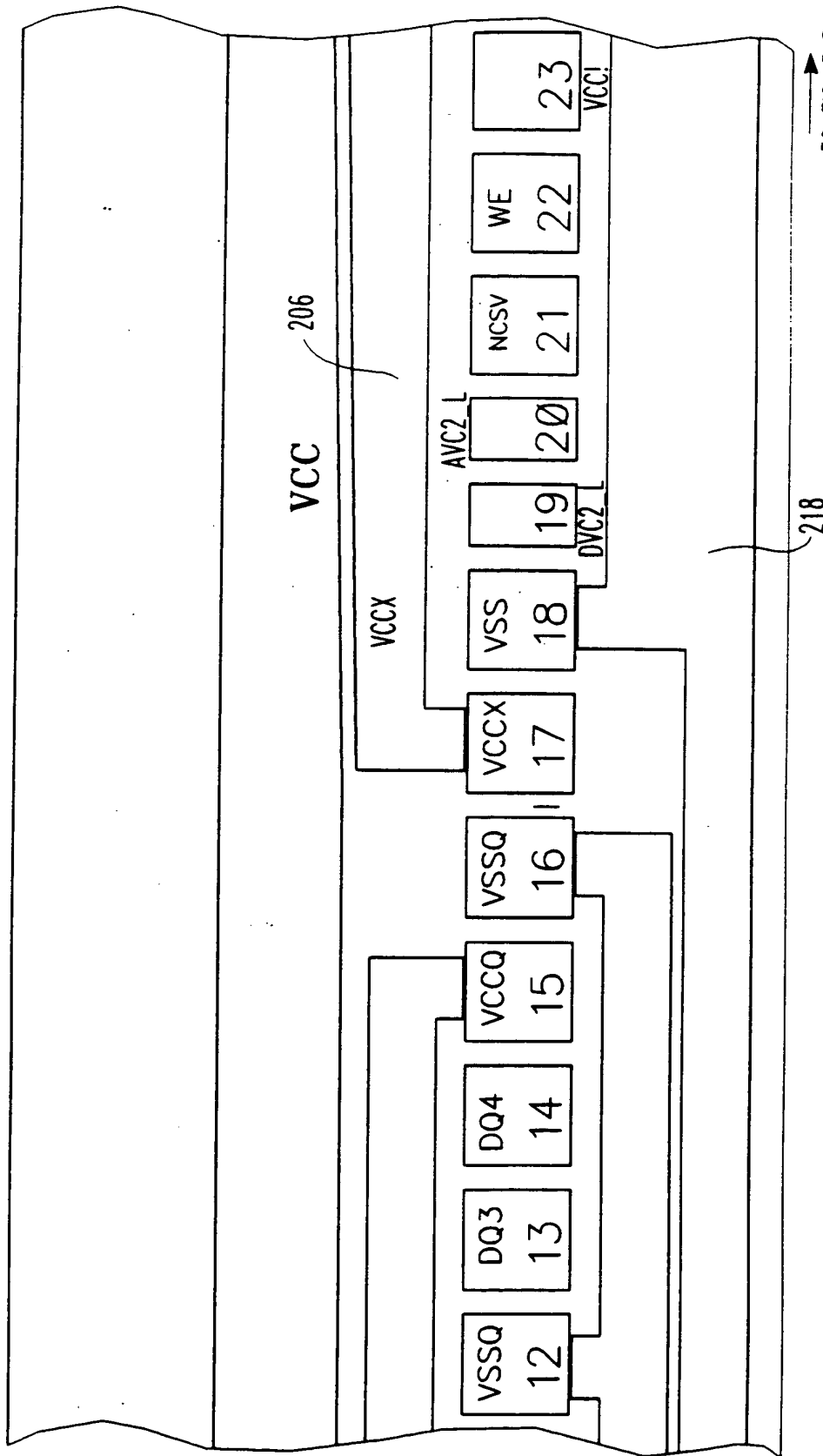
TO
FIG.
33E2

FIG. 33E4



FIG. 34A1

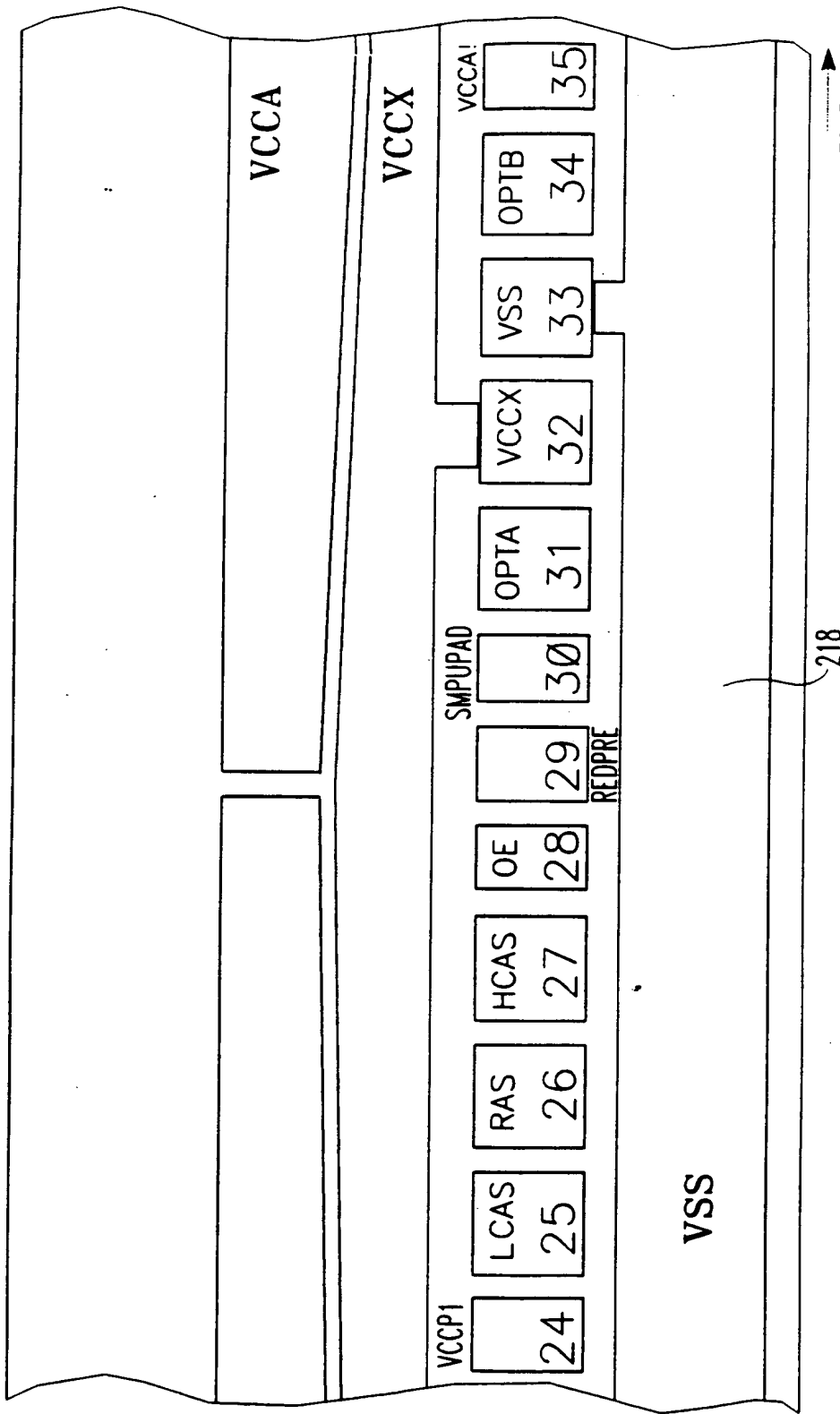
TO FIG. 34A1



TO FIG. 34B1

FIG. 34A2

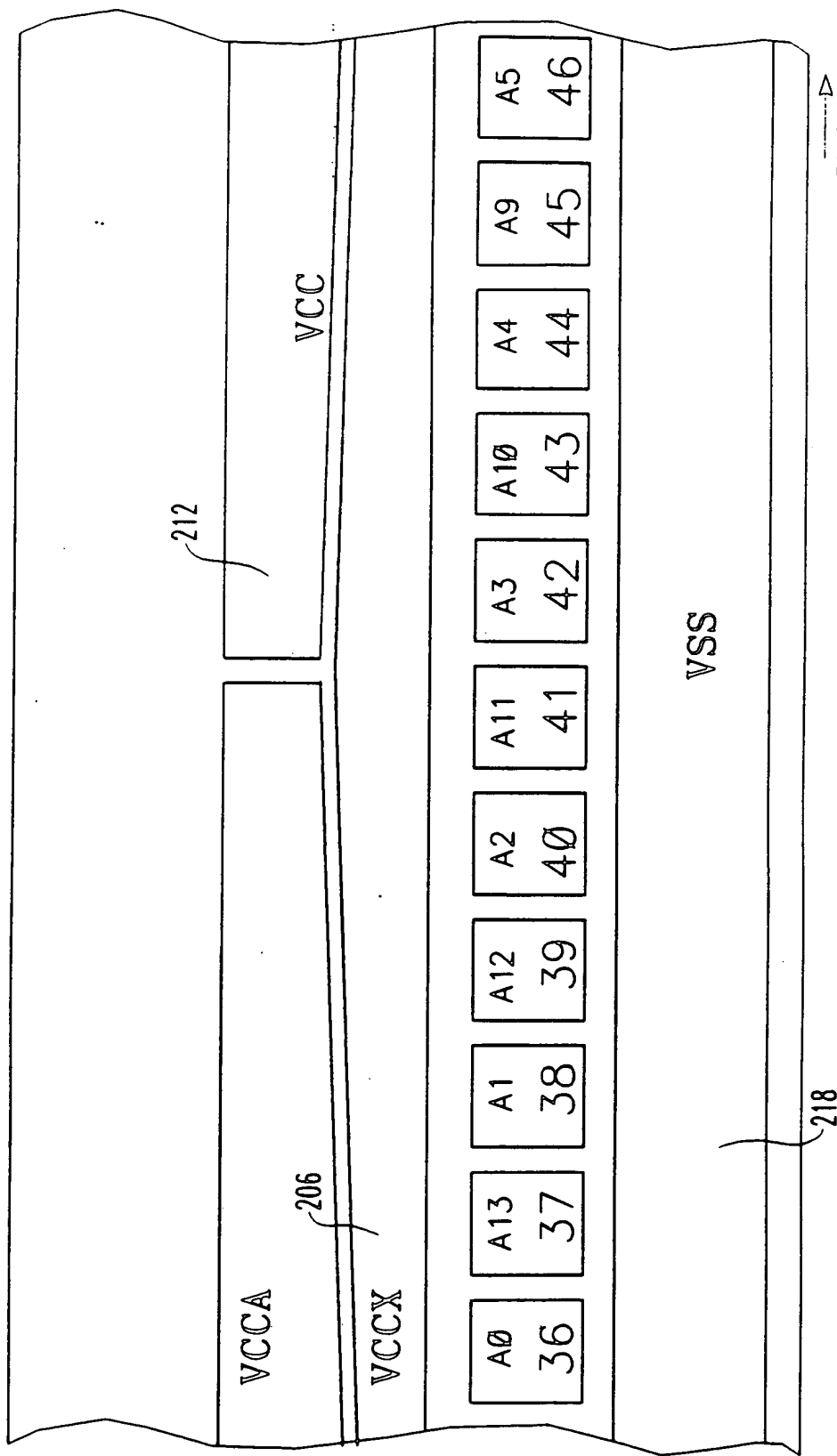
TO FIG. 34A2



TO FIG. 34B2

FIG. 34B1

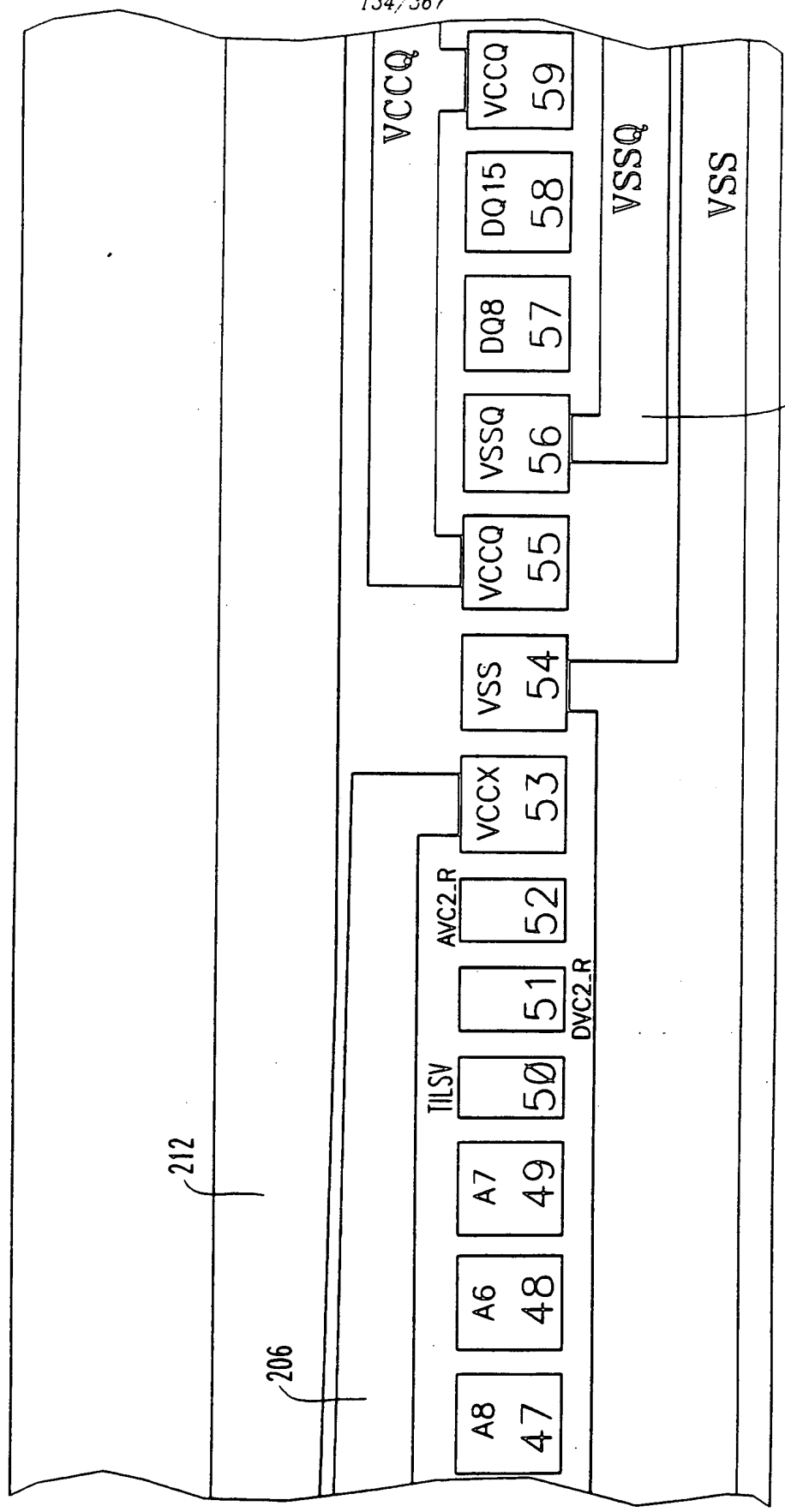
TO FIG. 34B1



TO FIG. 34C1

FIG. 34B2

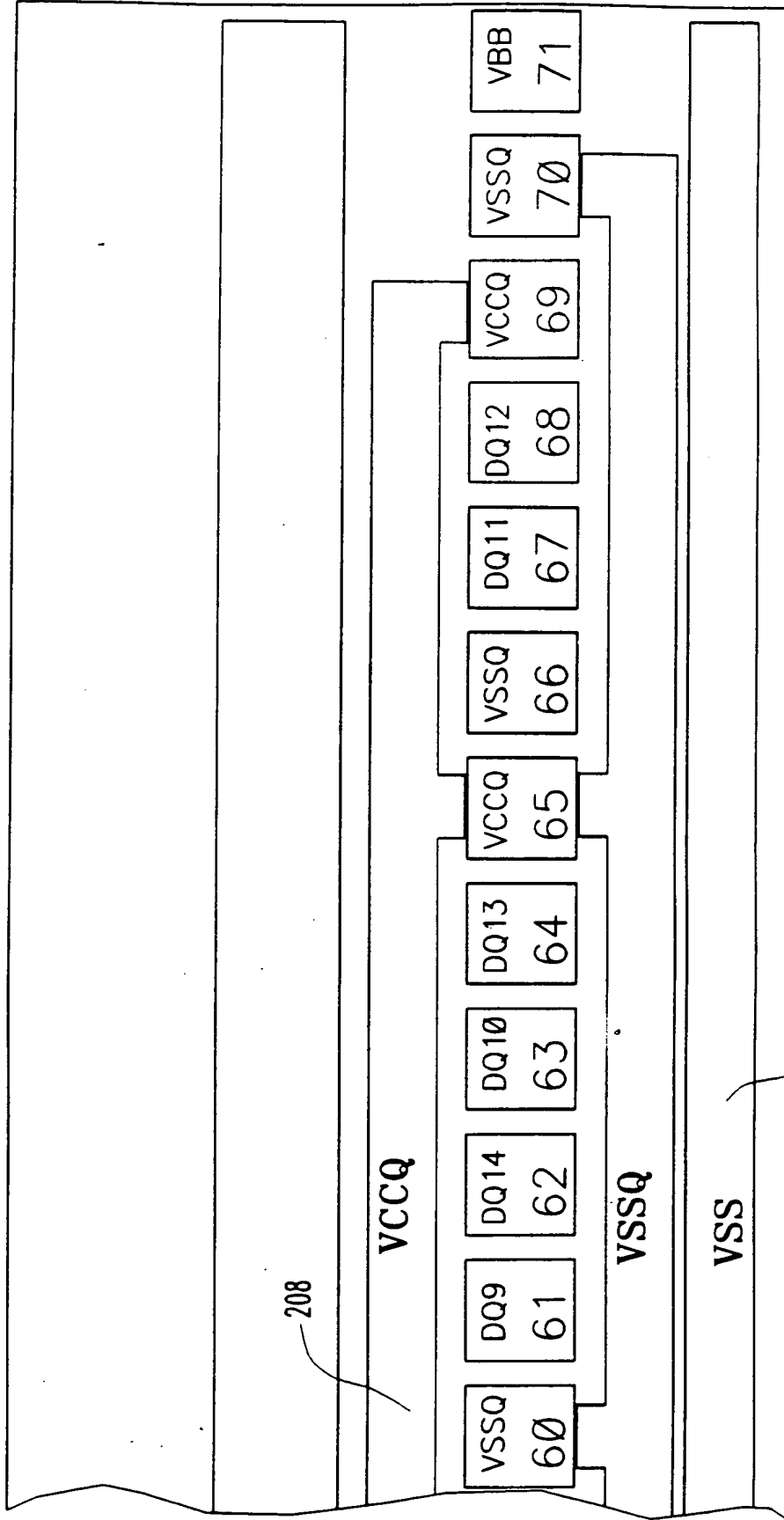
10 FIG. 34B2



10 FIG. 34C2

FIG. 34C1

TO FIG. 34C1



218

FIG. 34C2

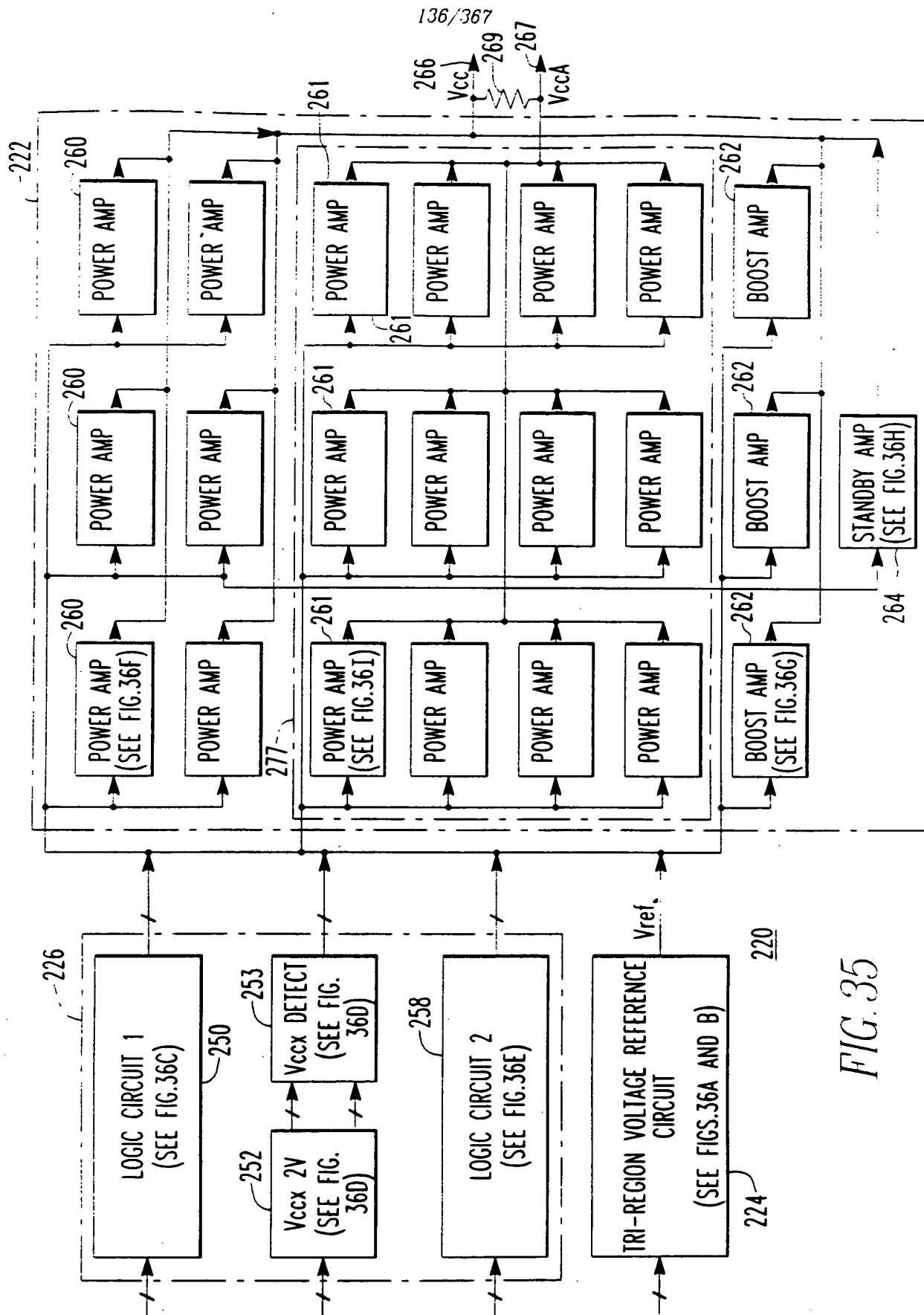


FIG. 35

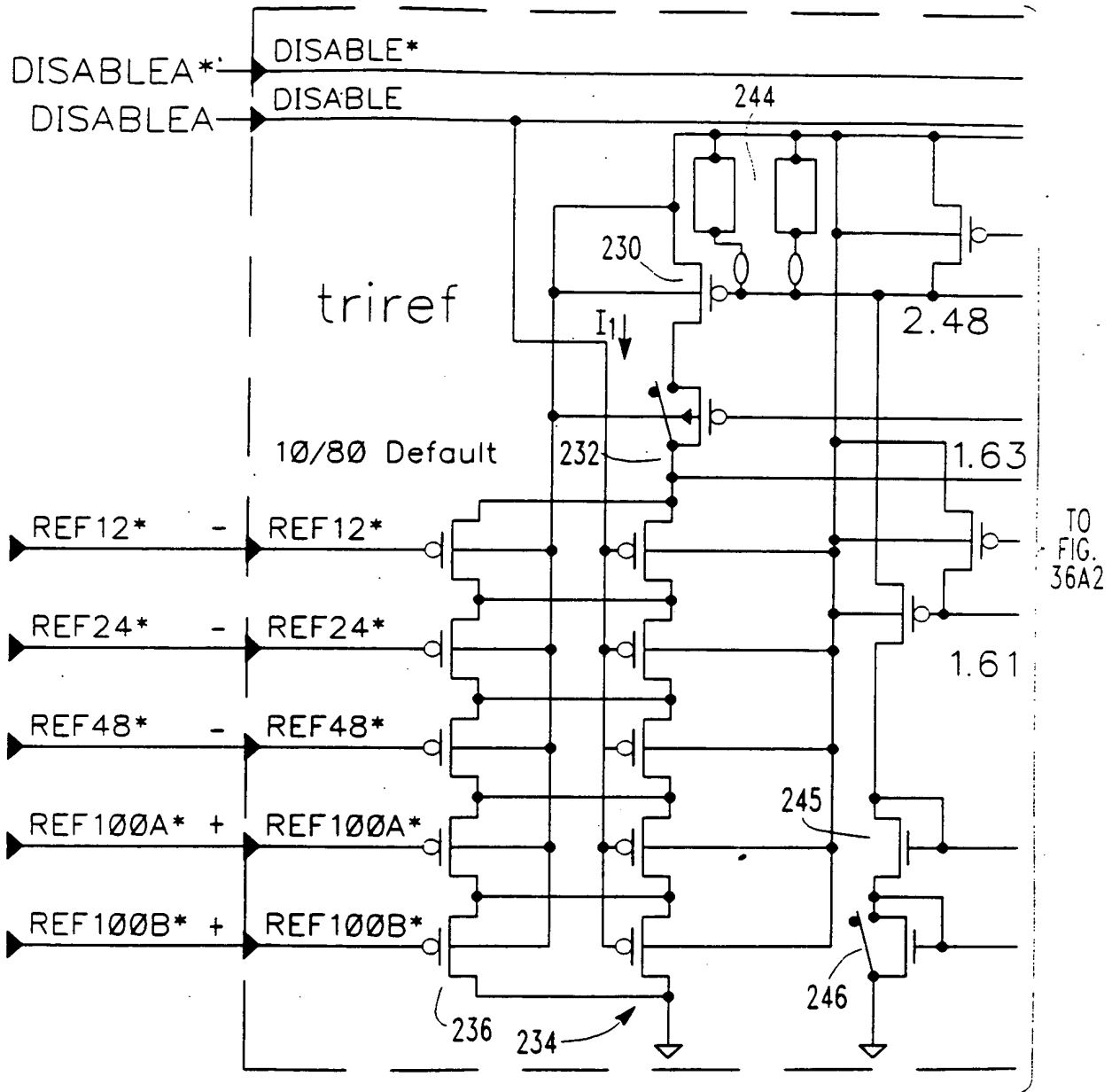


FIG. 36A1

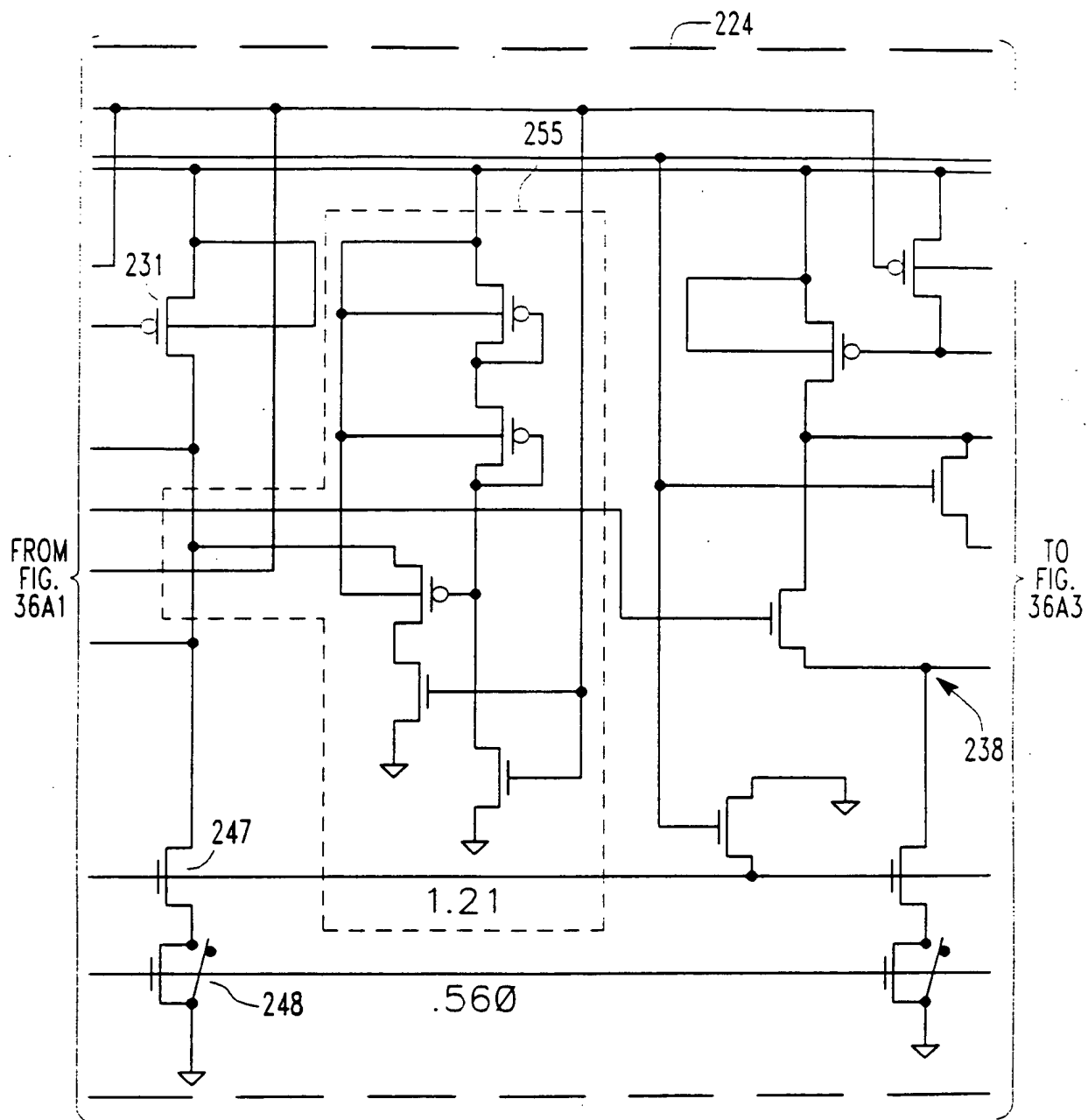


FIG. 36A2

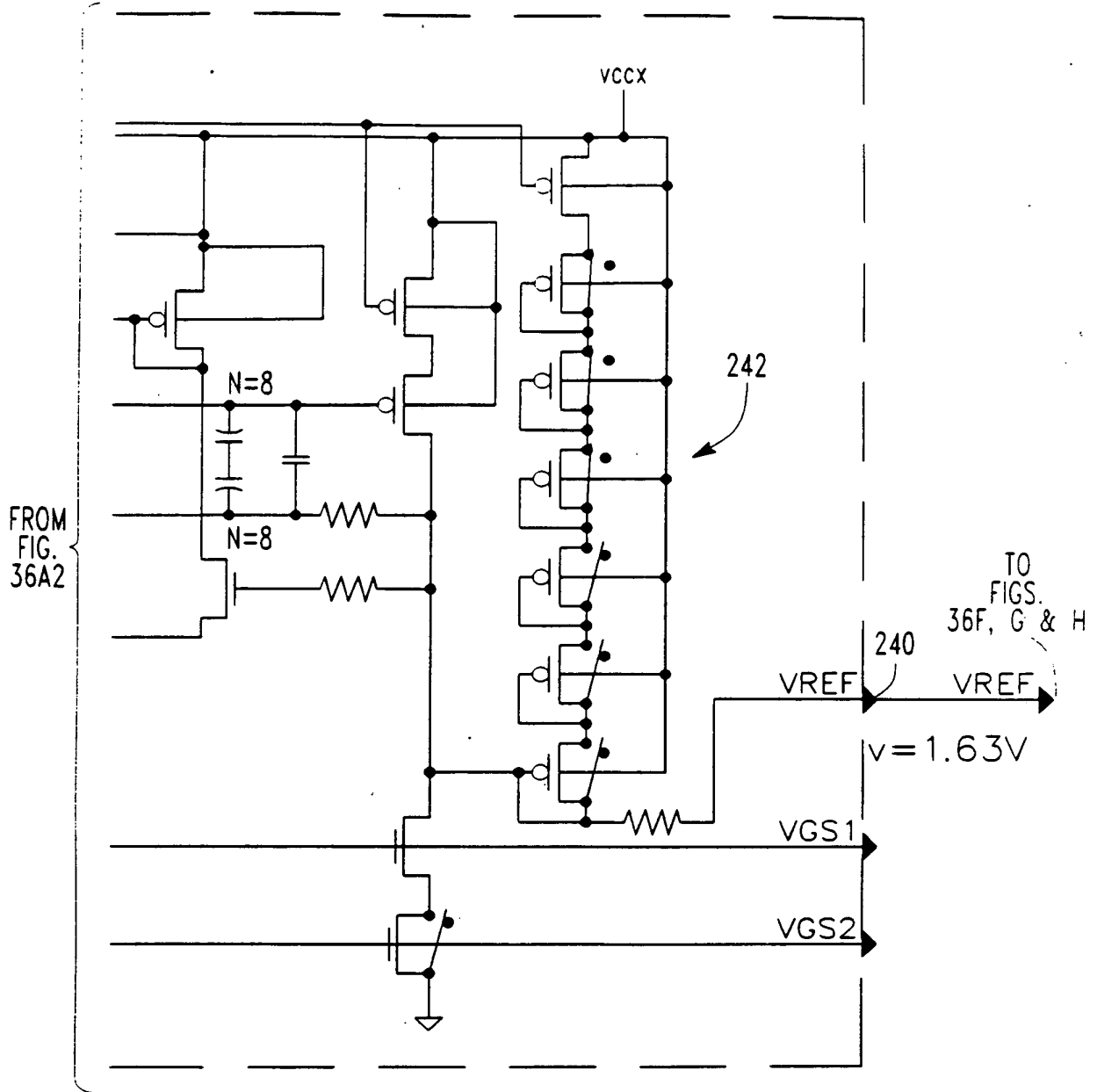


FIG. 36A3

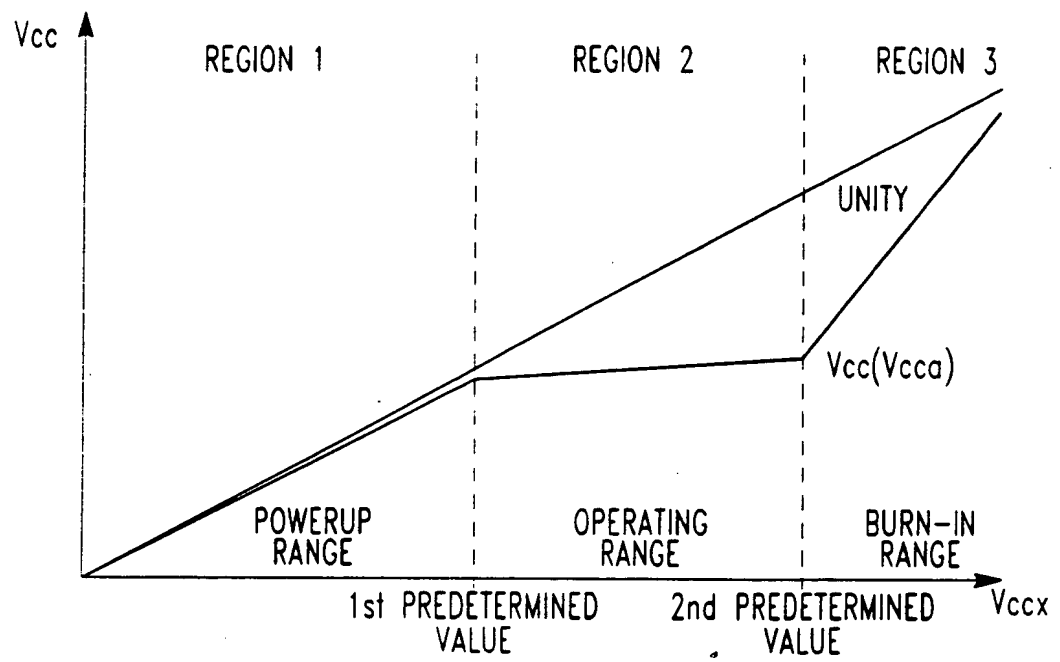


FIG. 36B

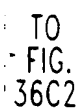


FIG. 36C1

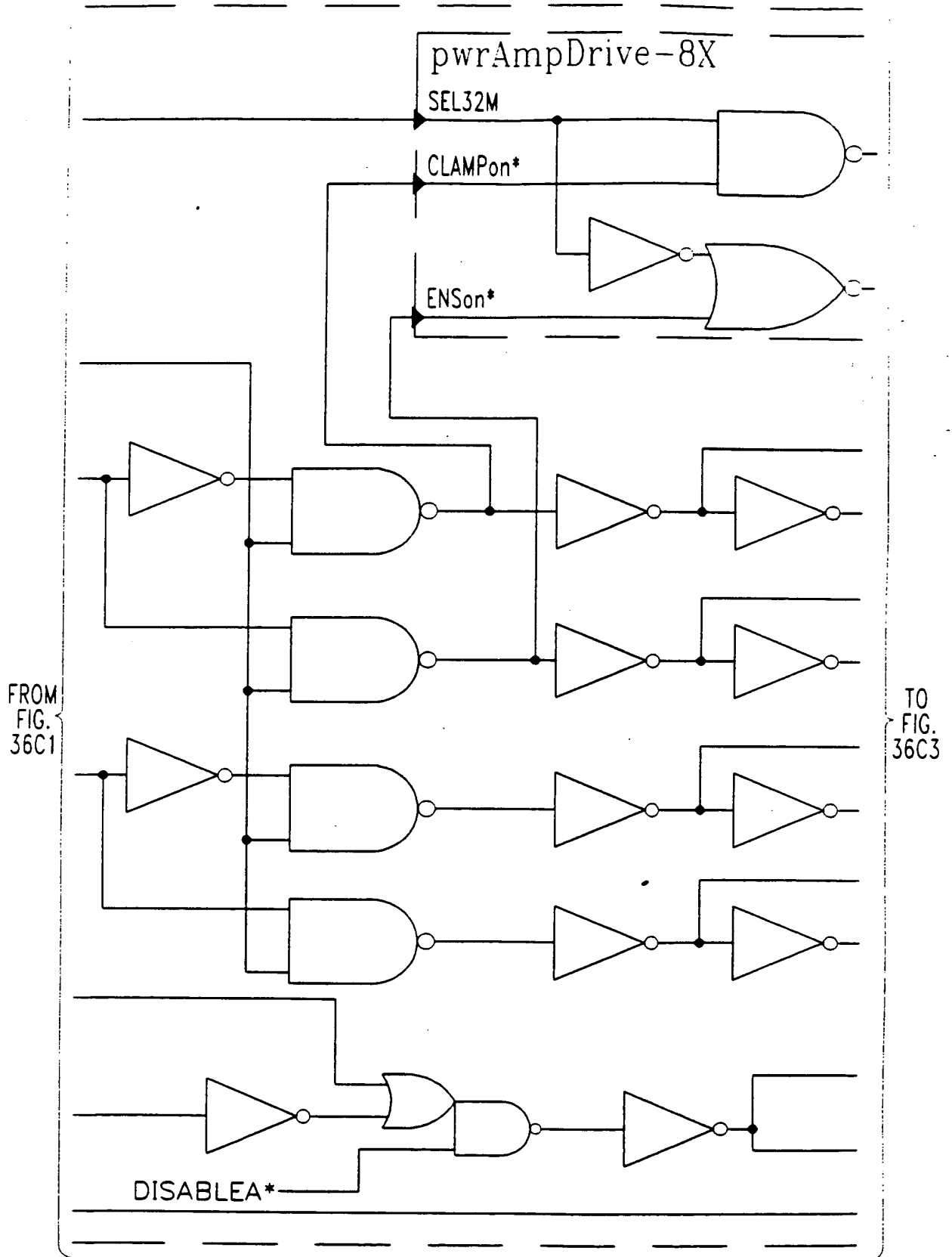


FIG. 36C2

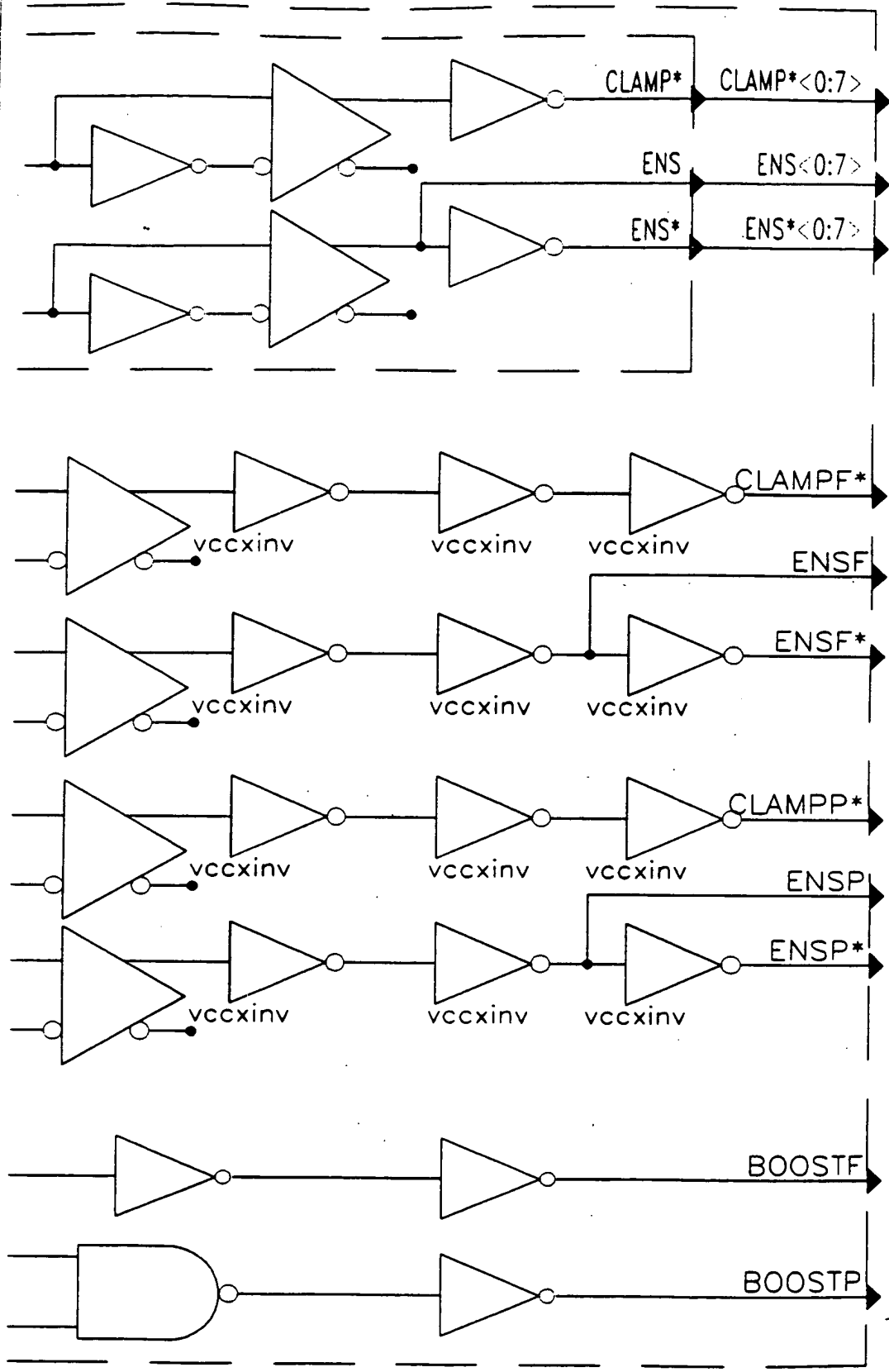
FROM
FIG.
36C2TO
FIG.
36F

FIG. 36C3

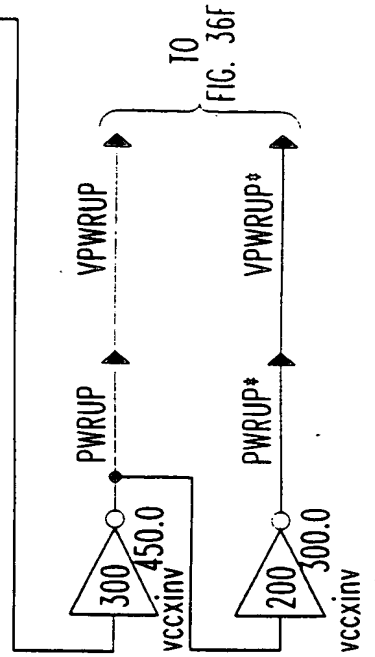
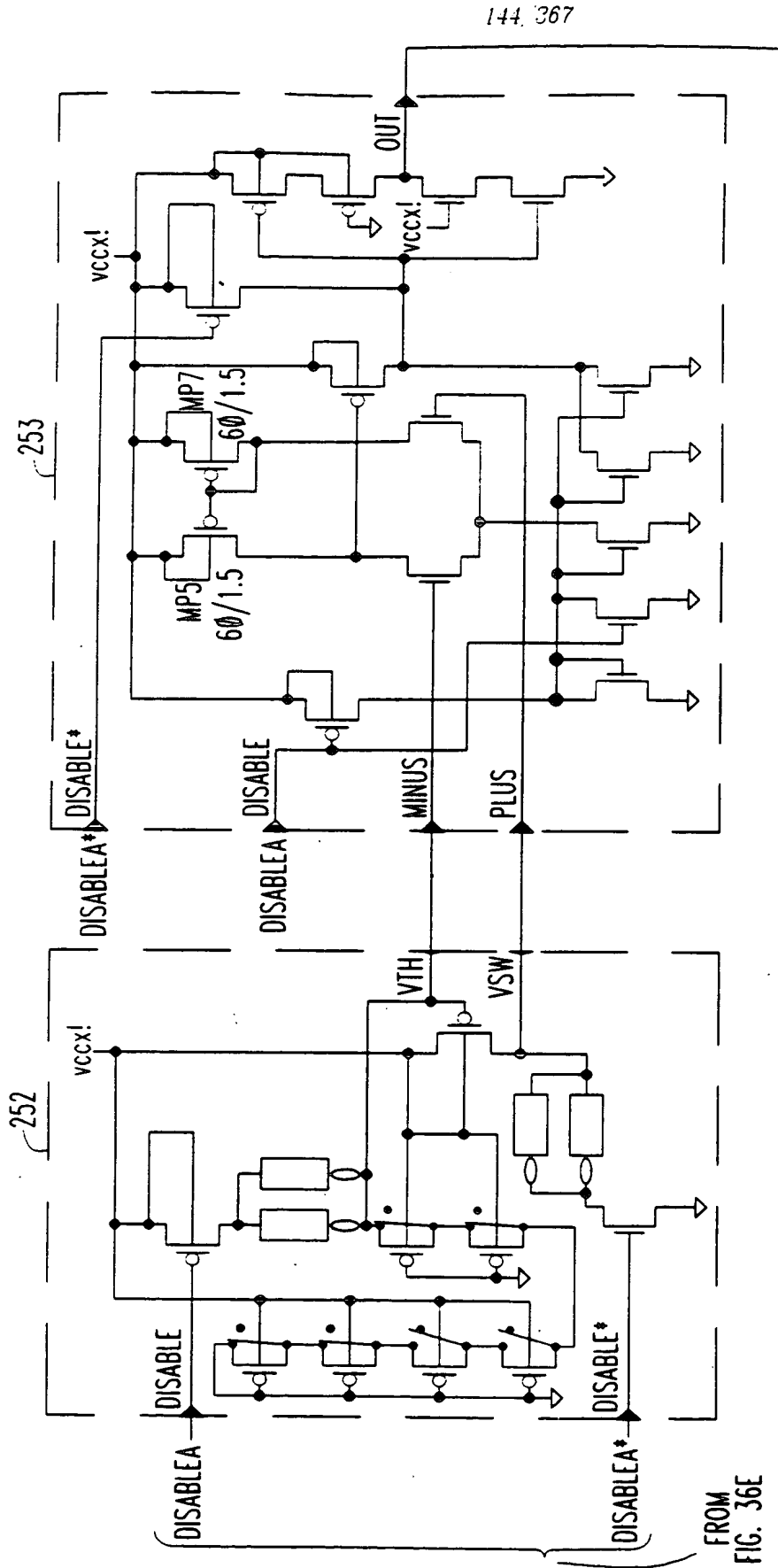


FIG. 36D

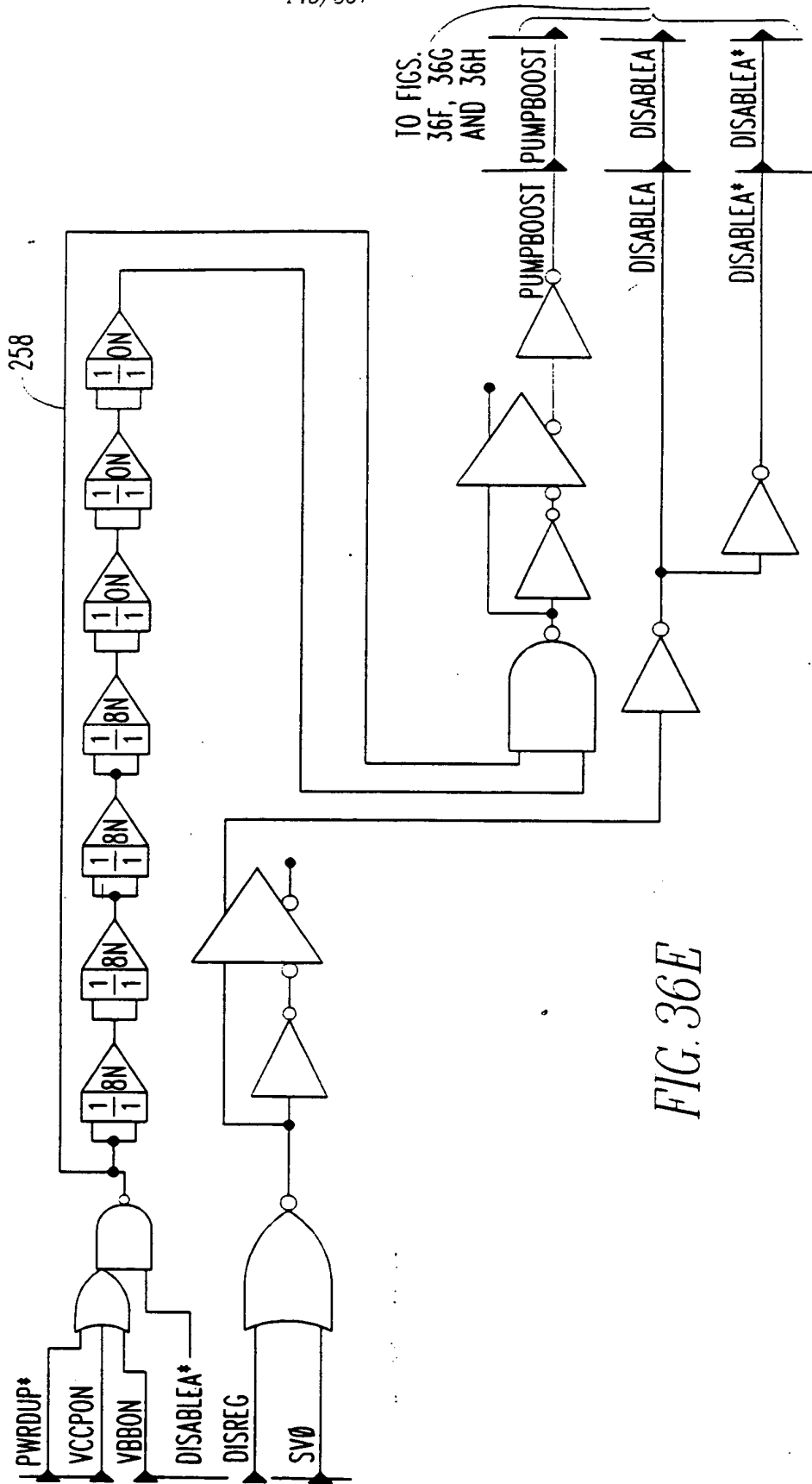
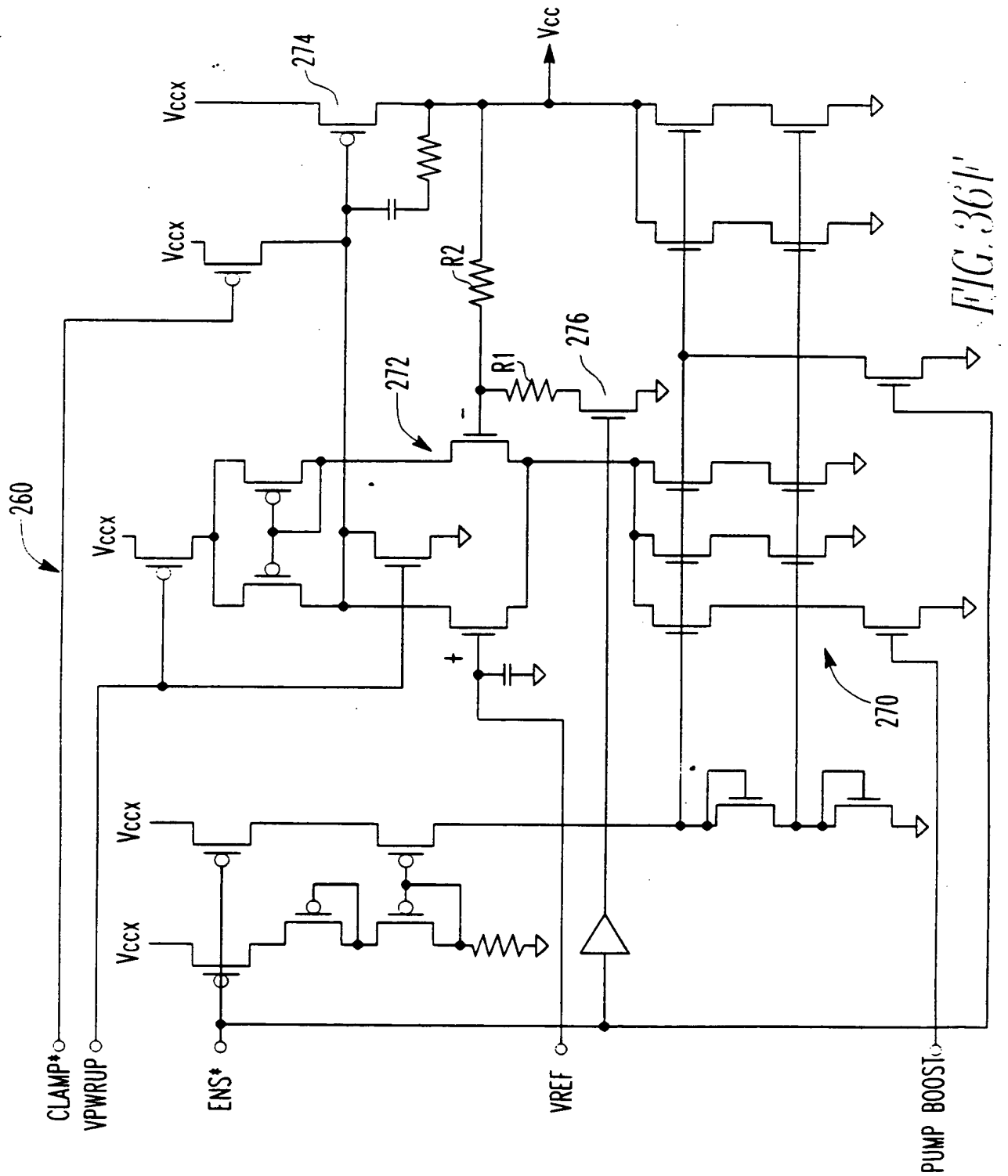


FIG. 36E



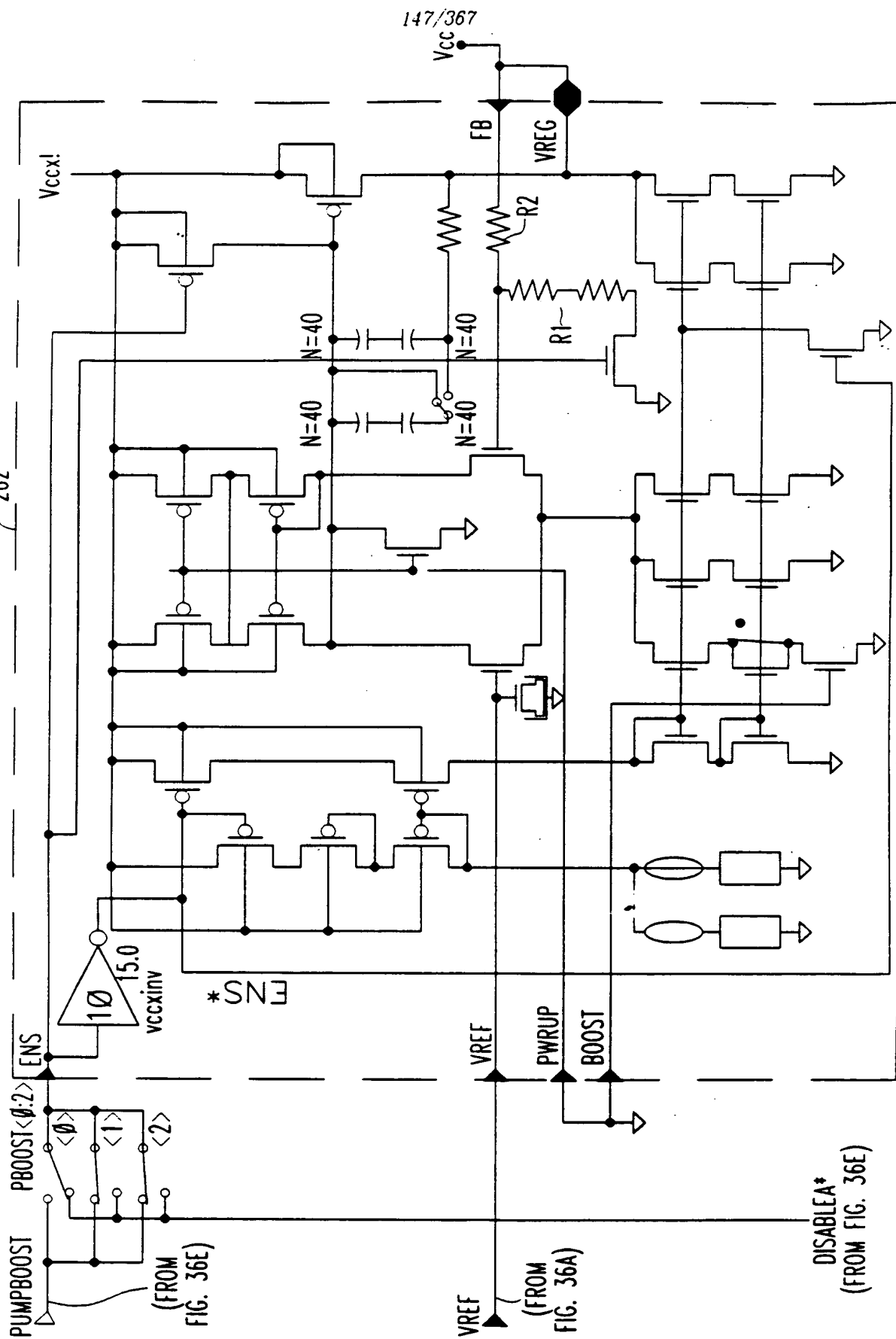


FIG. 36G

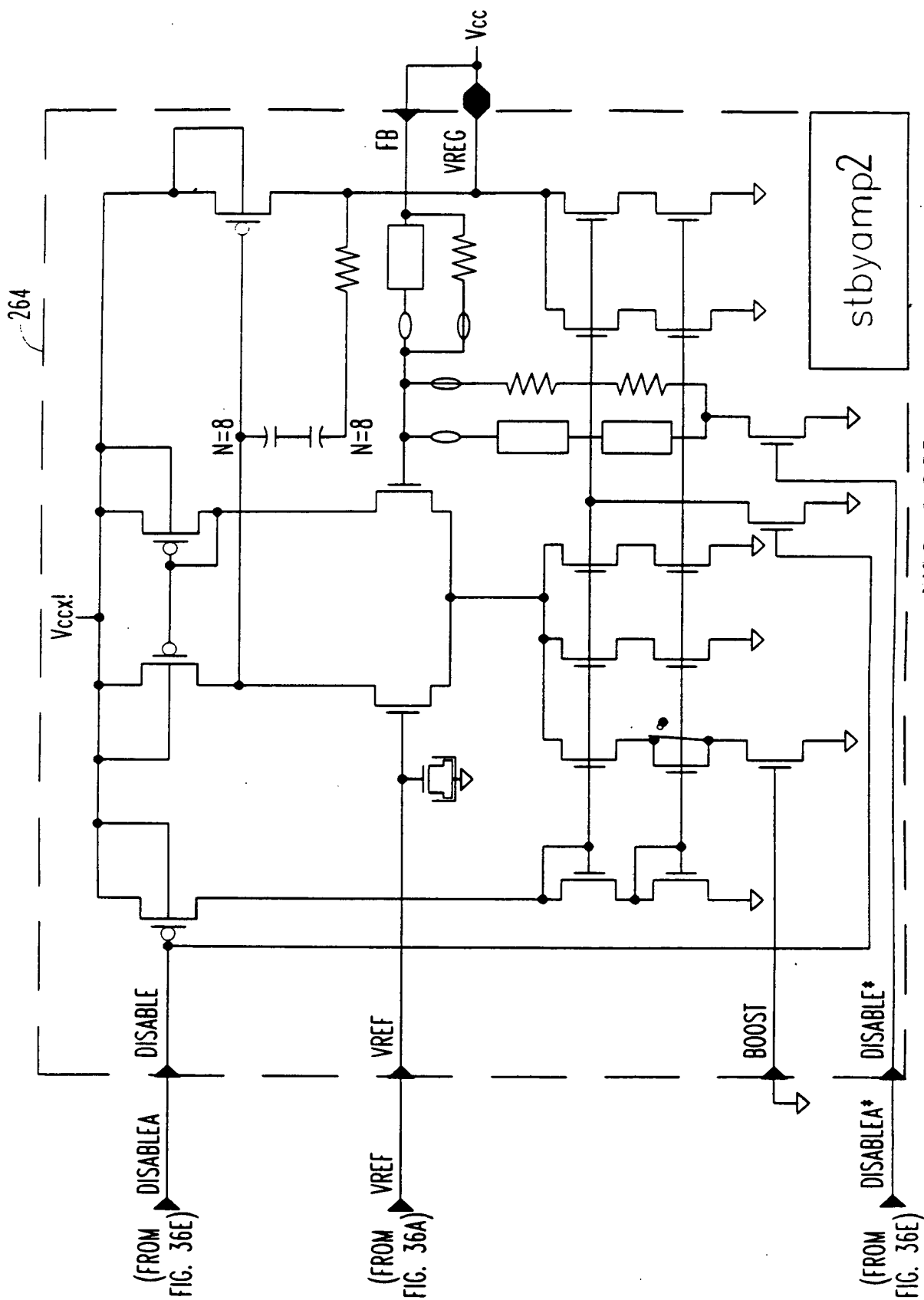


FIG. 36H

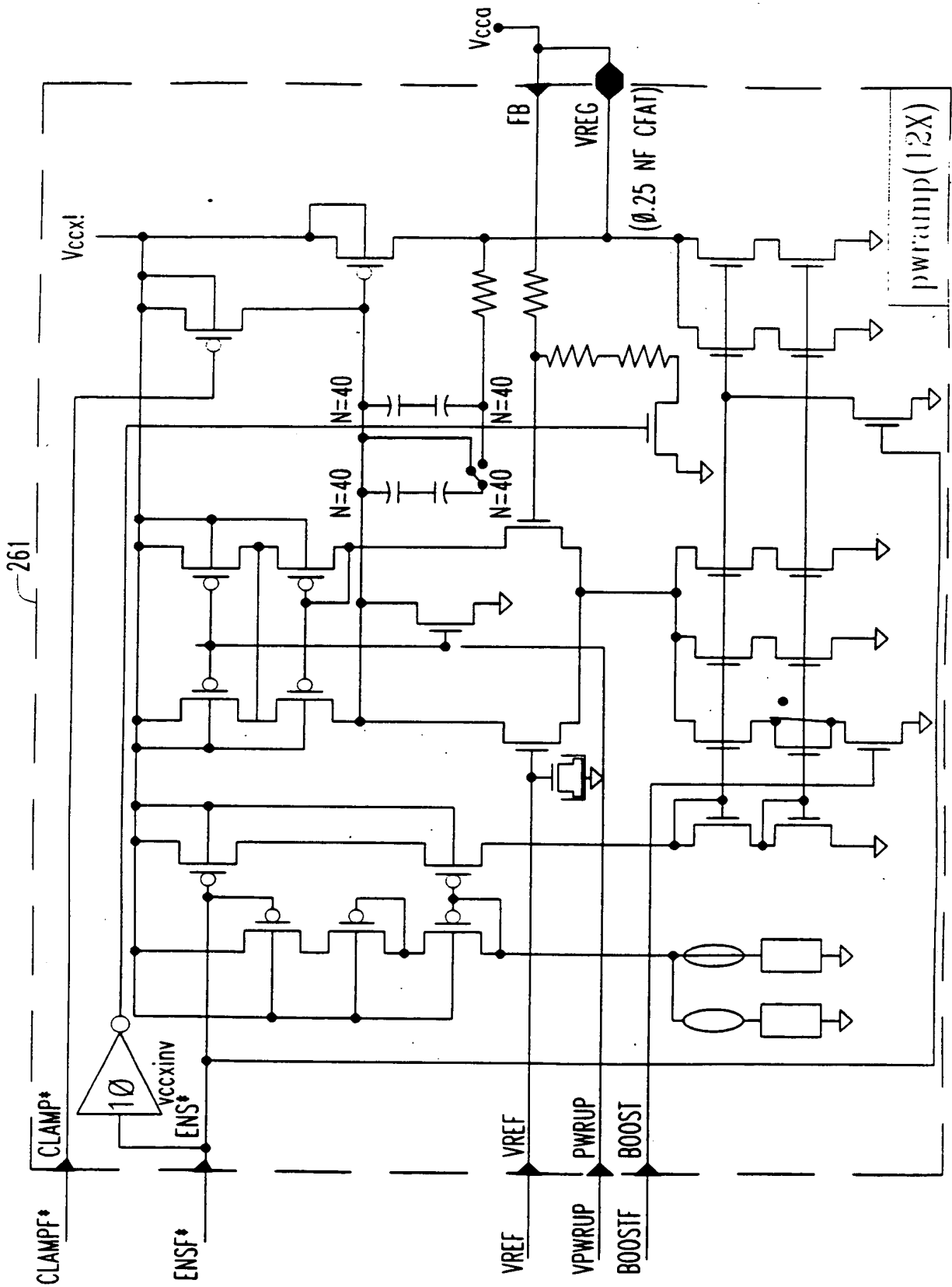


FIG. 361

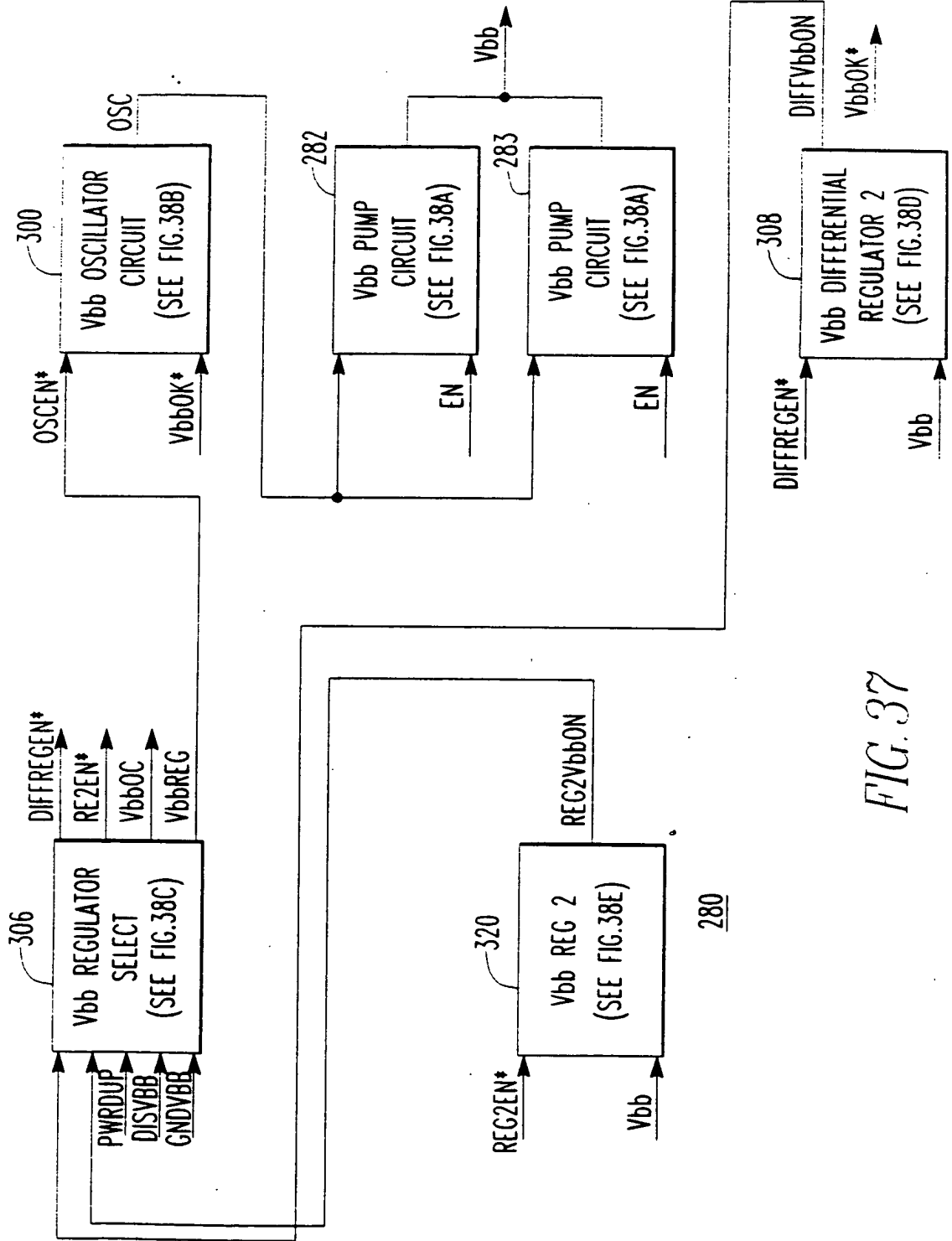


FIG. 37

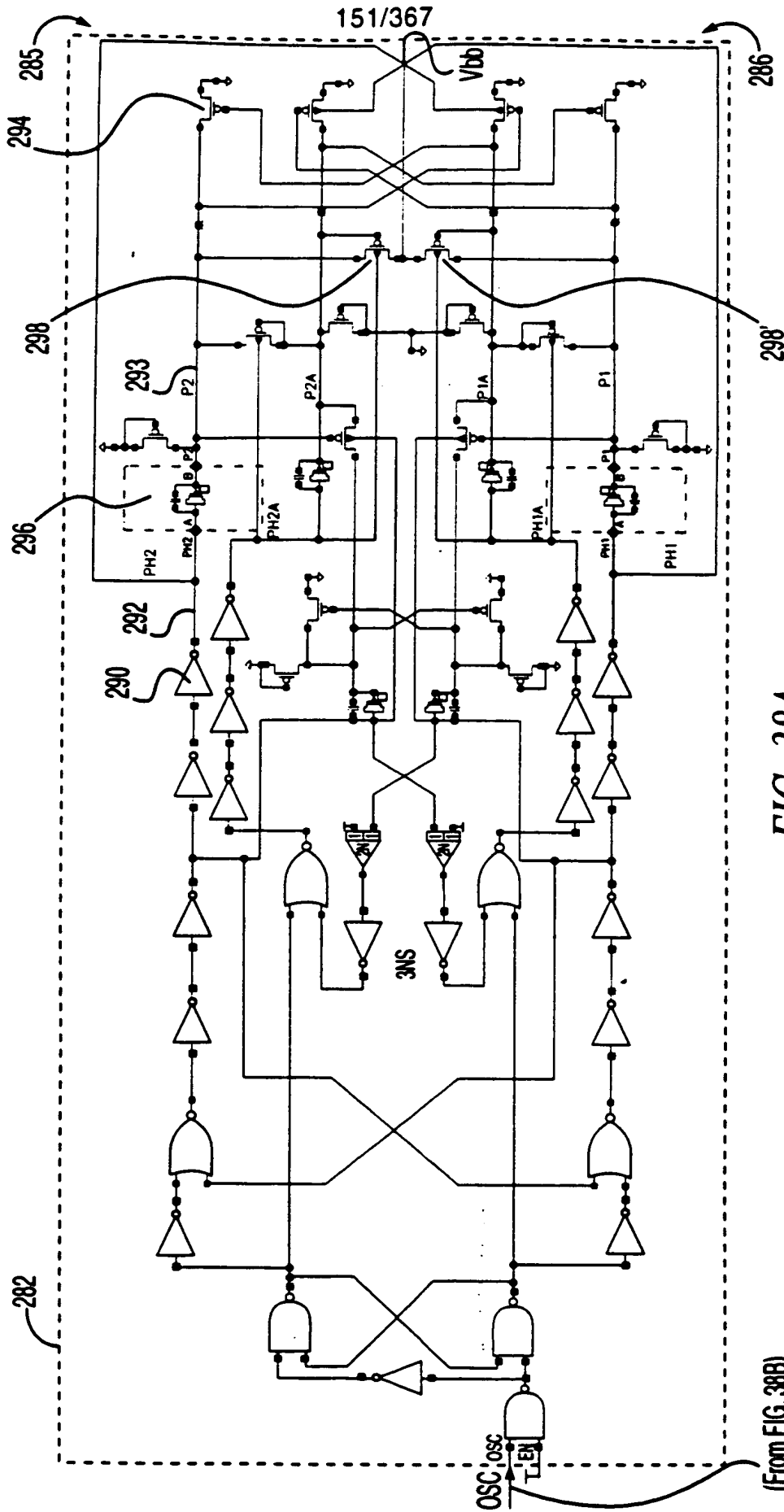
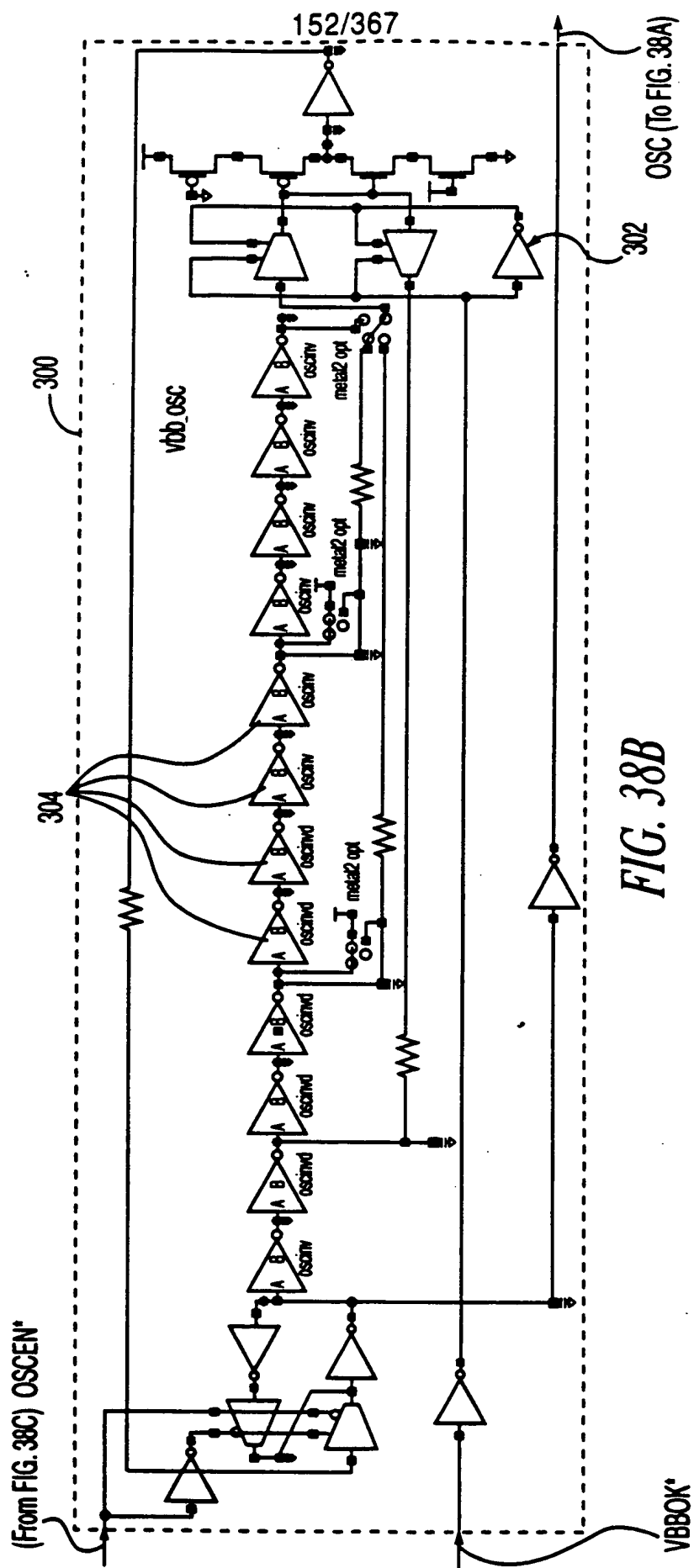


FIG. 38A

(From FIG. 38B)



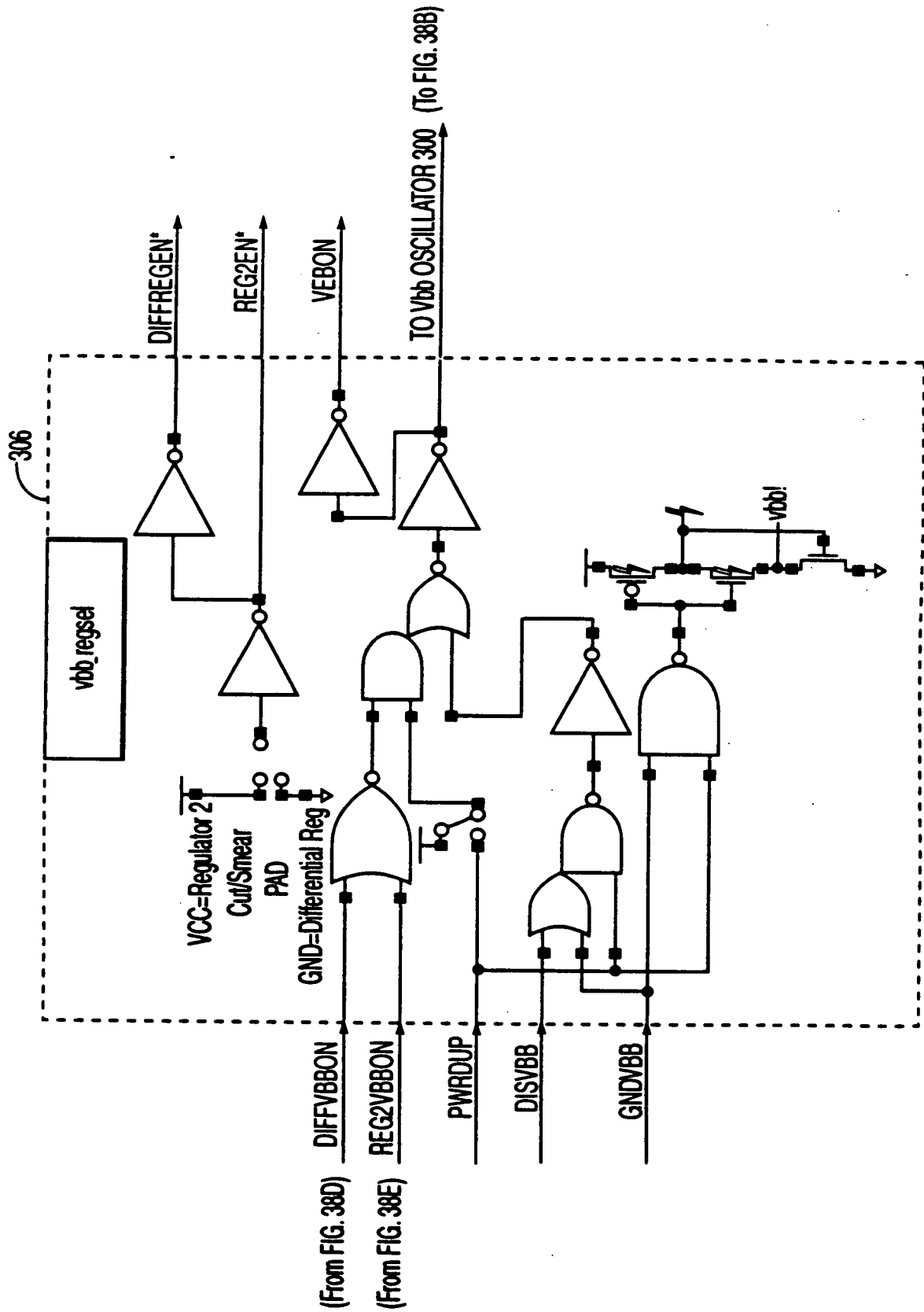


FIG. 38C

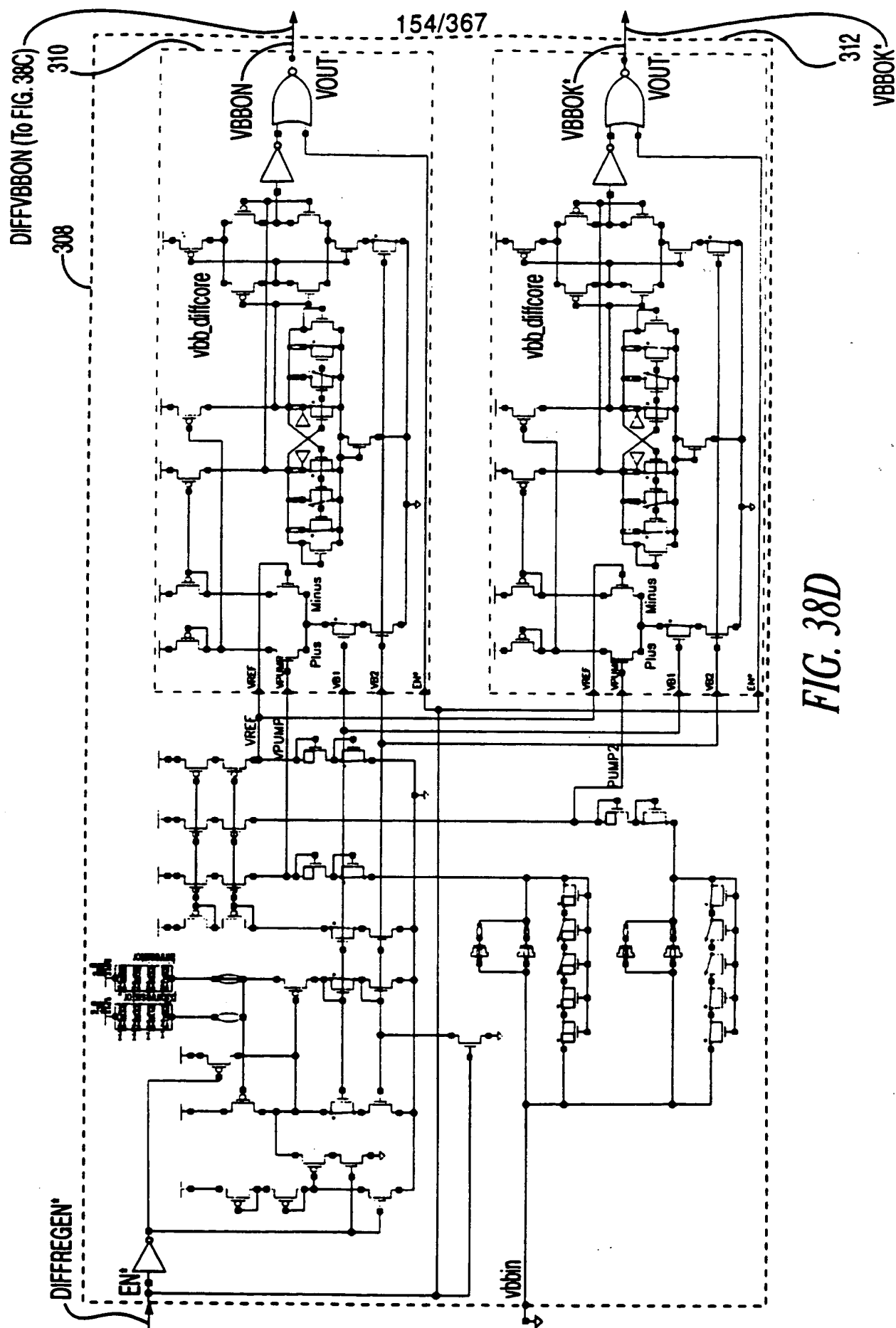


FIG. 38D

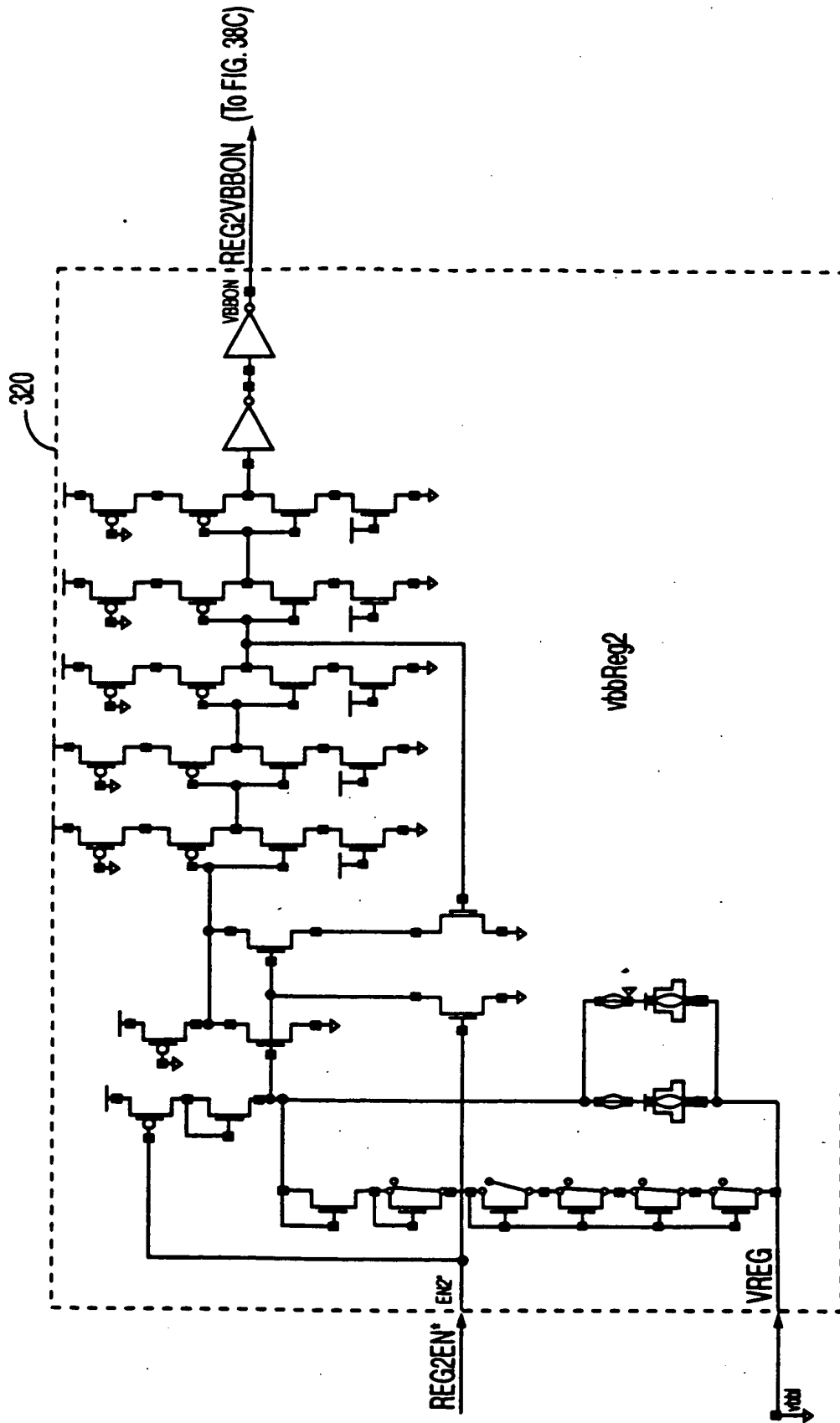


FIG. 38E

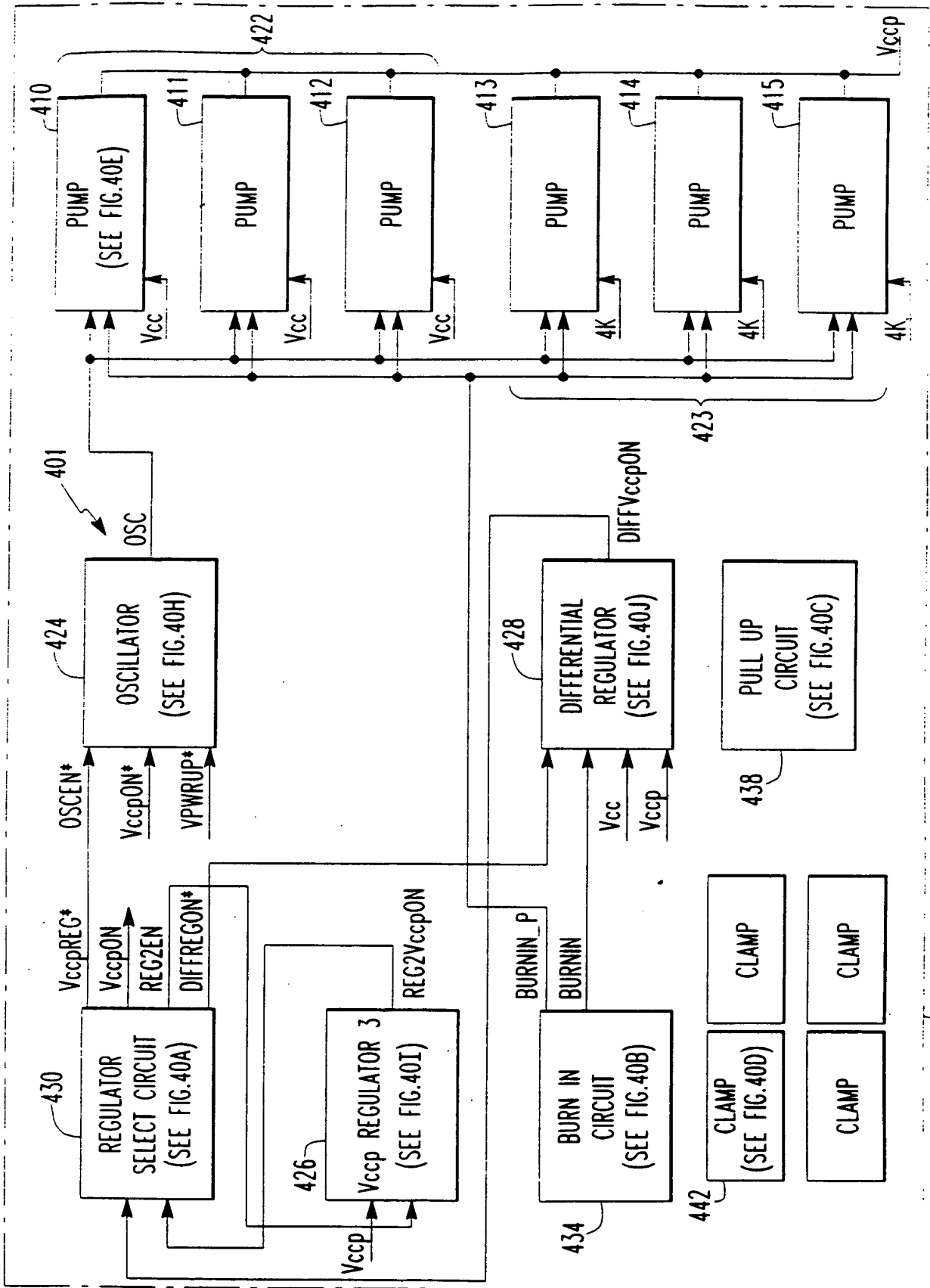


FIG. 39

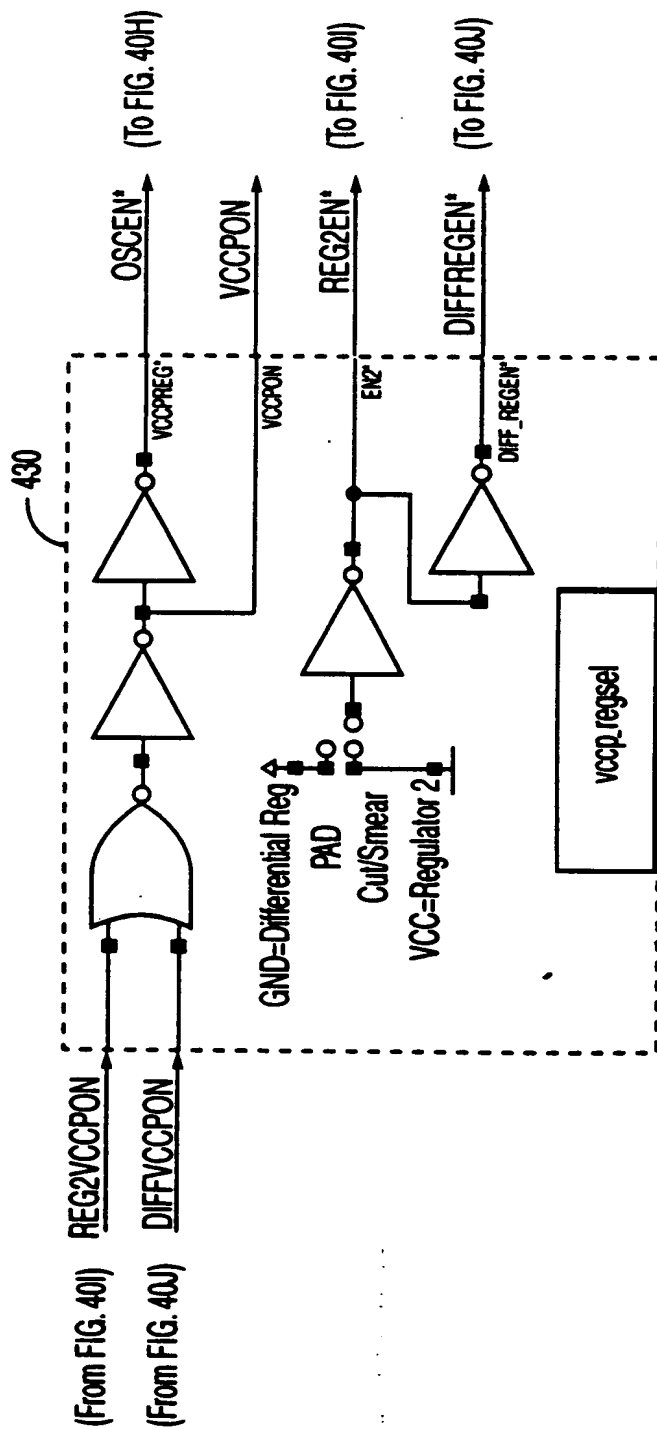


FIG. 40A

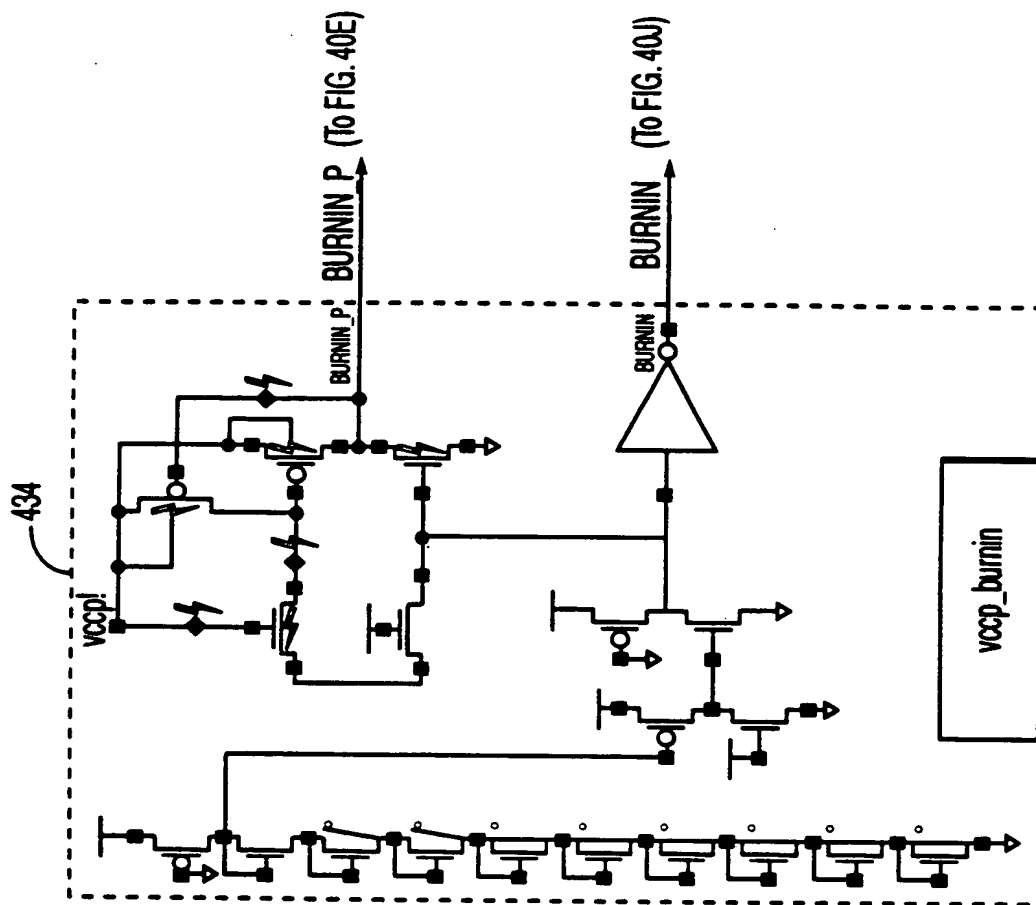


FIG. 40B

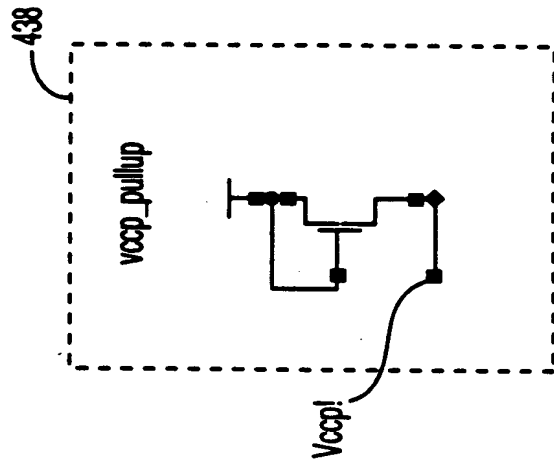


FIG. 40C

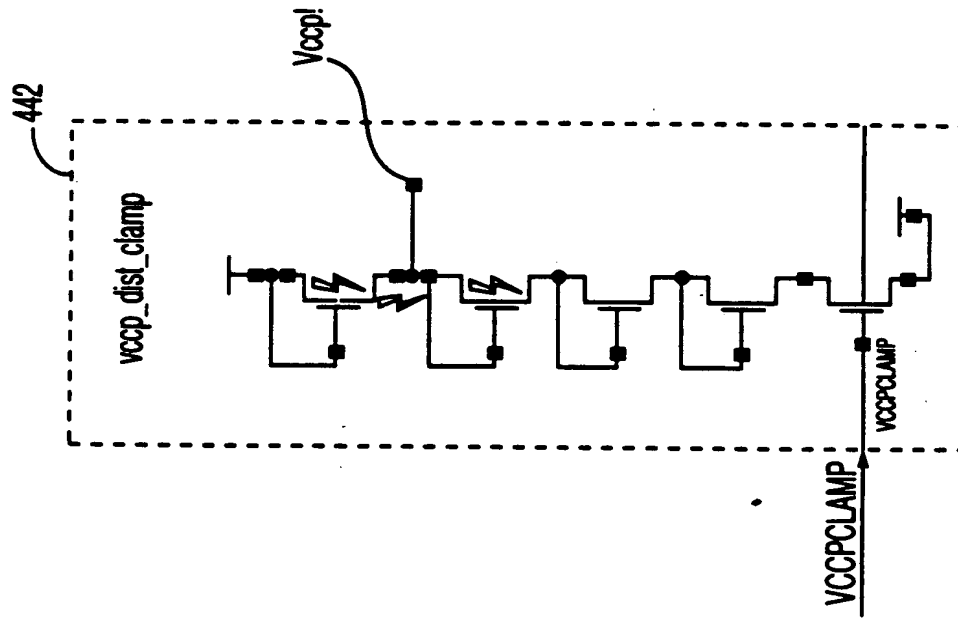
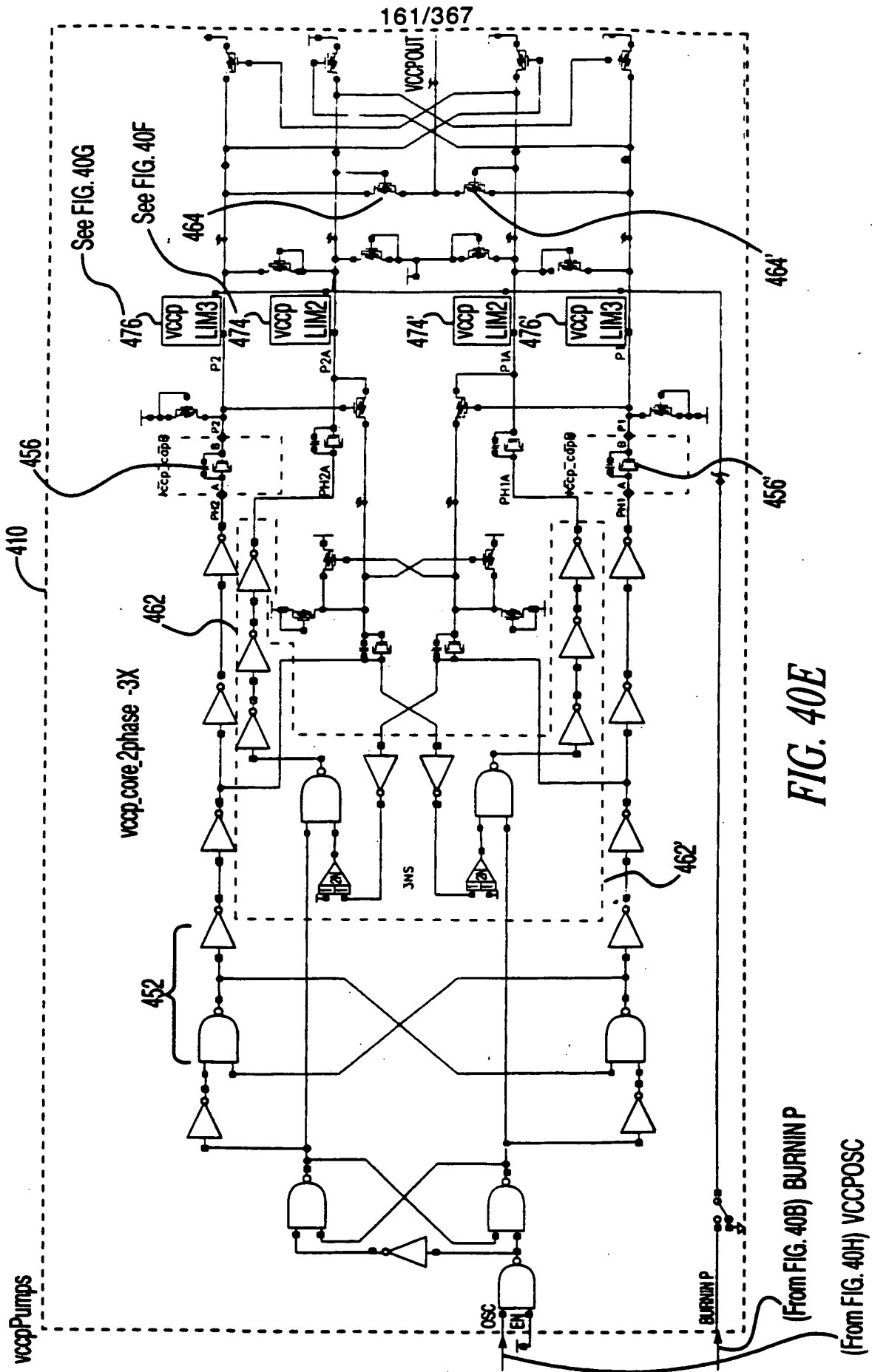


FIG. 40D



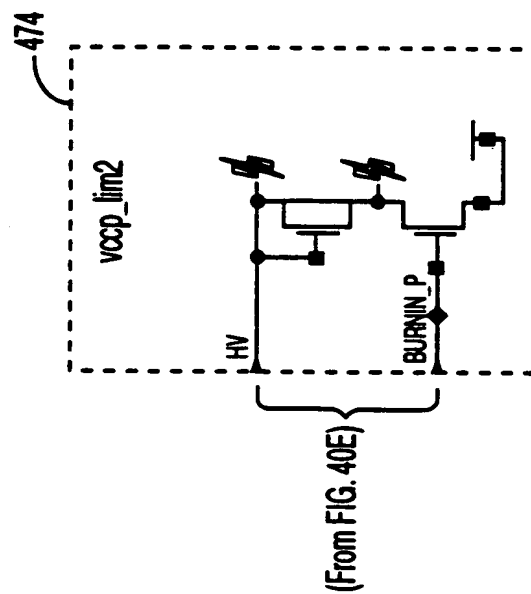


FIG. 40F

FIG. 40G

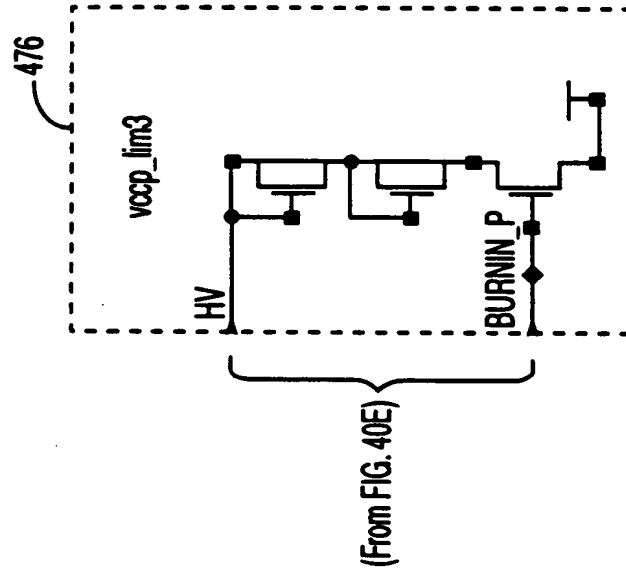


FIG. 40G

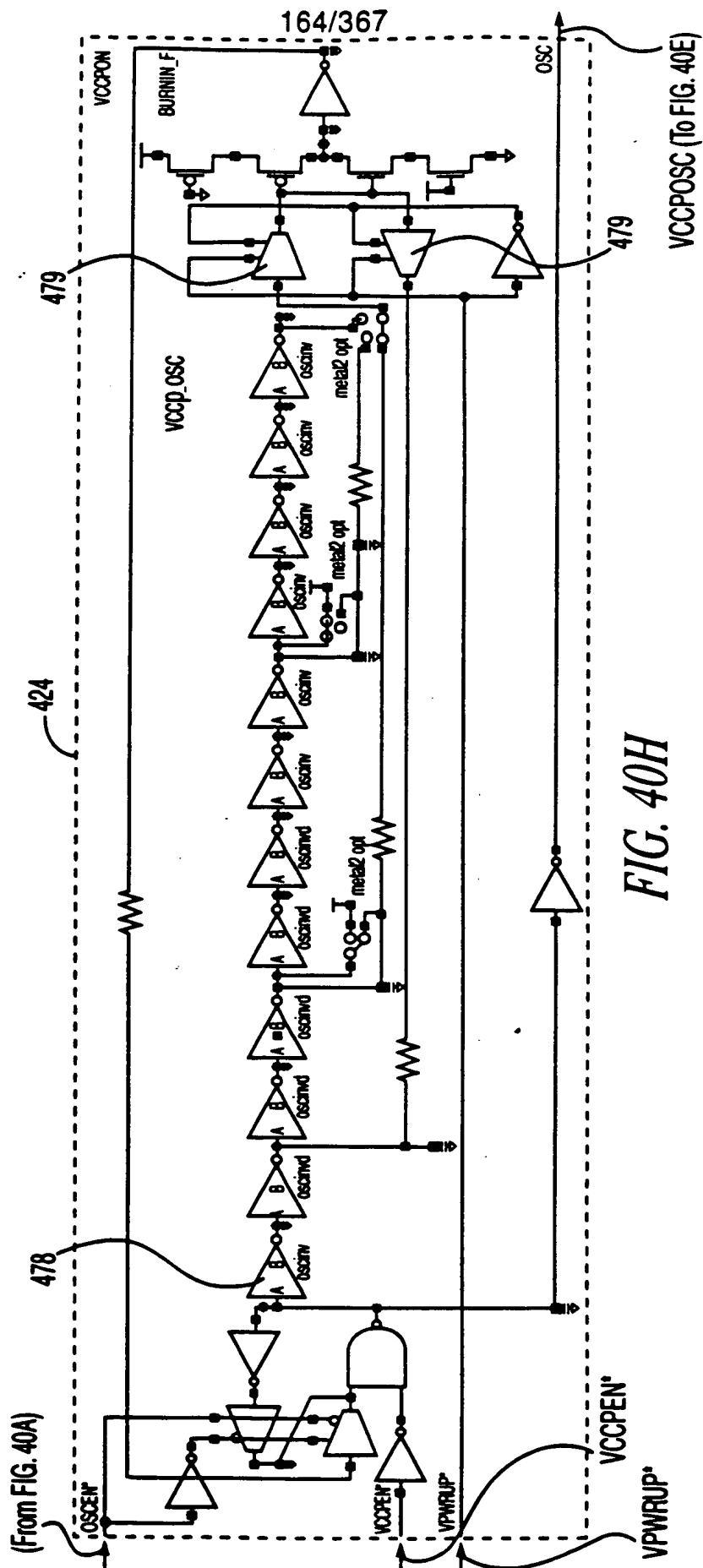


FIG. 40H

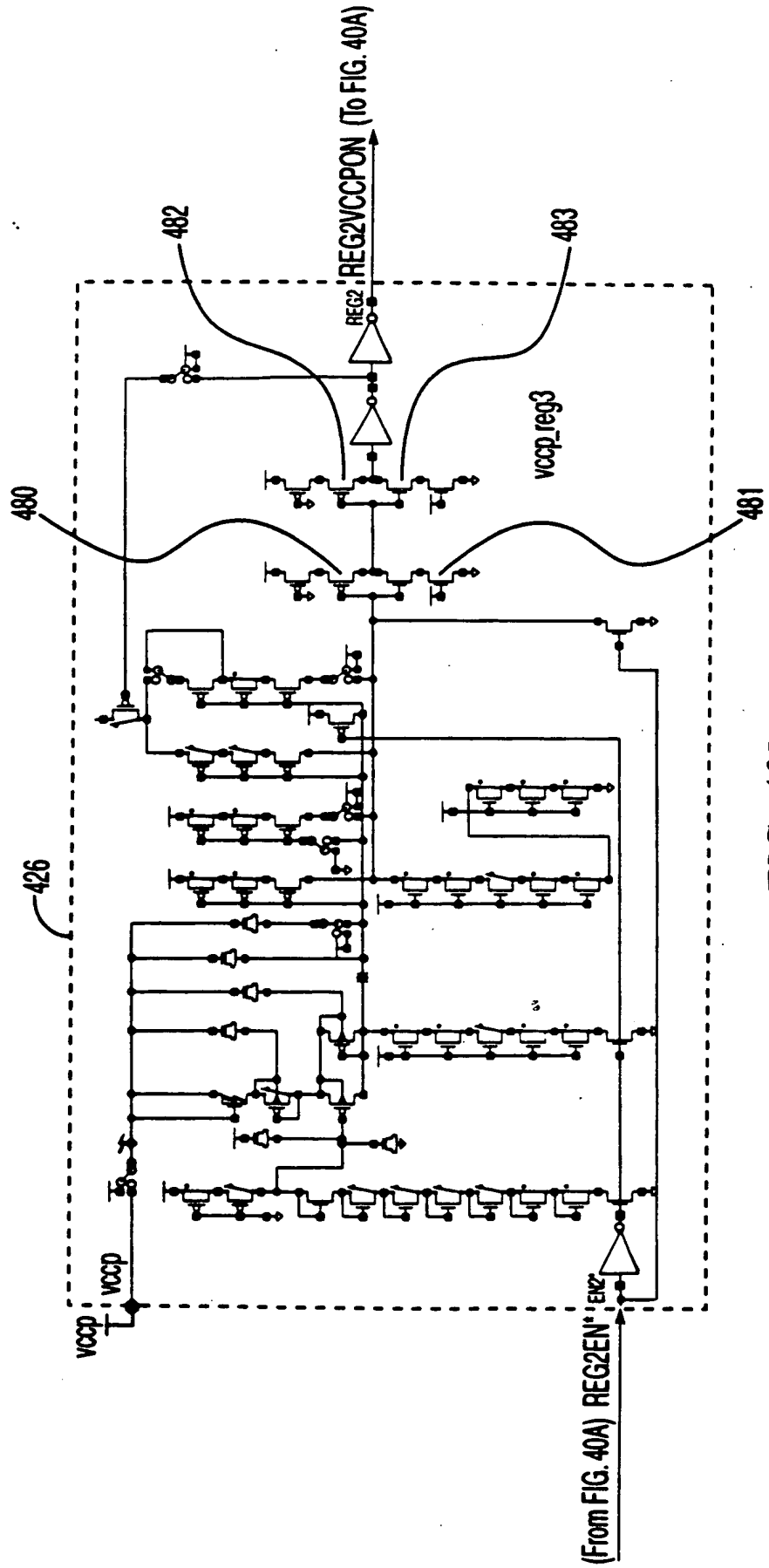


FIG. 40I

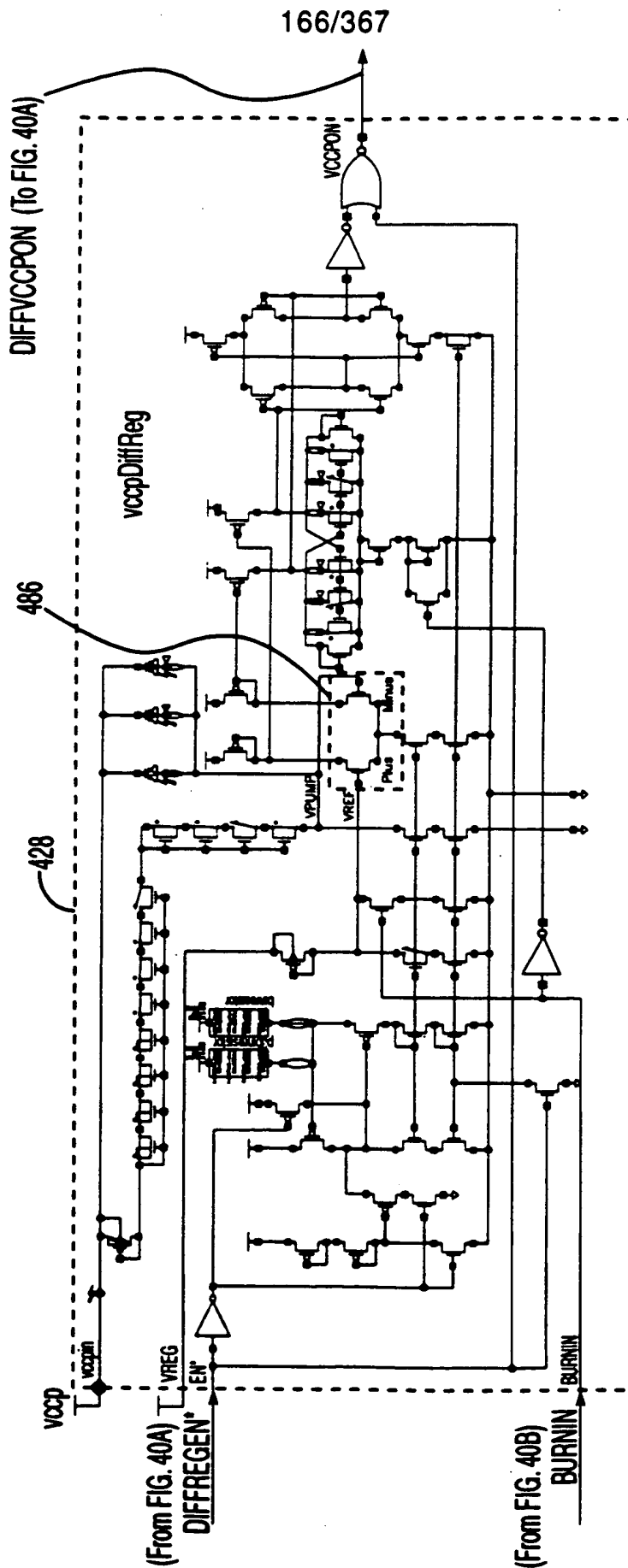
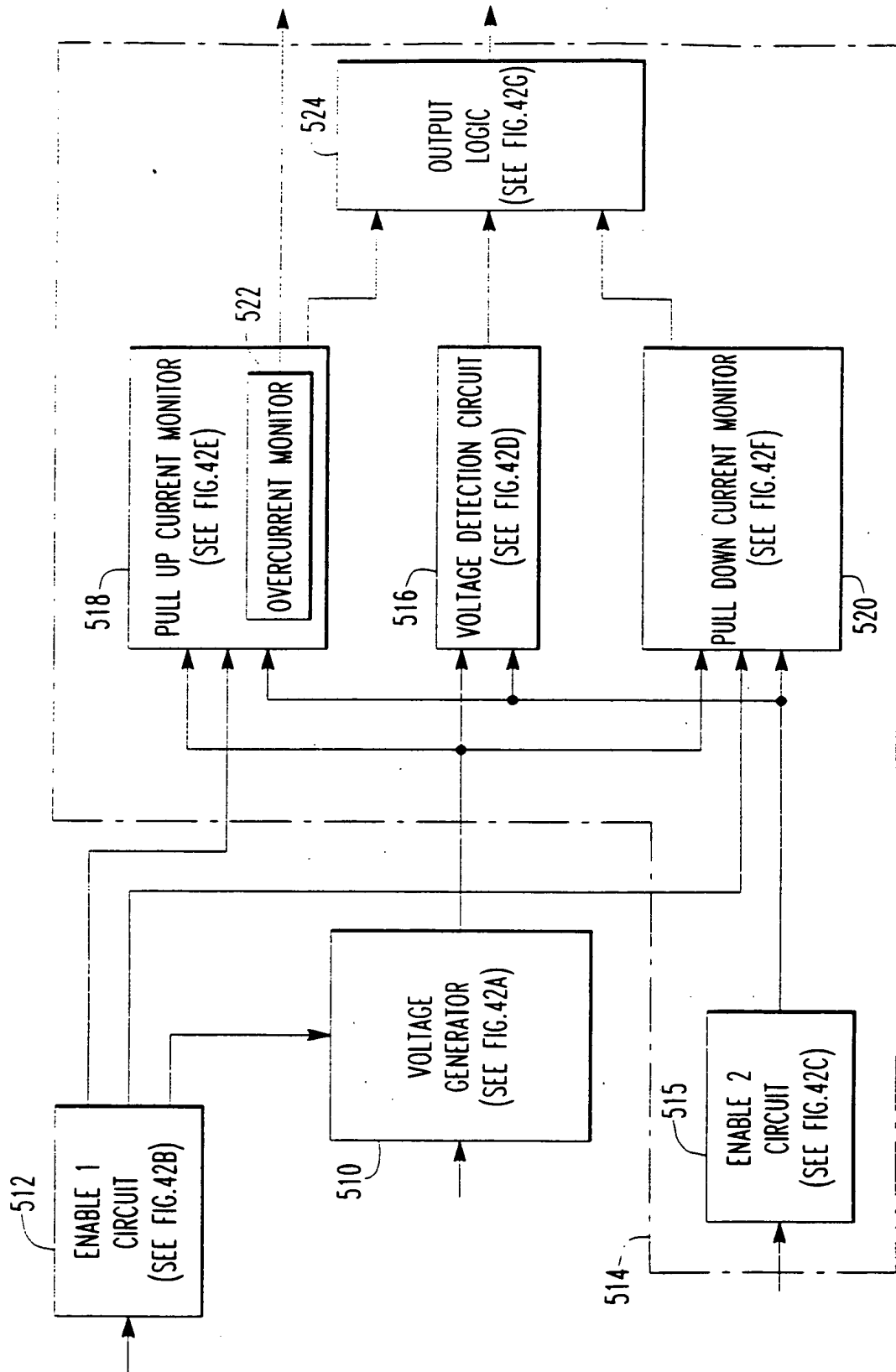


FIG. 40J



500

FIG. 41

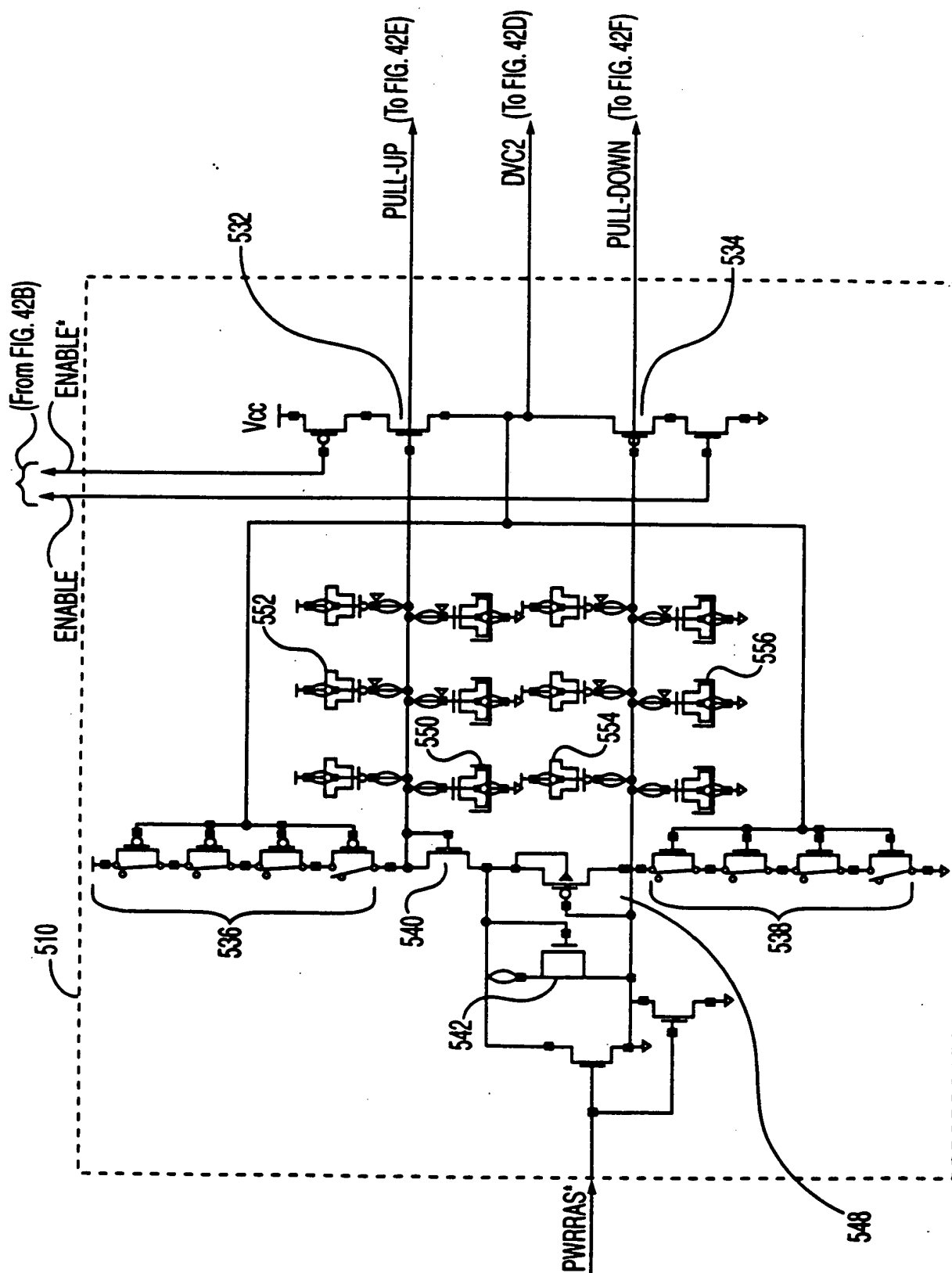


FIG. 42A

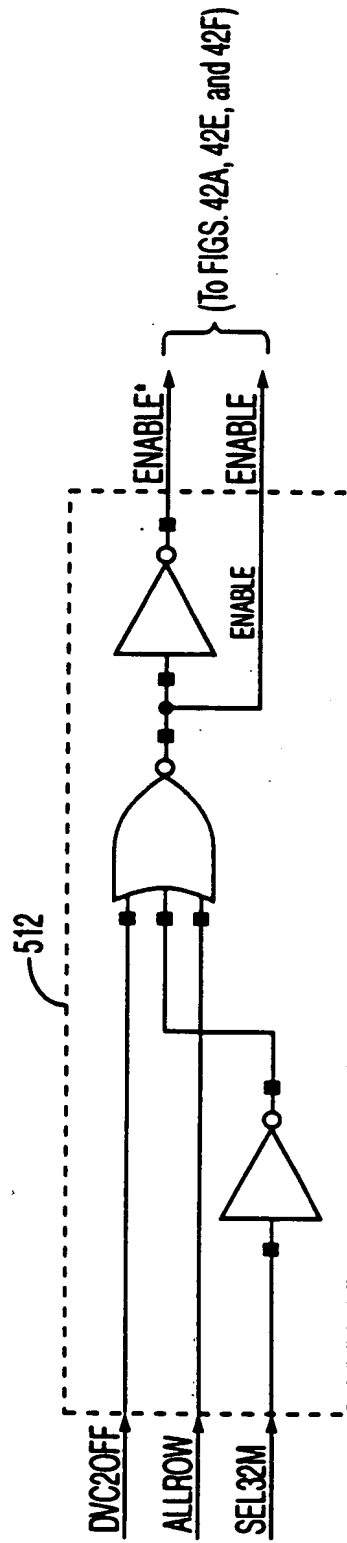


FIG. 42B

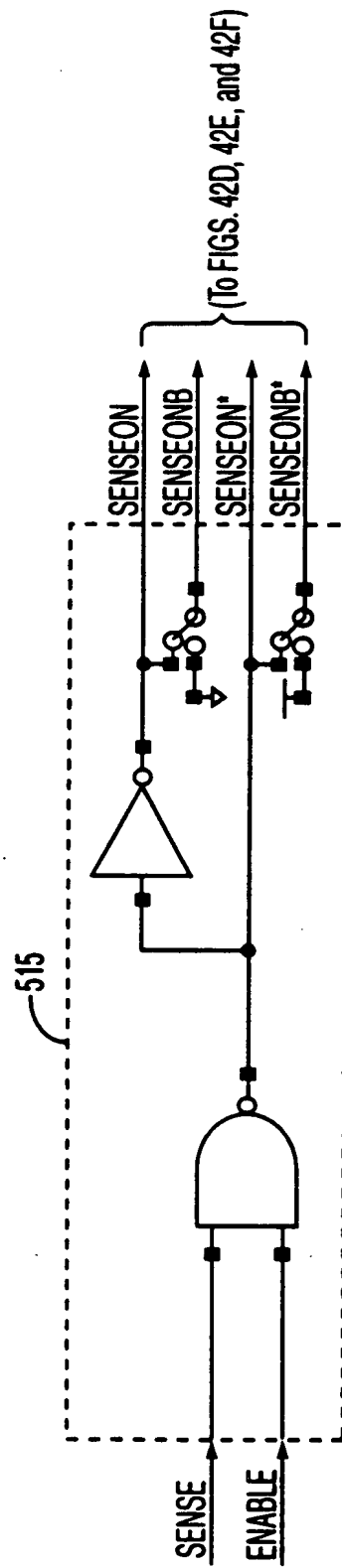


FIG. 42C

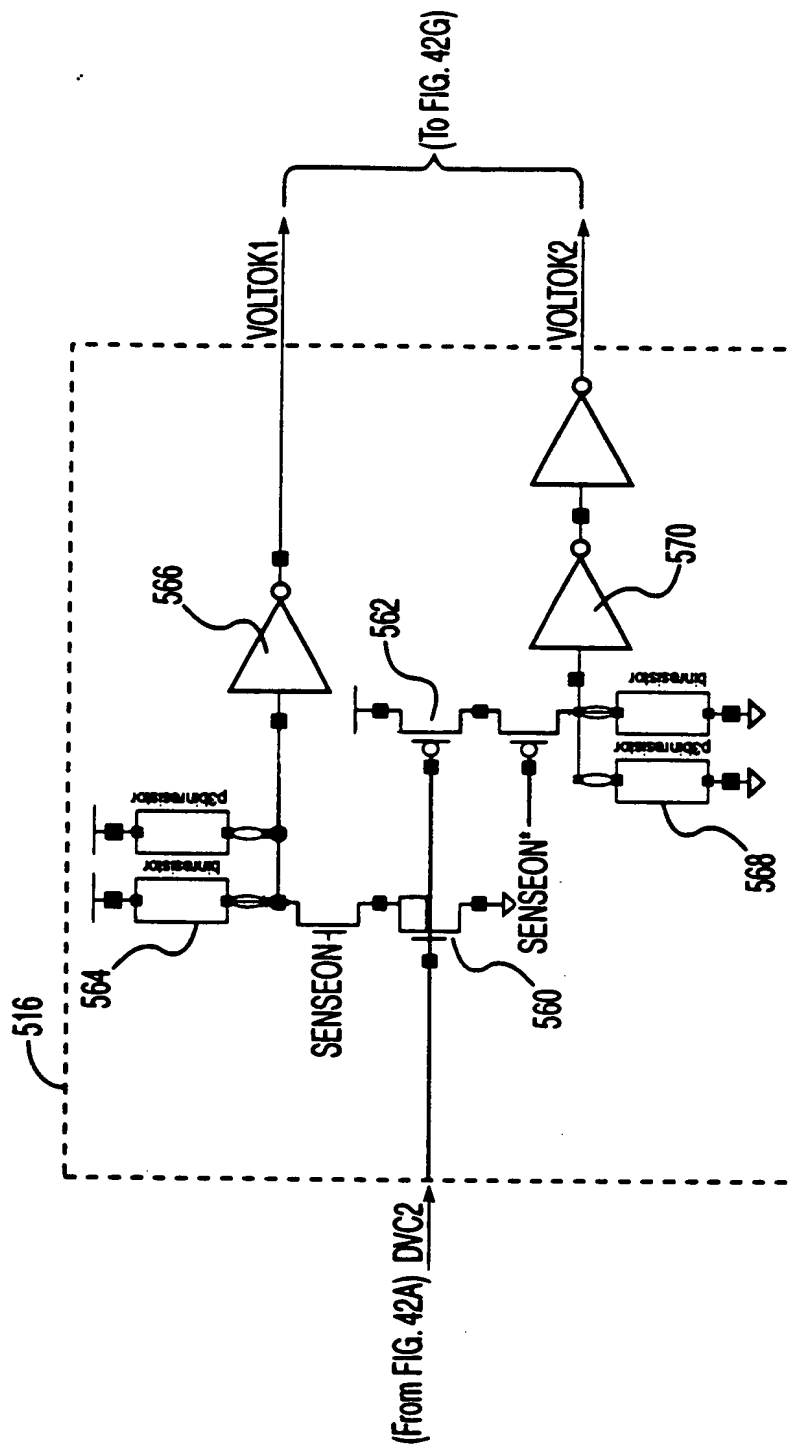


FIG. 42D

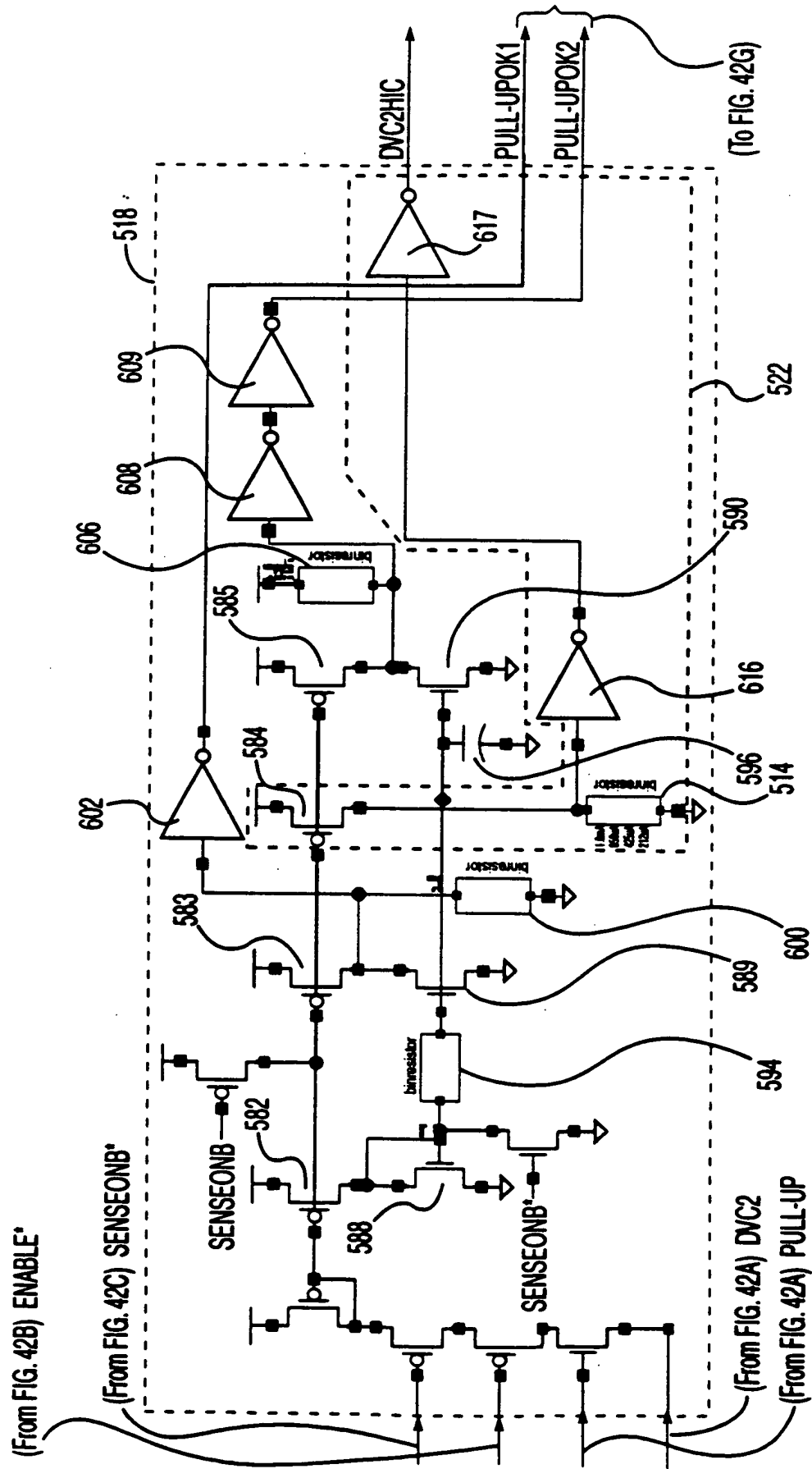


FIG. 42E

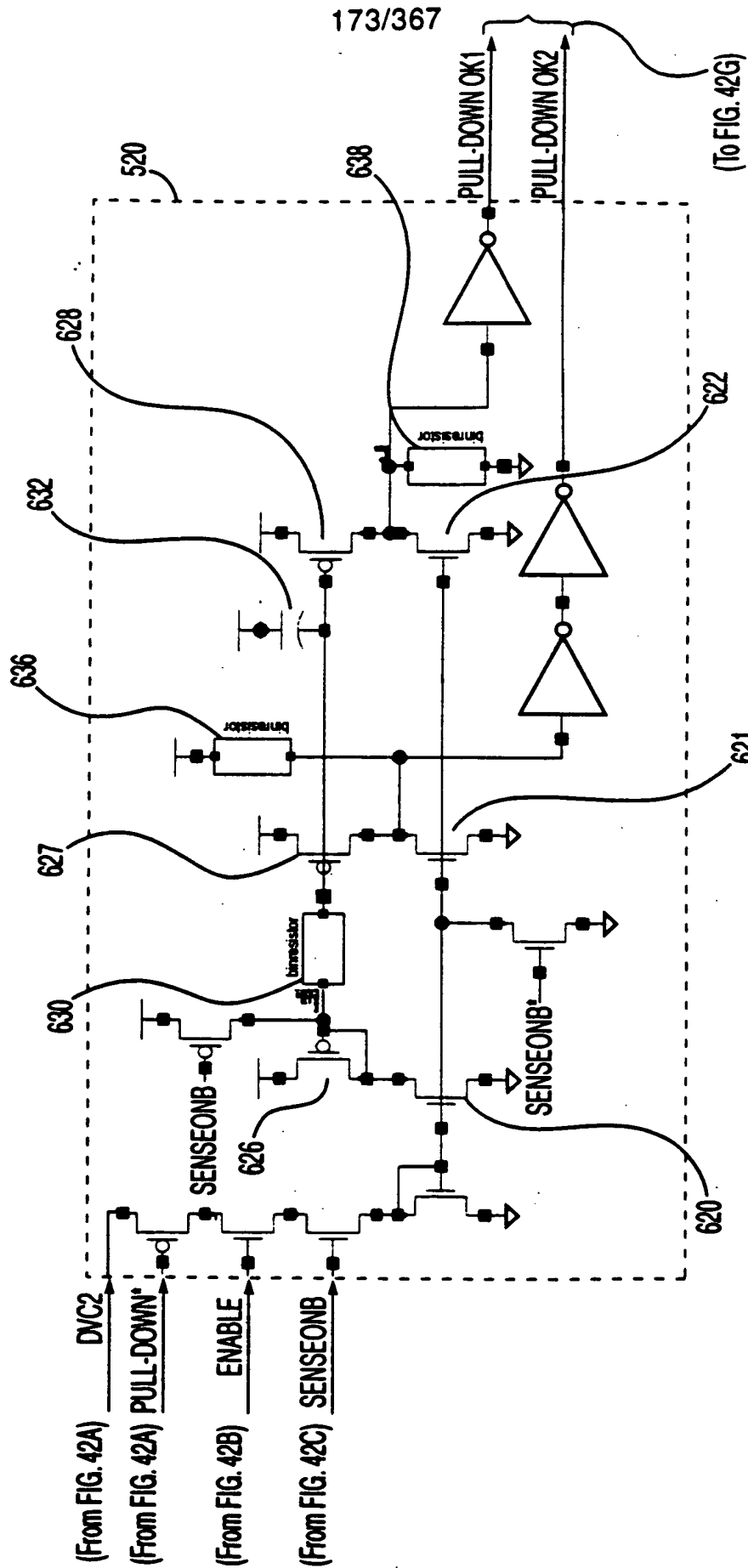


FIG. 42F

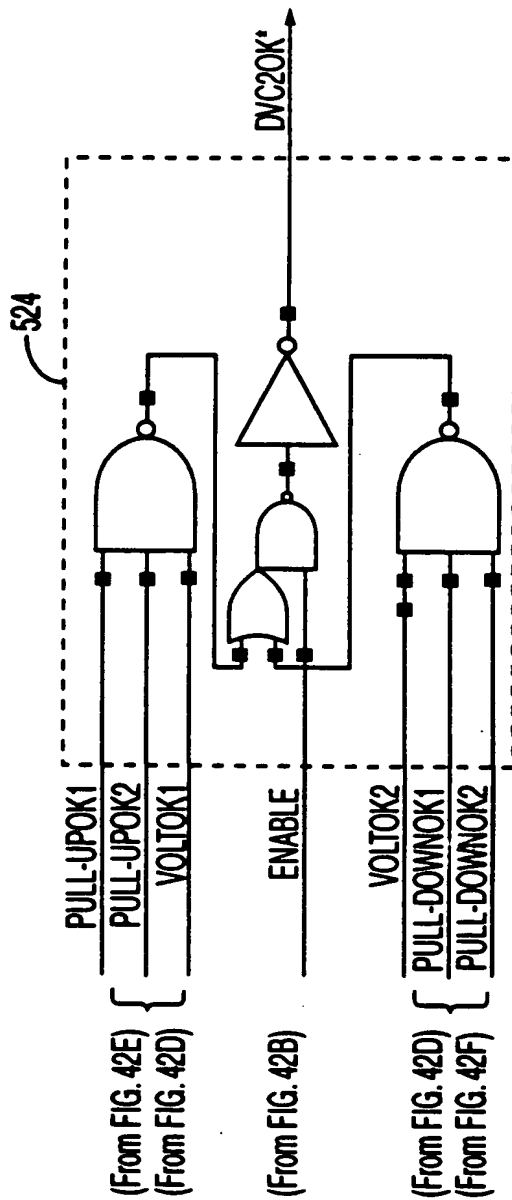


FIG. 42G

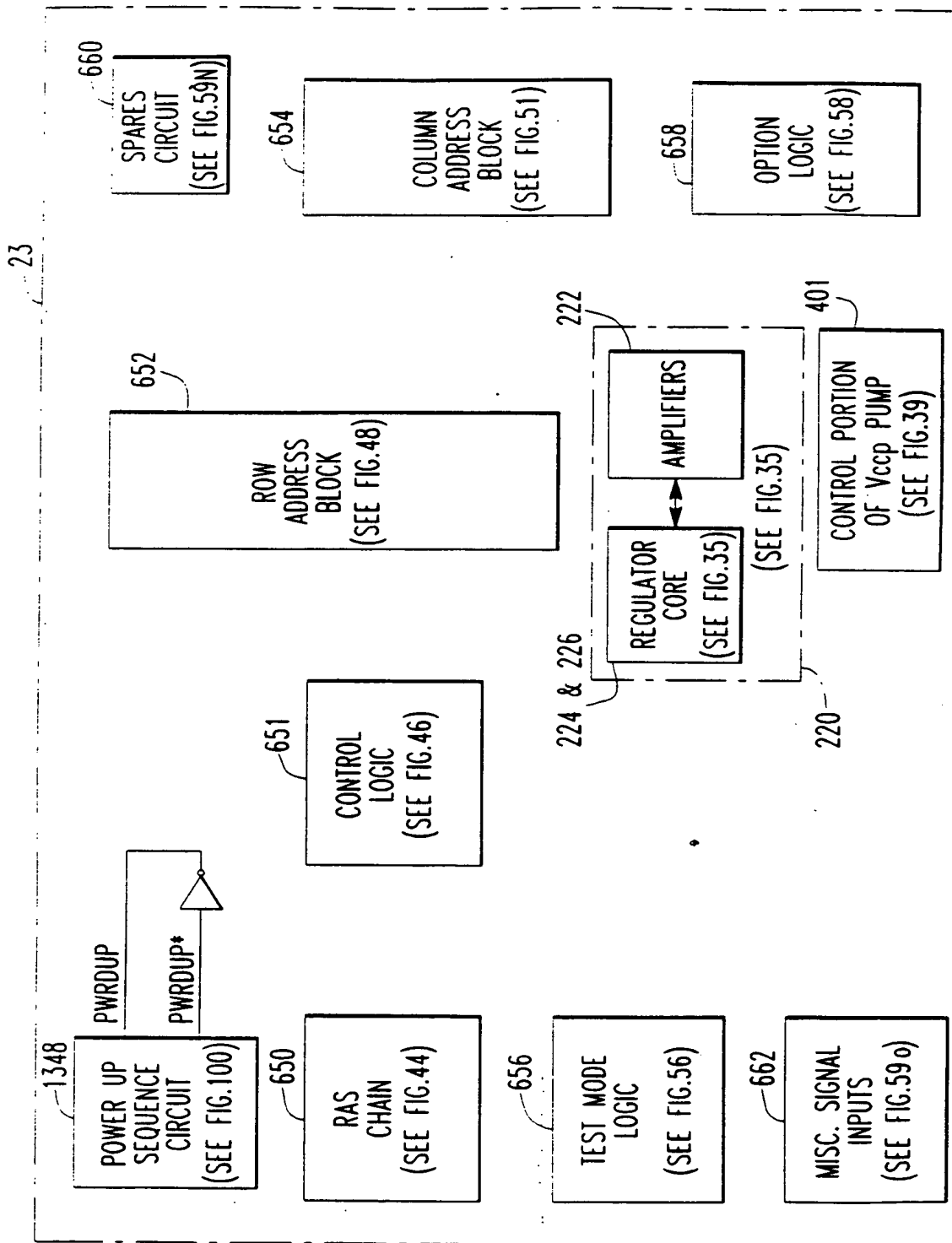


FIG. 43

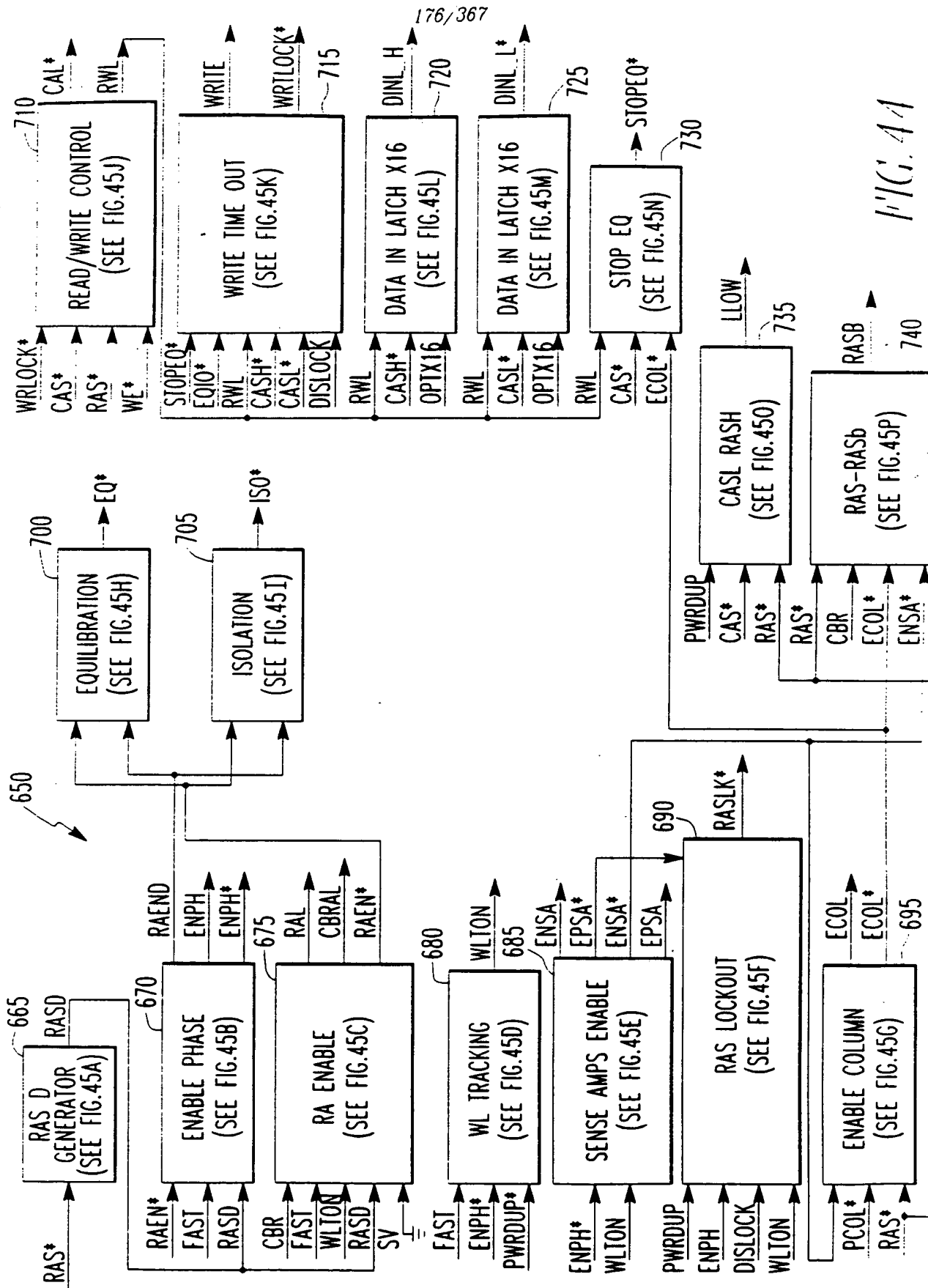


FIG. 44

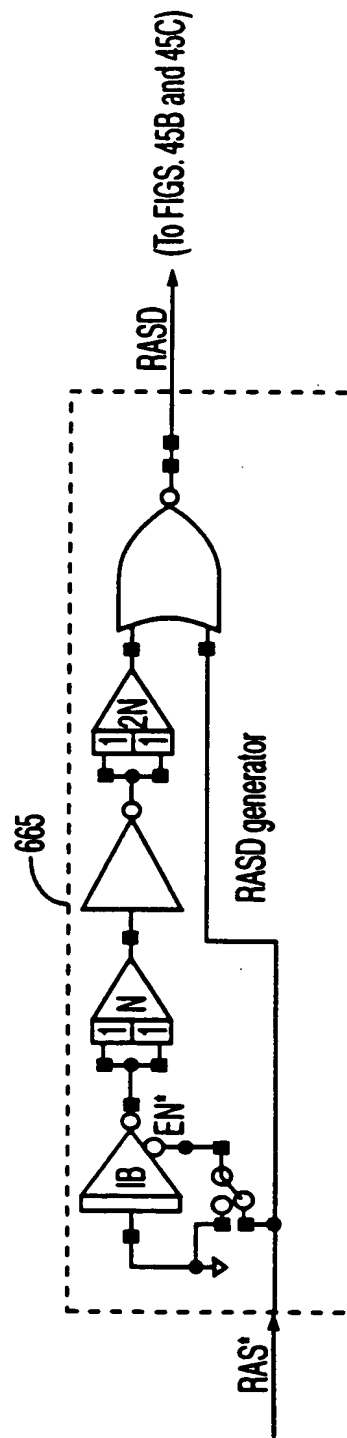


FIG. 45A

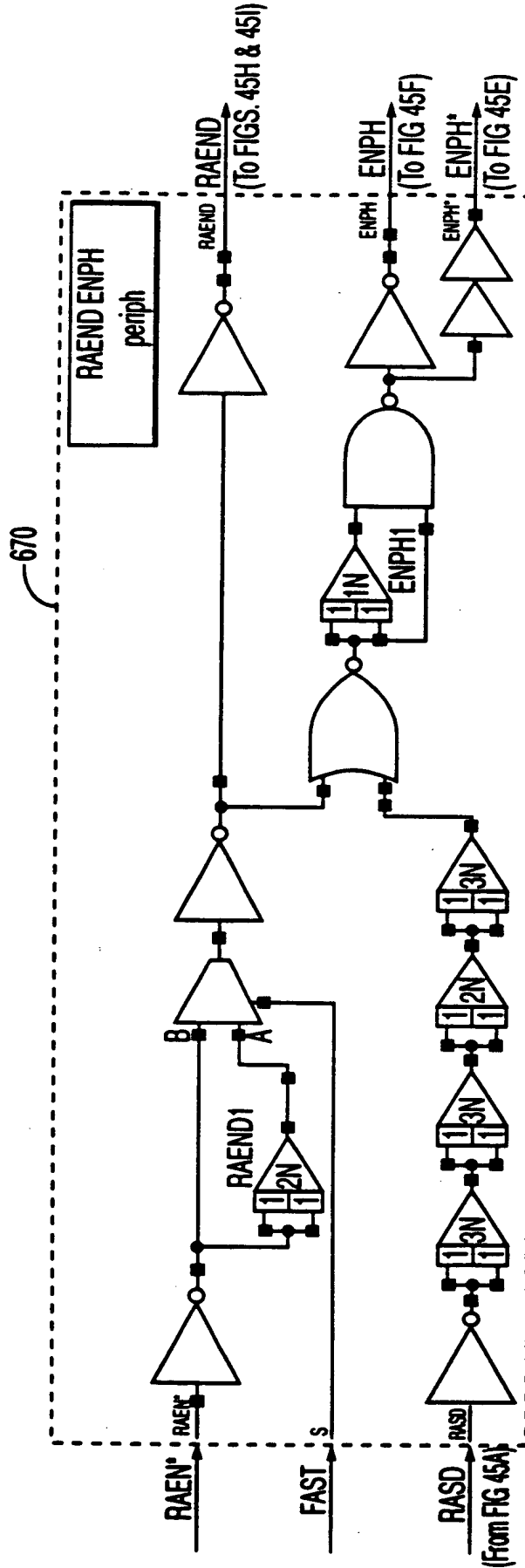
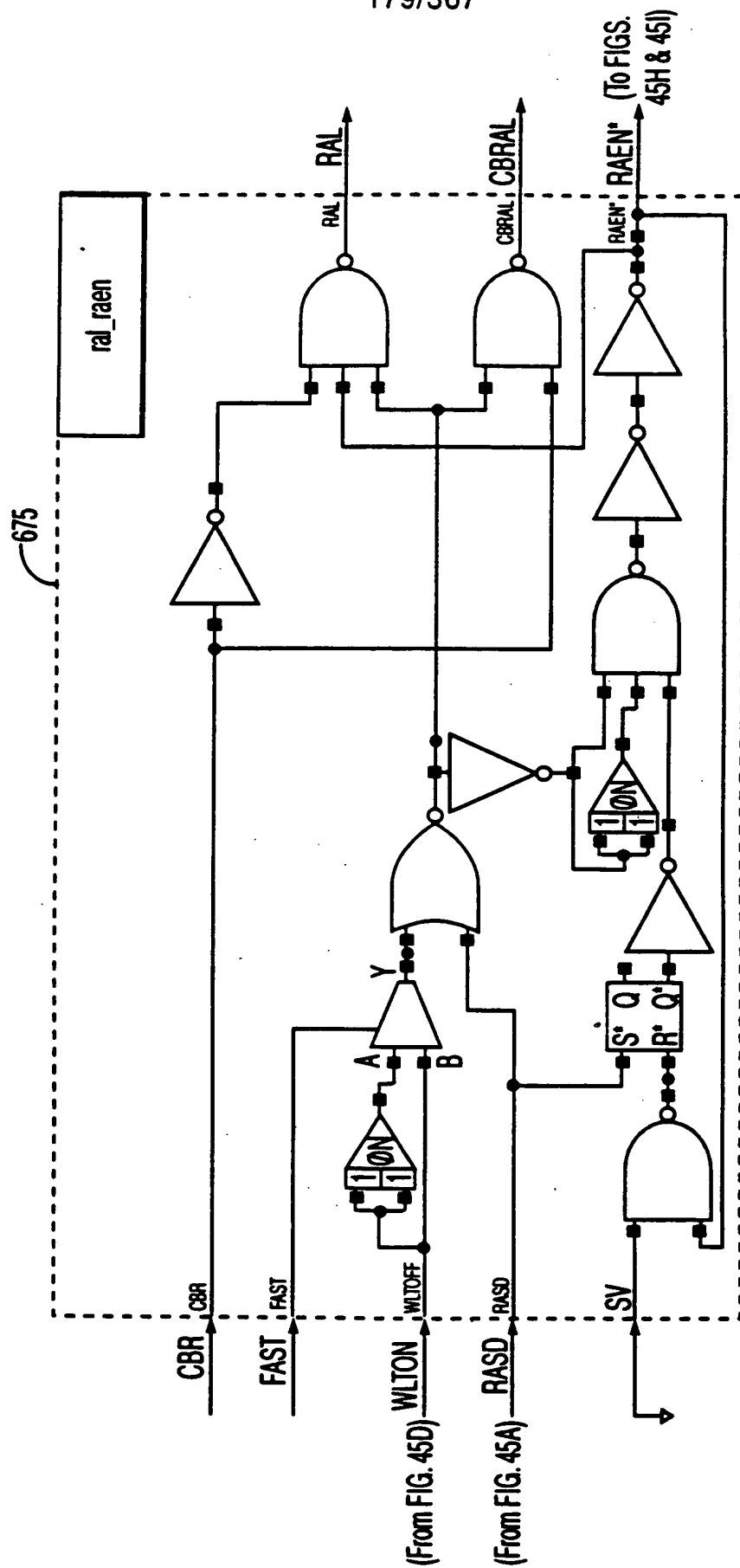
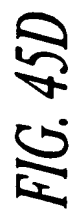


FIG. 45B





(From FIG. 45B)

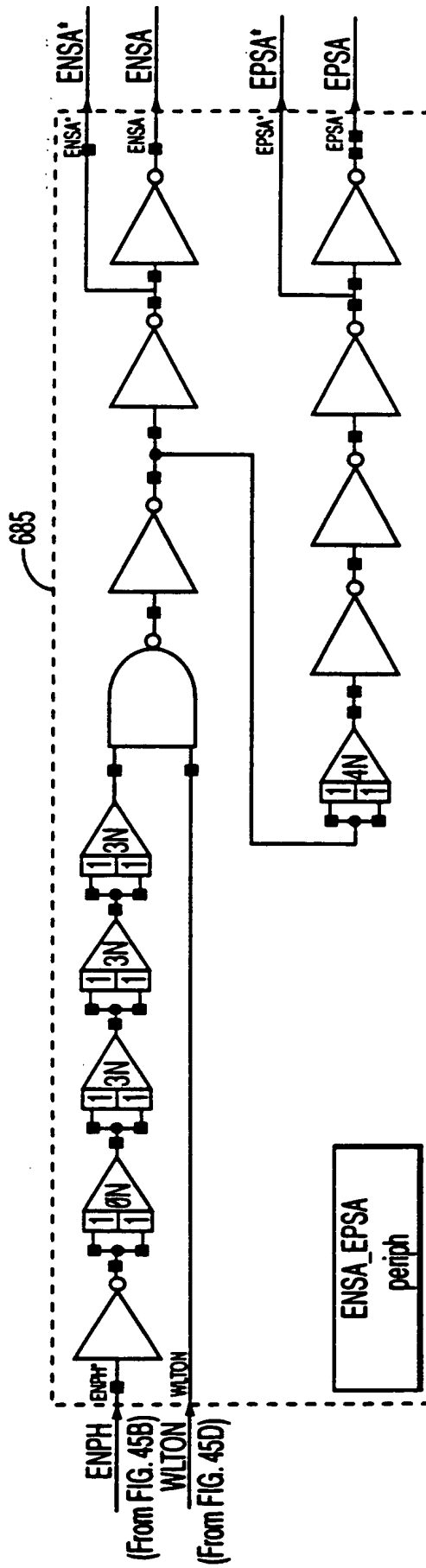


FIG. 45E

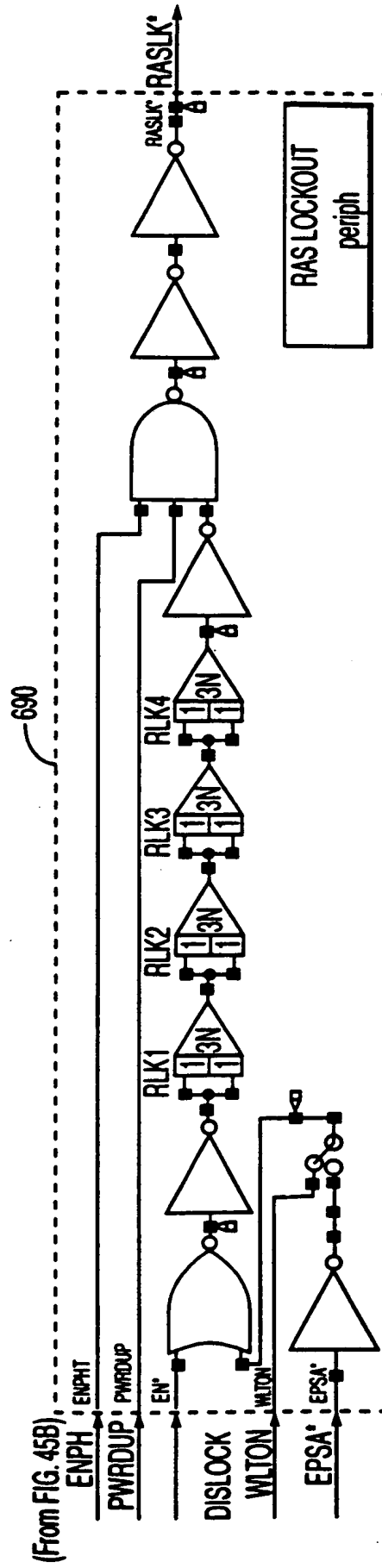


FIG. 45F

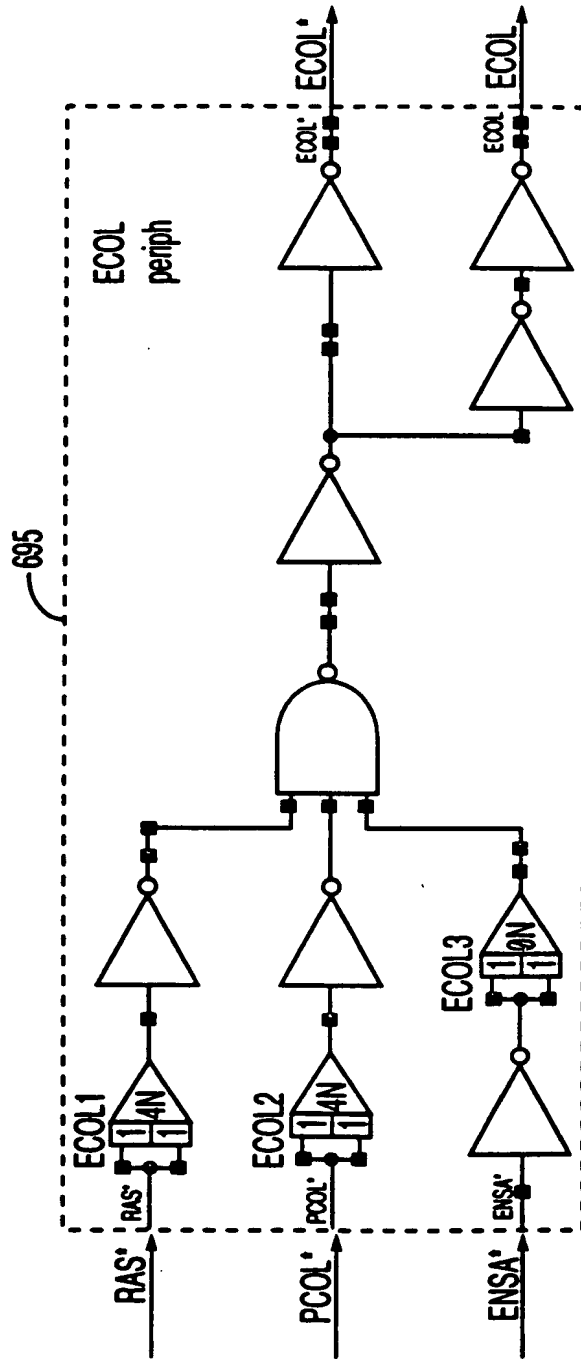


FIG. 45G

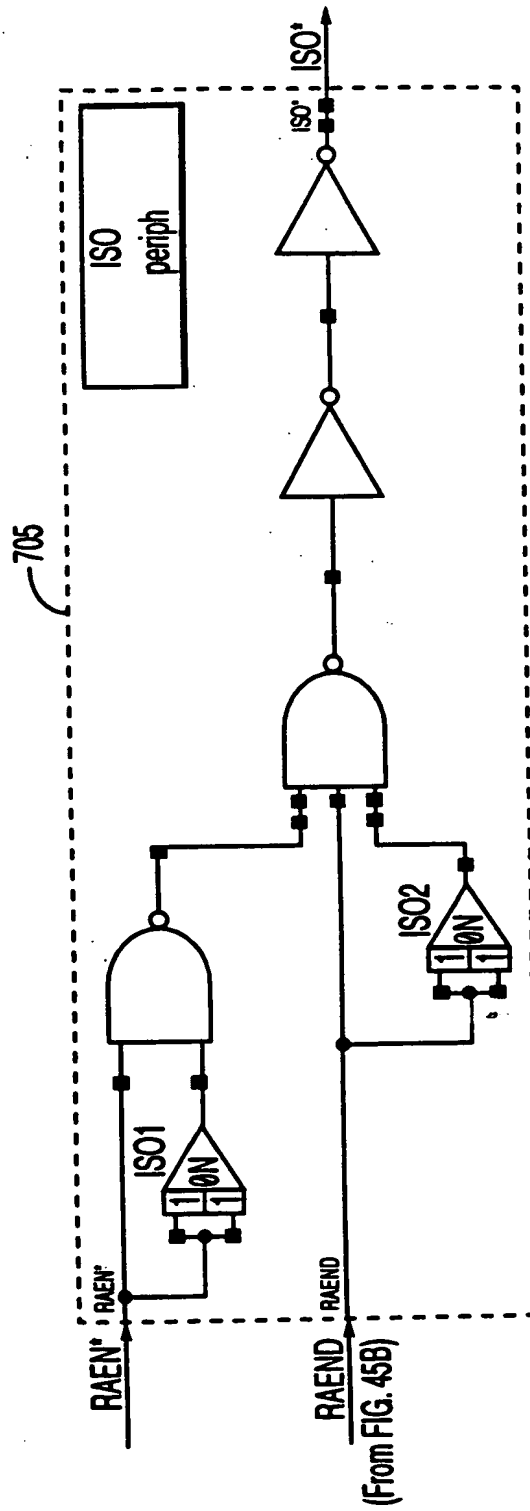


FIG. 45I



FIG. 45J

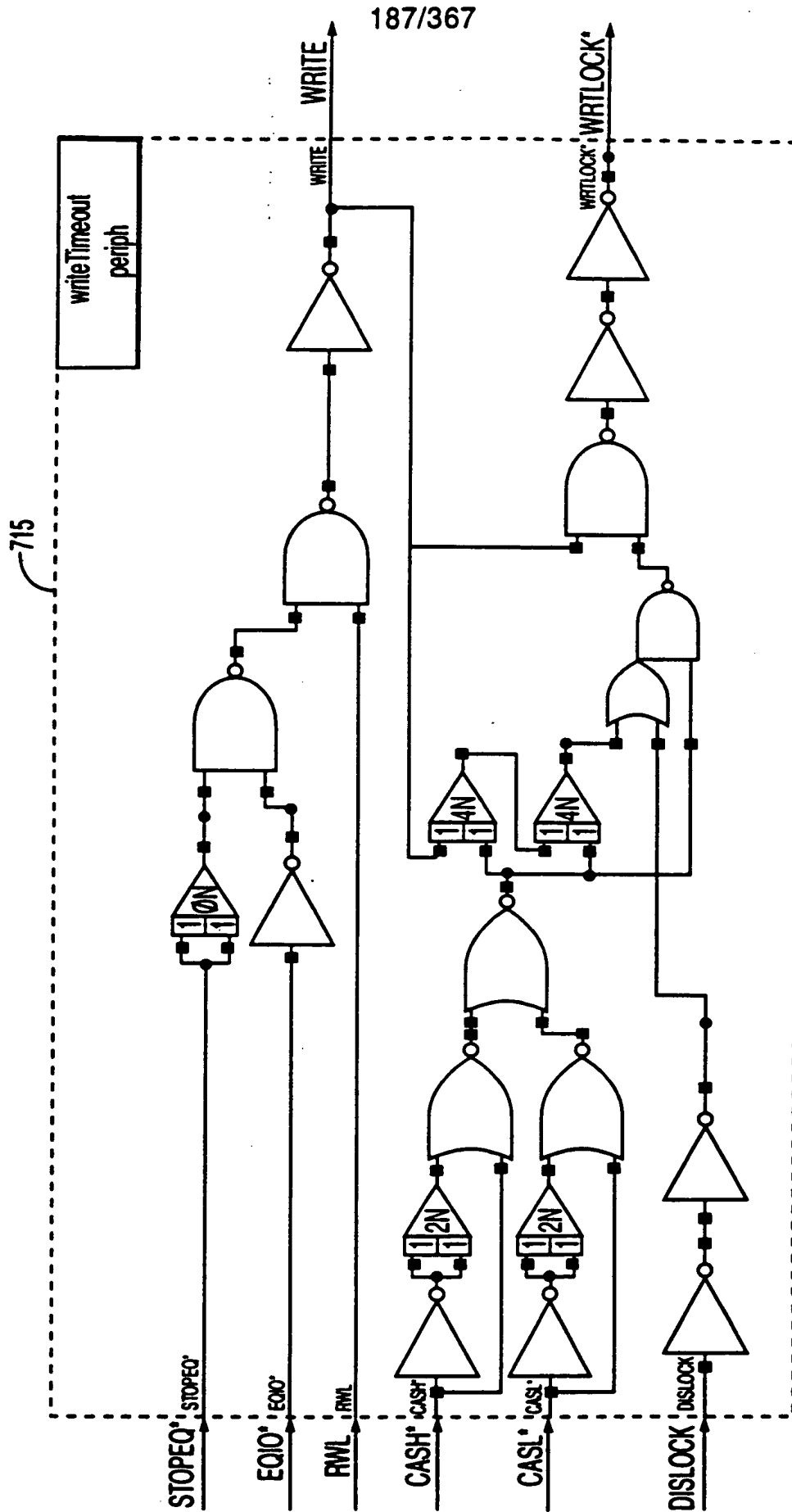


FIG. 45K

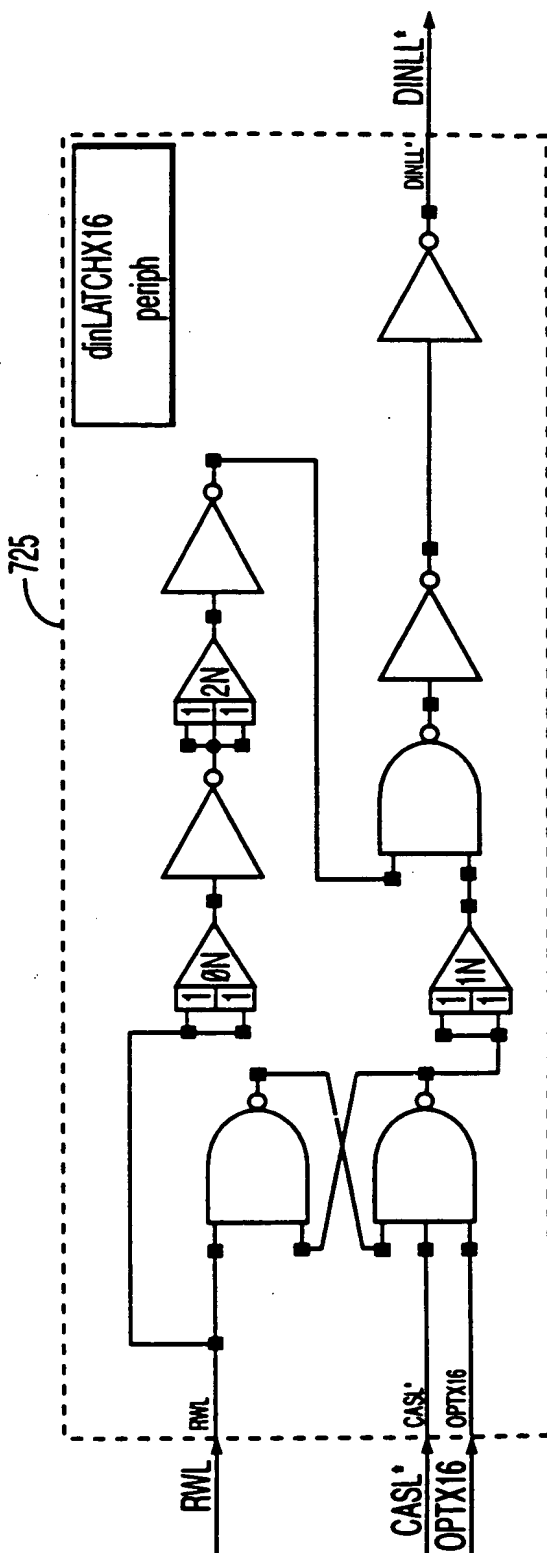


FIG. 45M

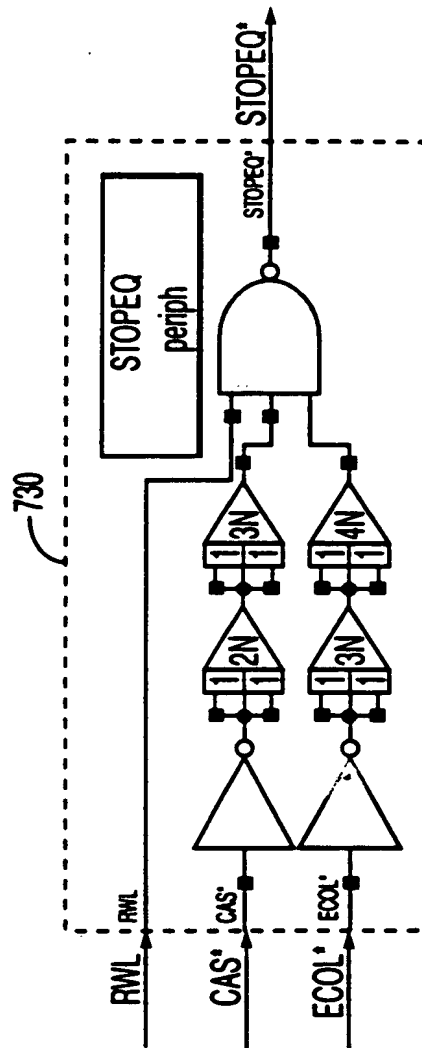


FIG. 45N



FIG. 450

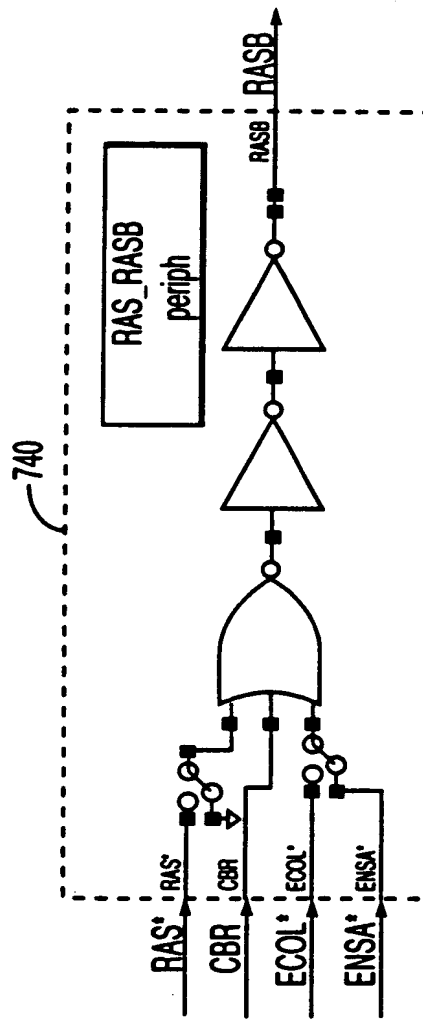


FIG. 45P

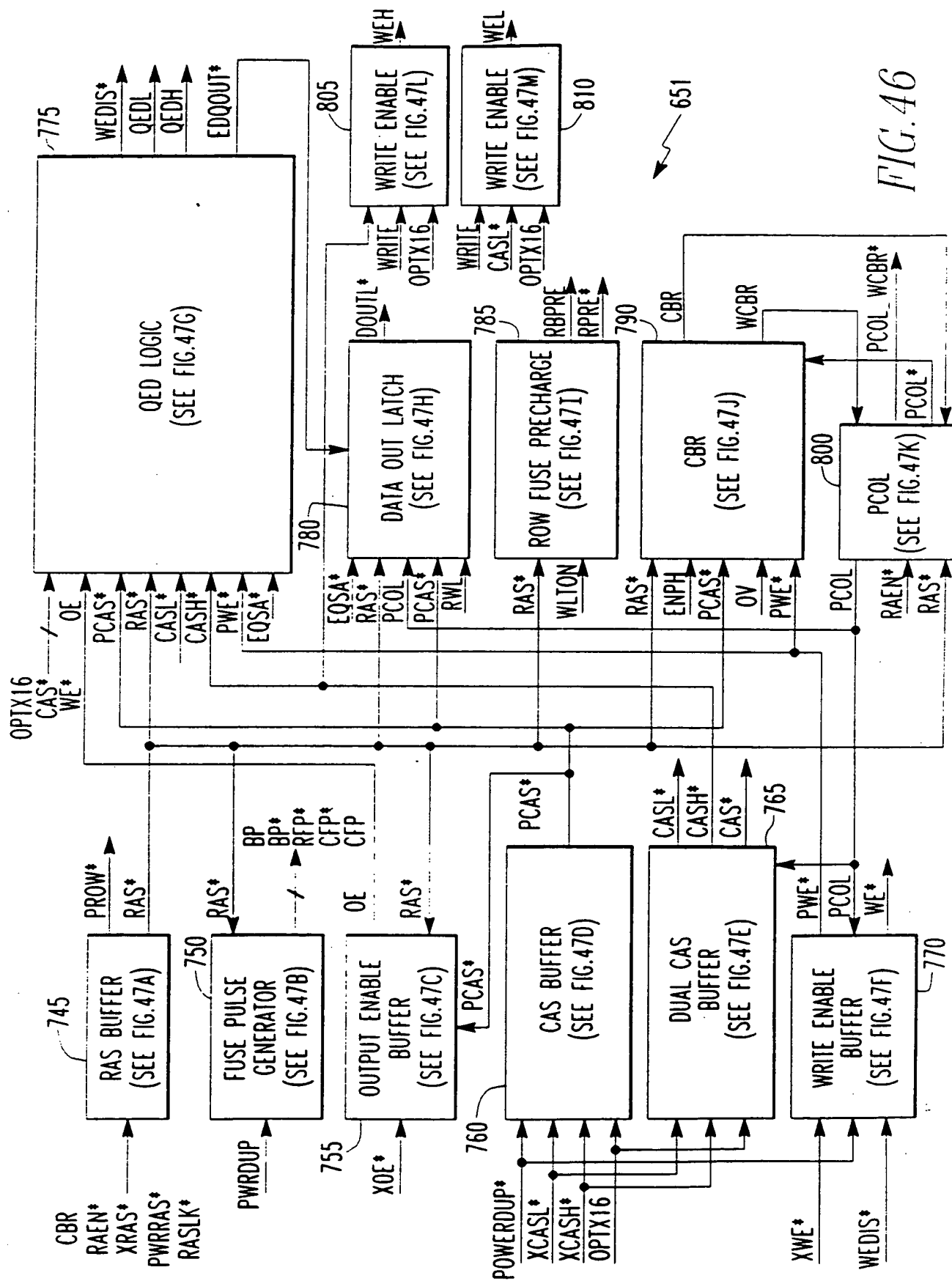


FIG. 46

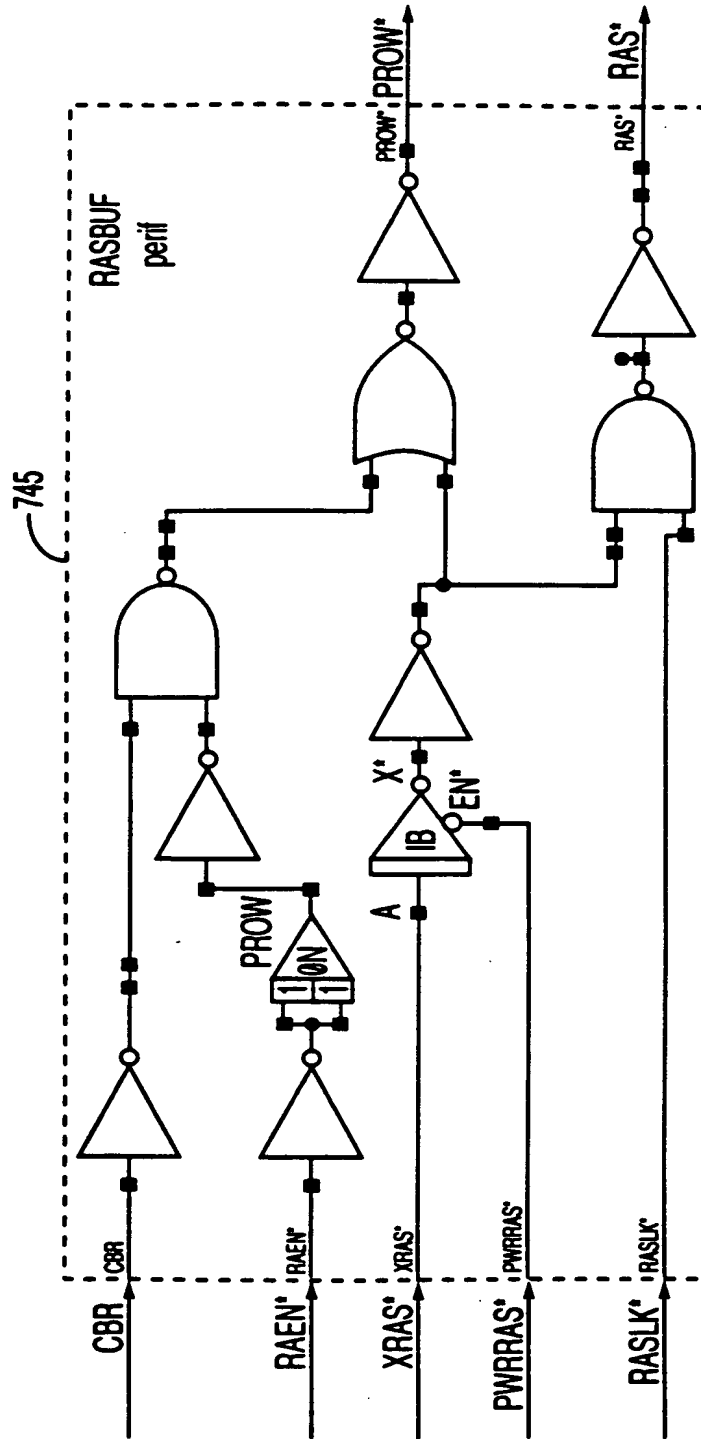


FIG. 47A

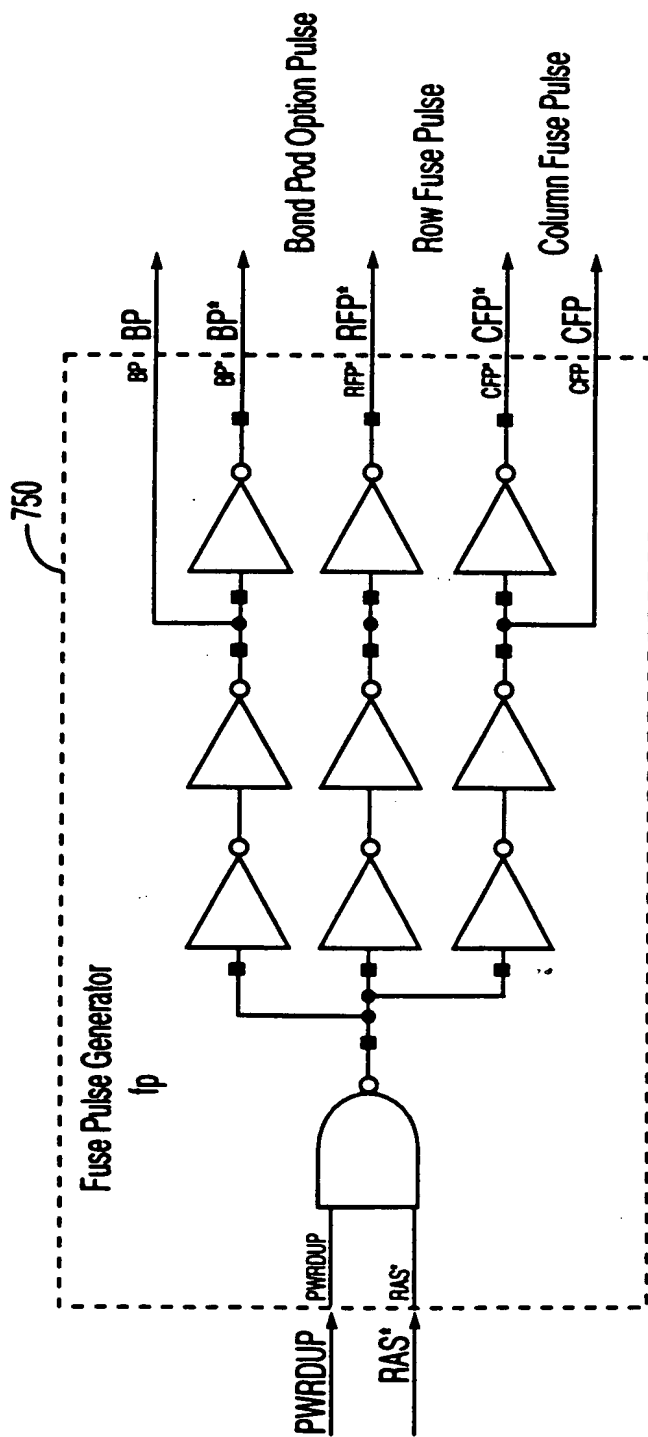


FIG. 47B

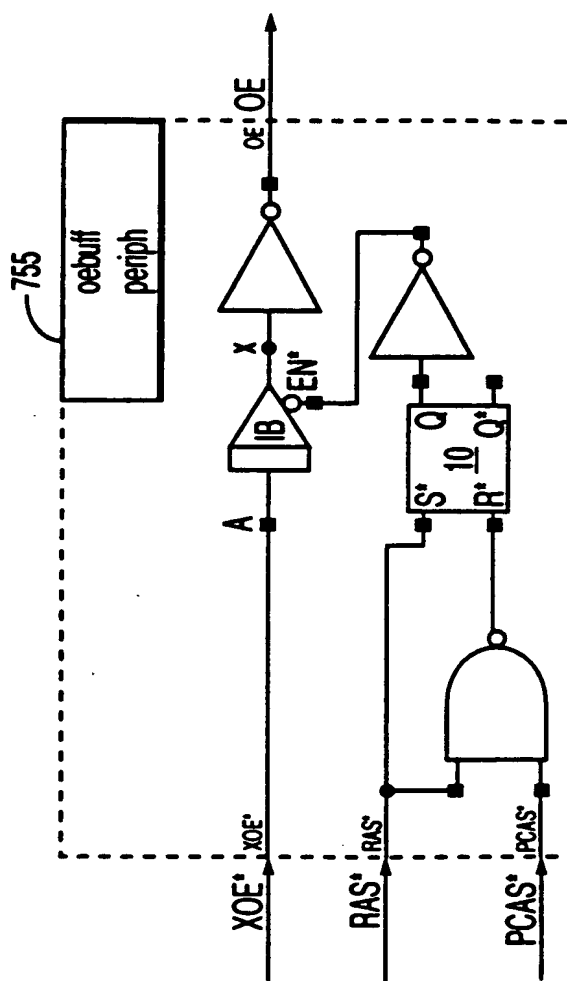


FIG. 47C

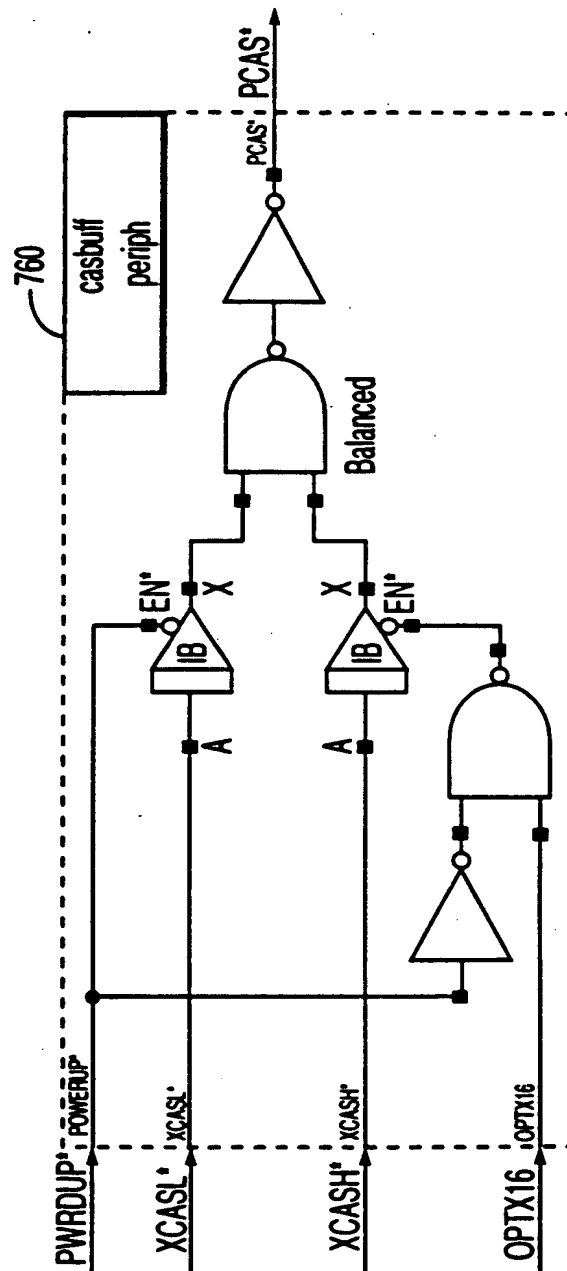


FIG. 47D

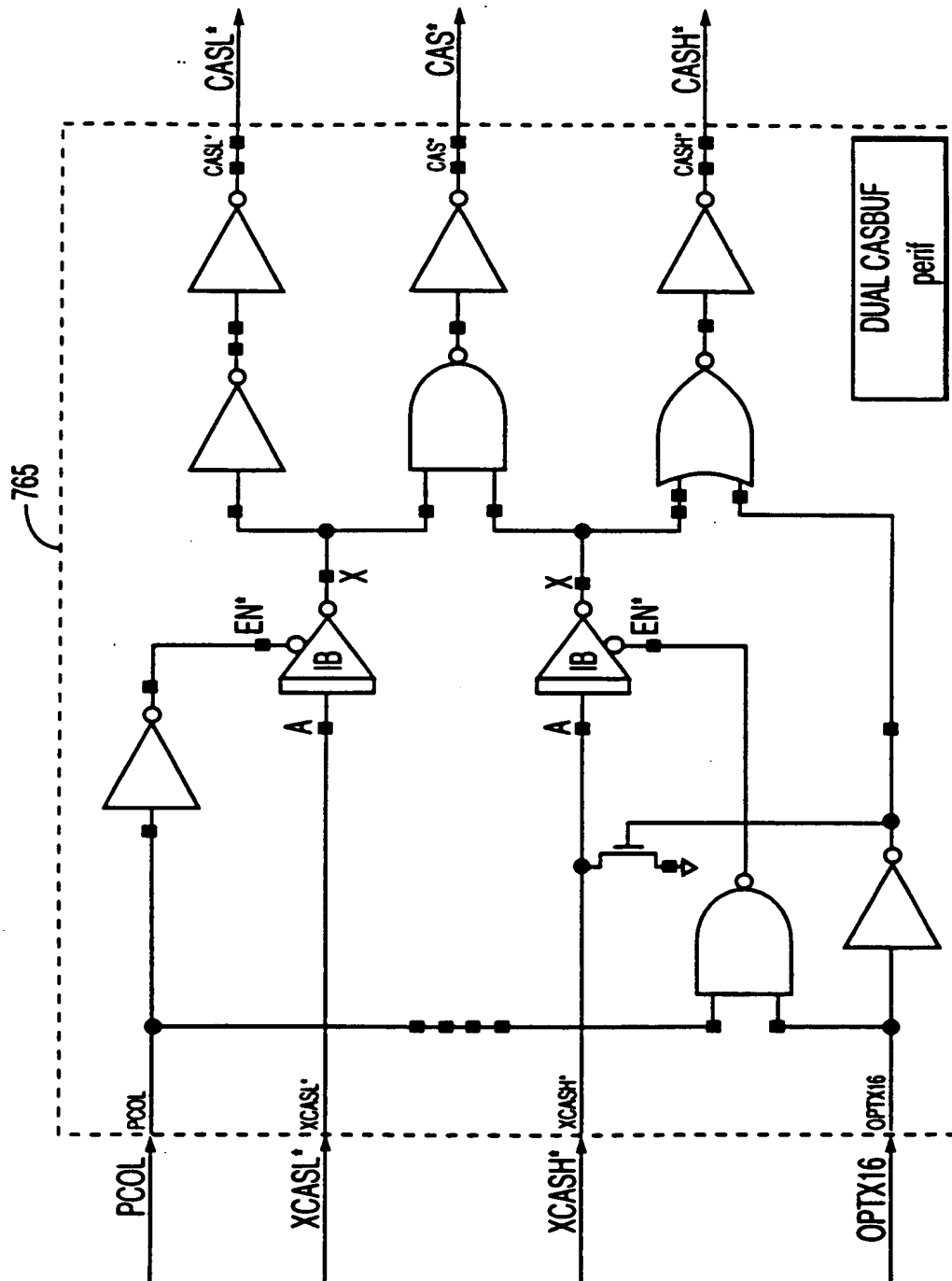


FIG. 47E

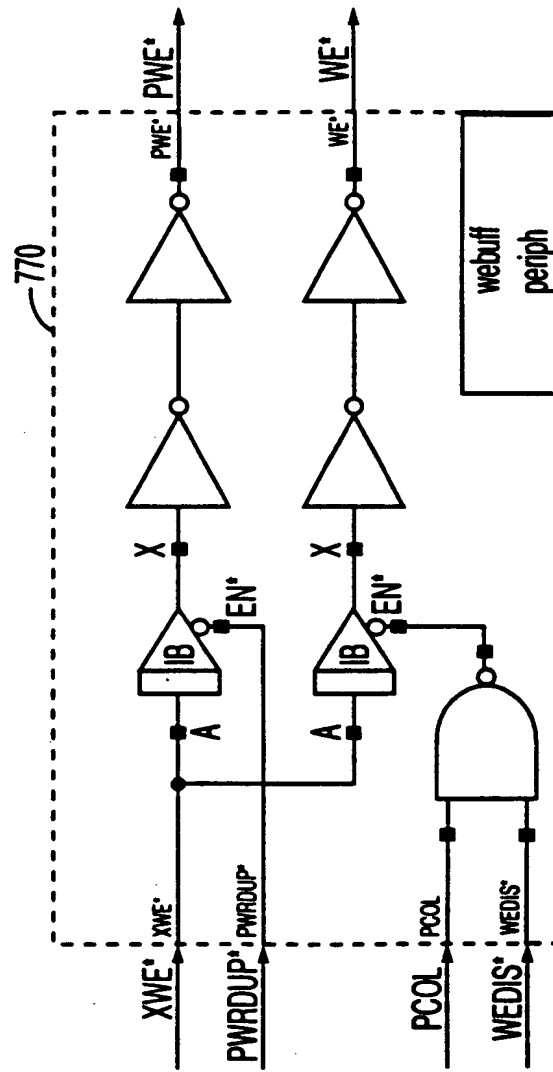


FIG. 47F

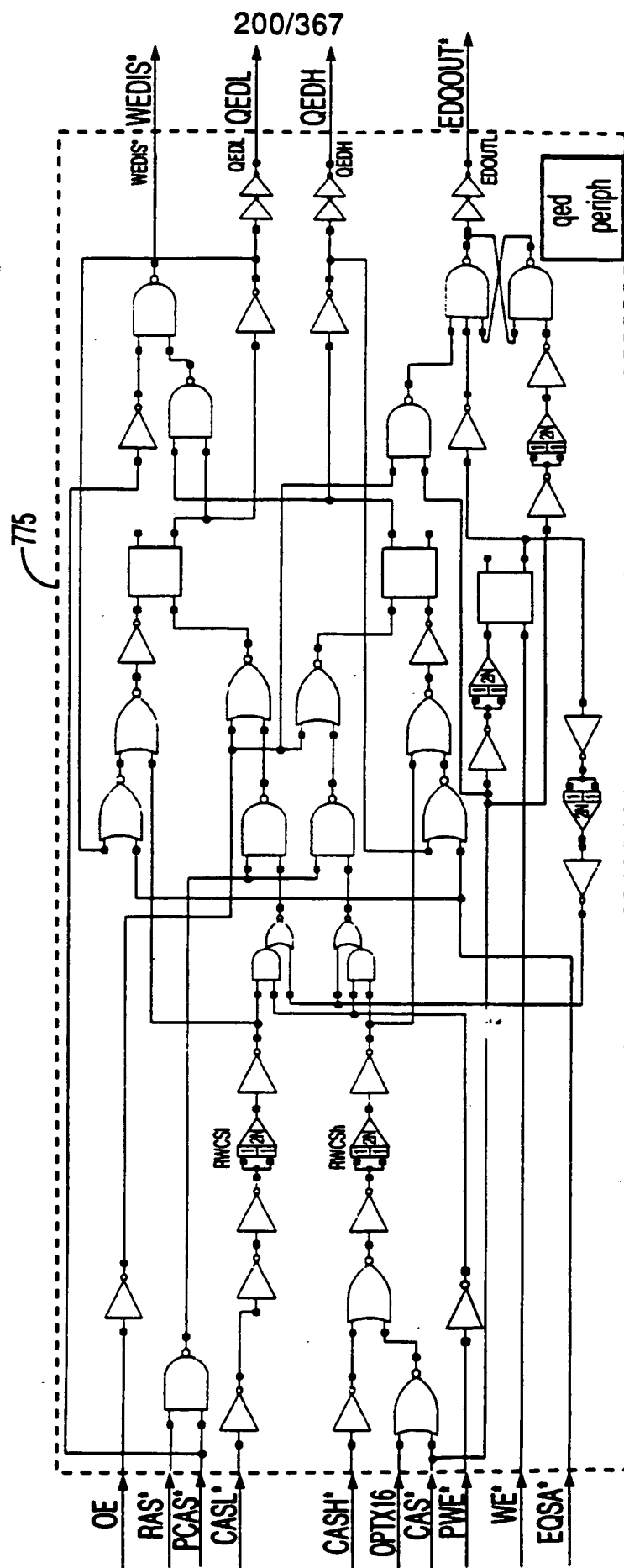


FIG. 47G

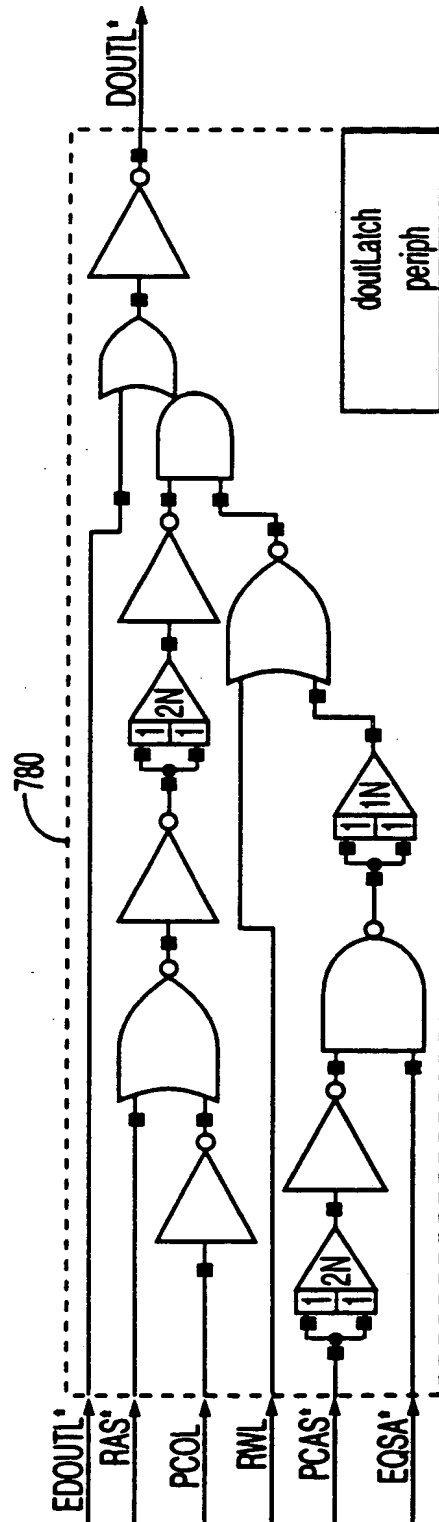
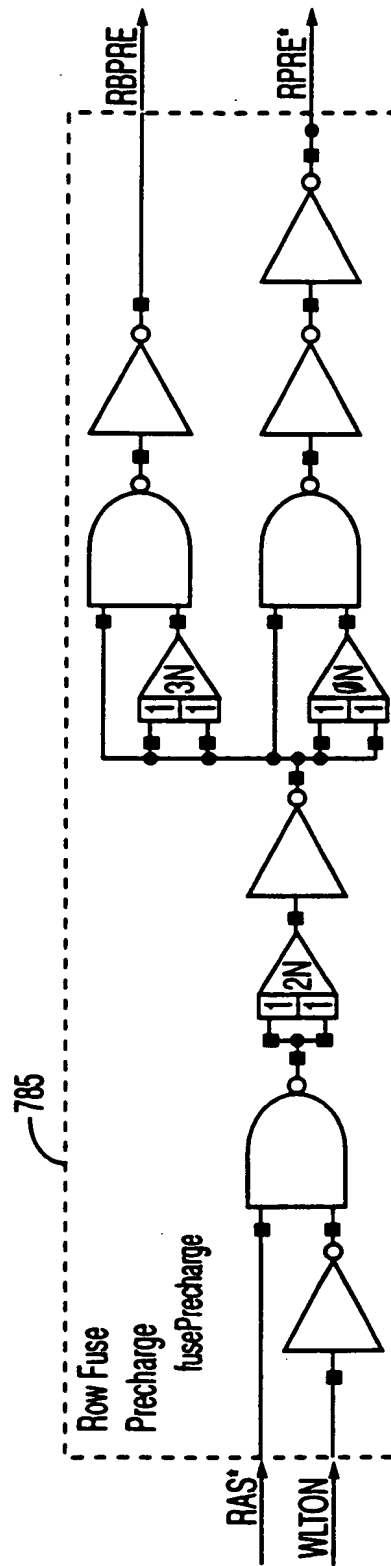


FIG. 47H



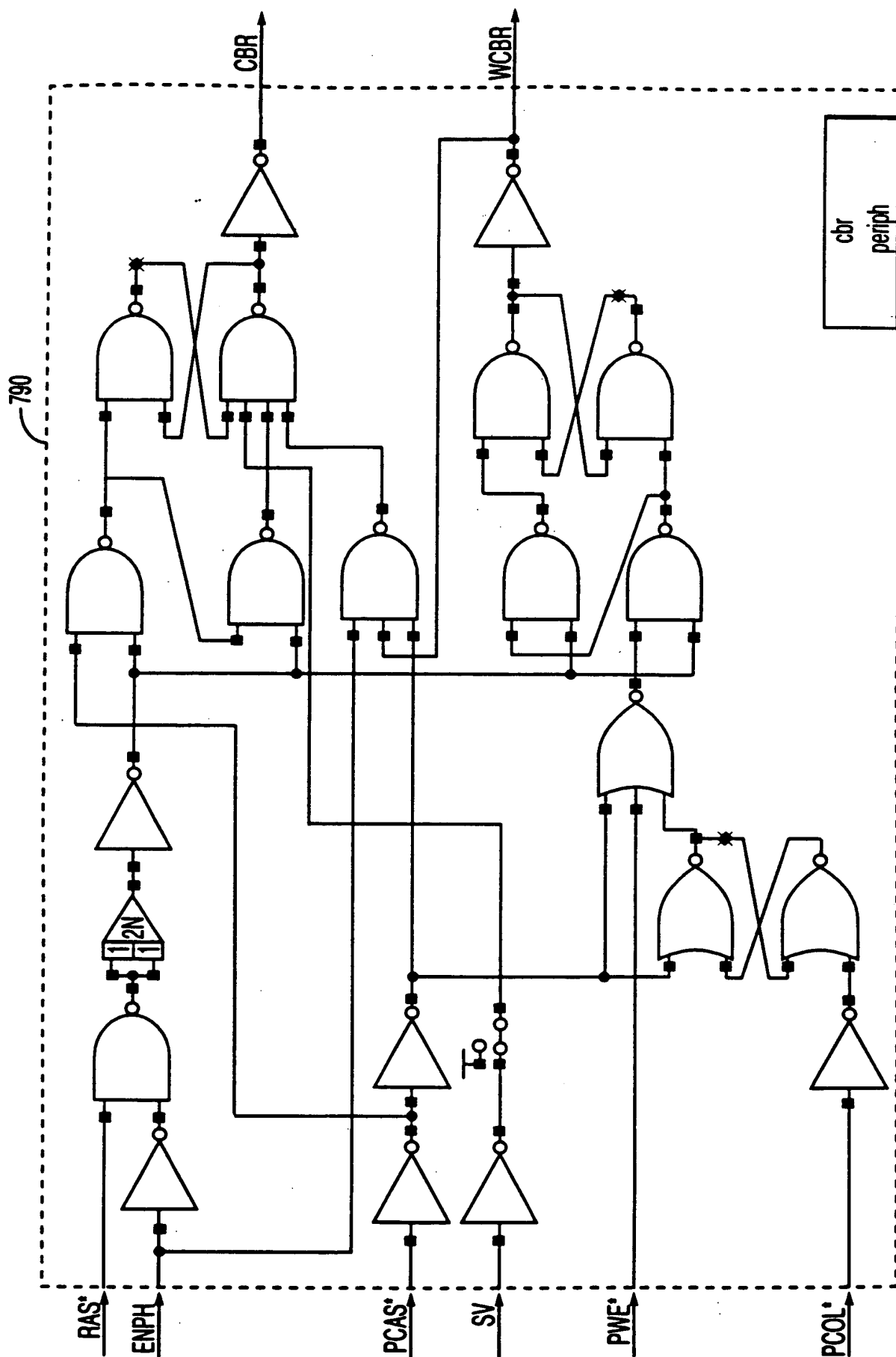


FIG. 47J



FIG. 47K

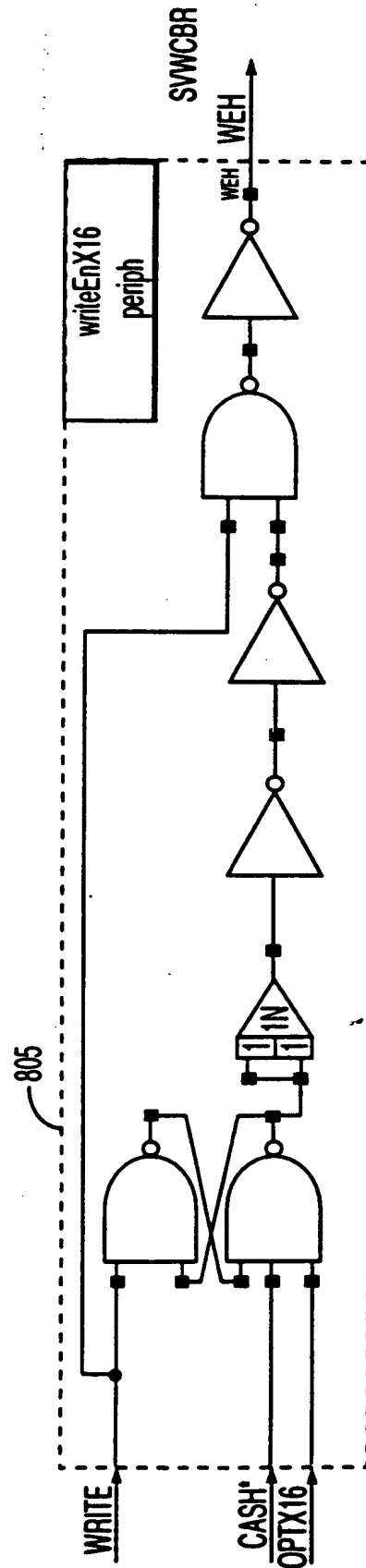


FIG. 47L



FIG. 47M

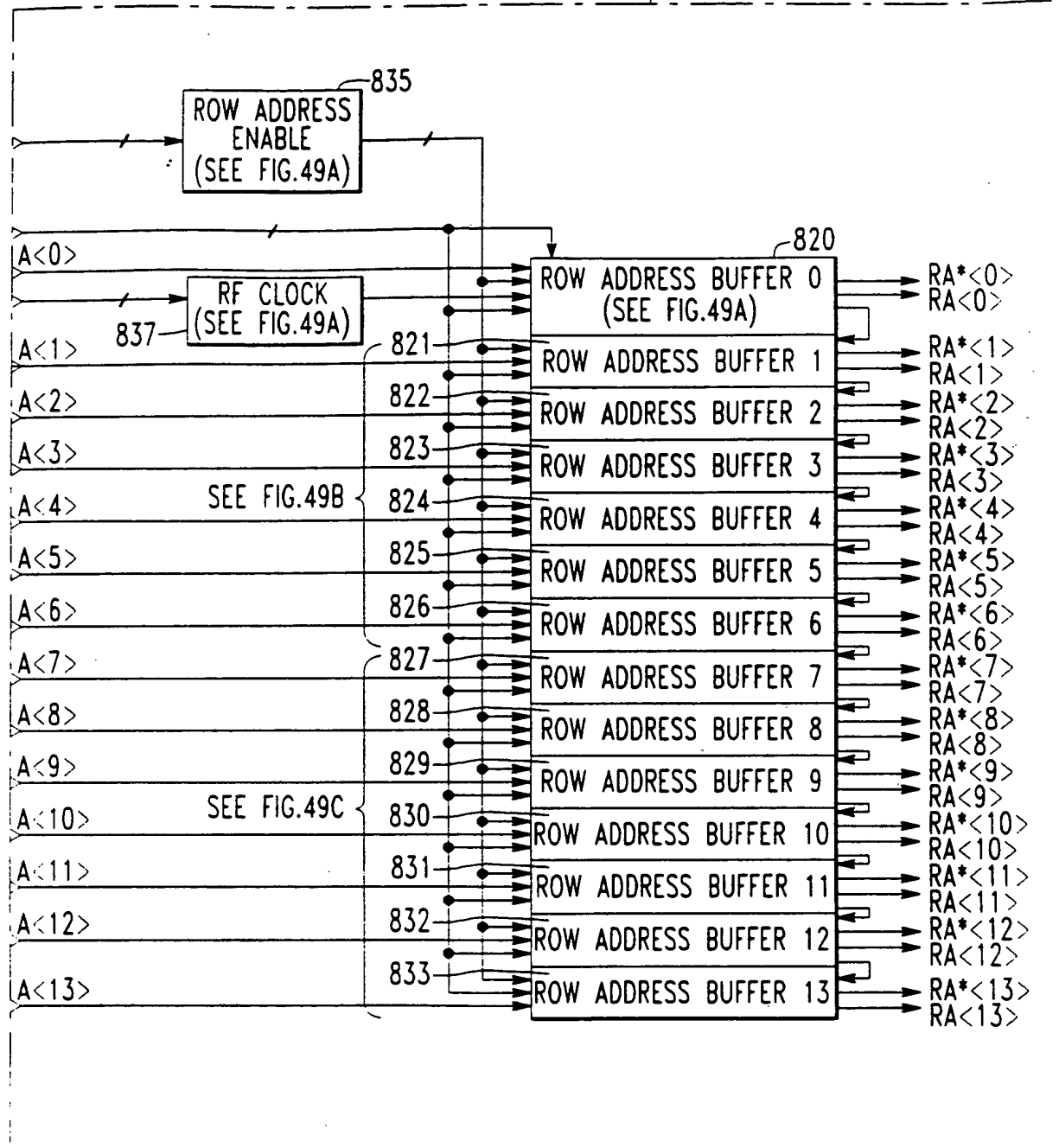


FIG. 48A

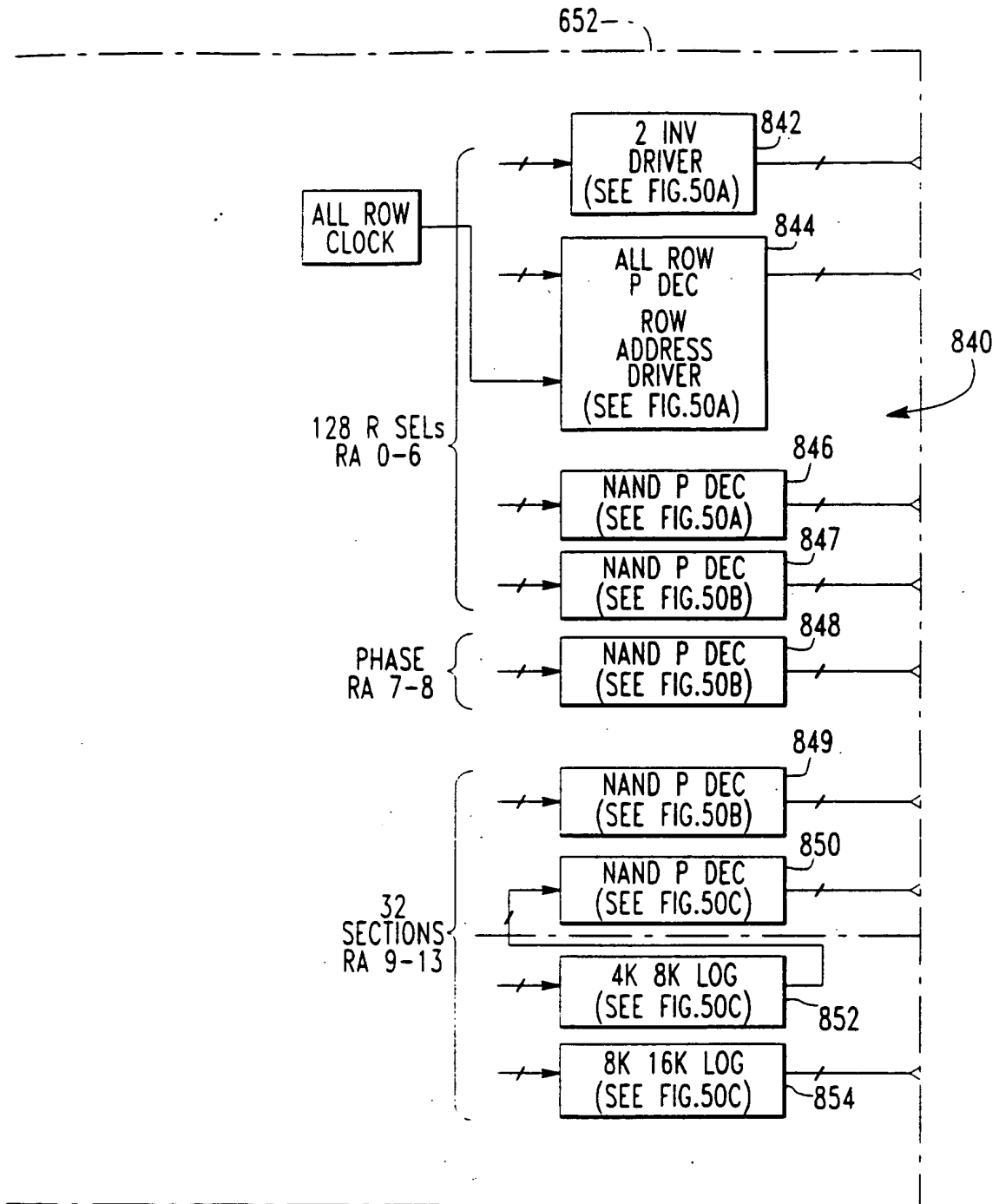


FIG. 48B

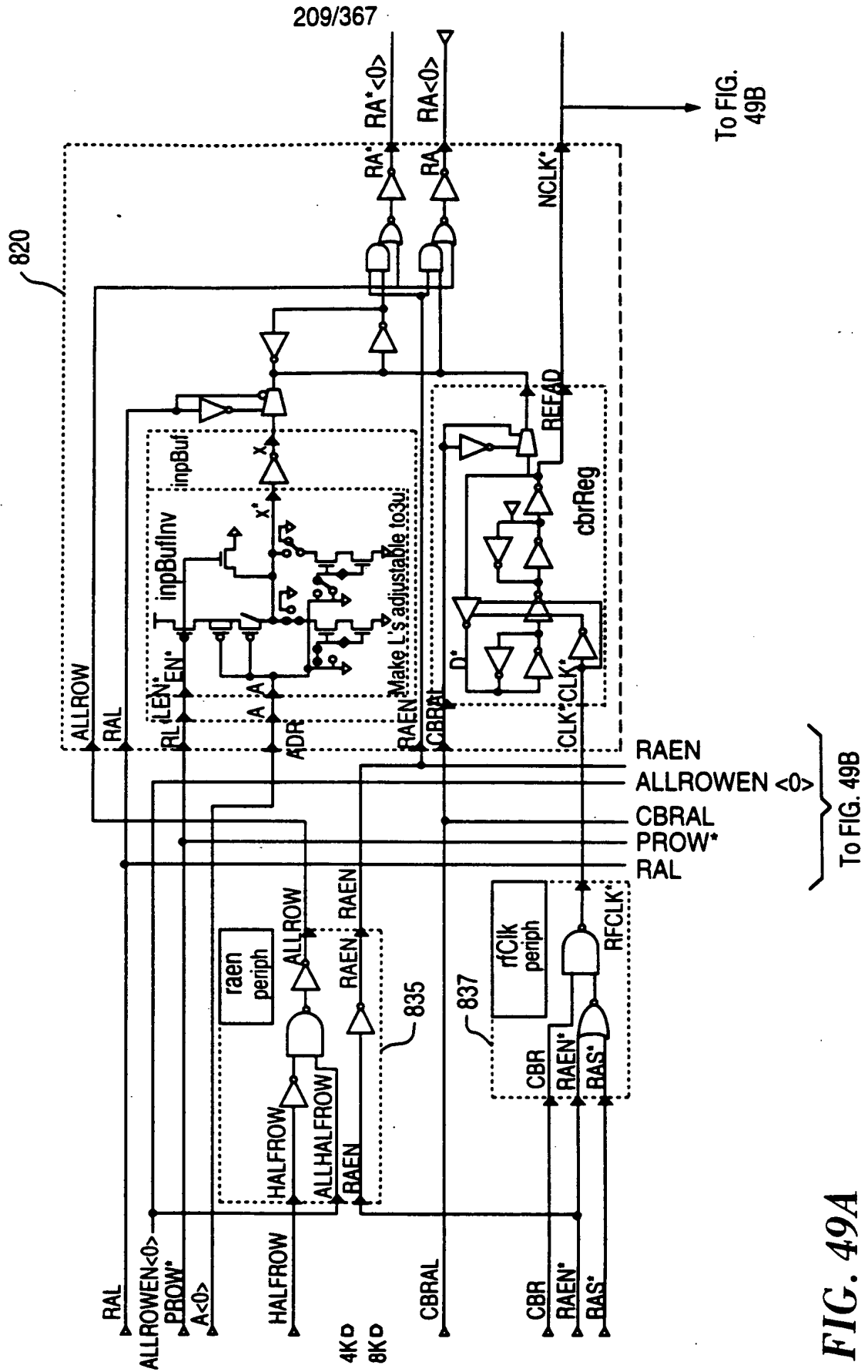
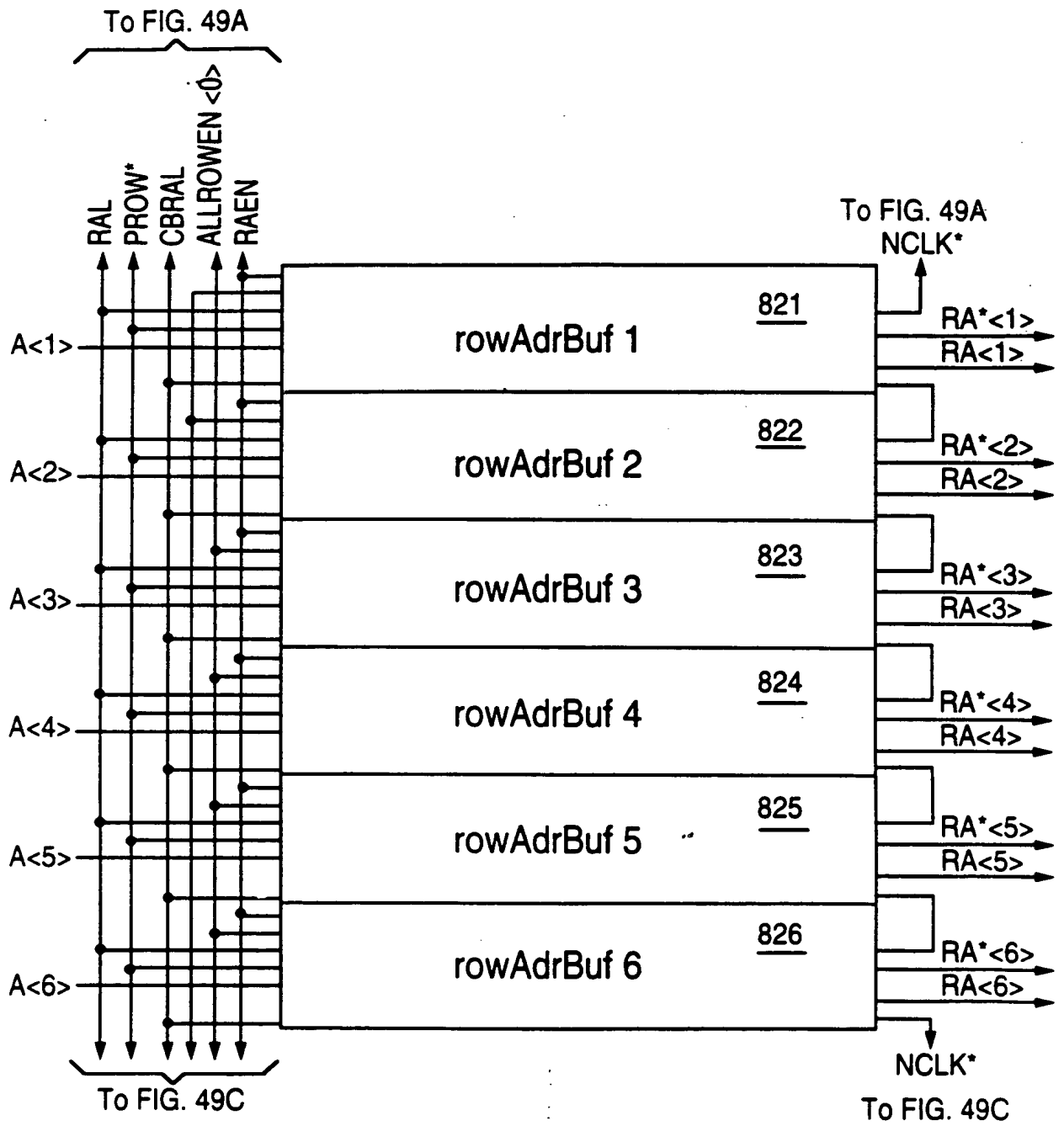
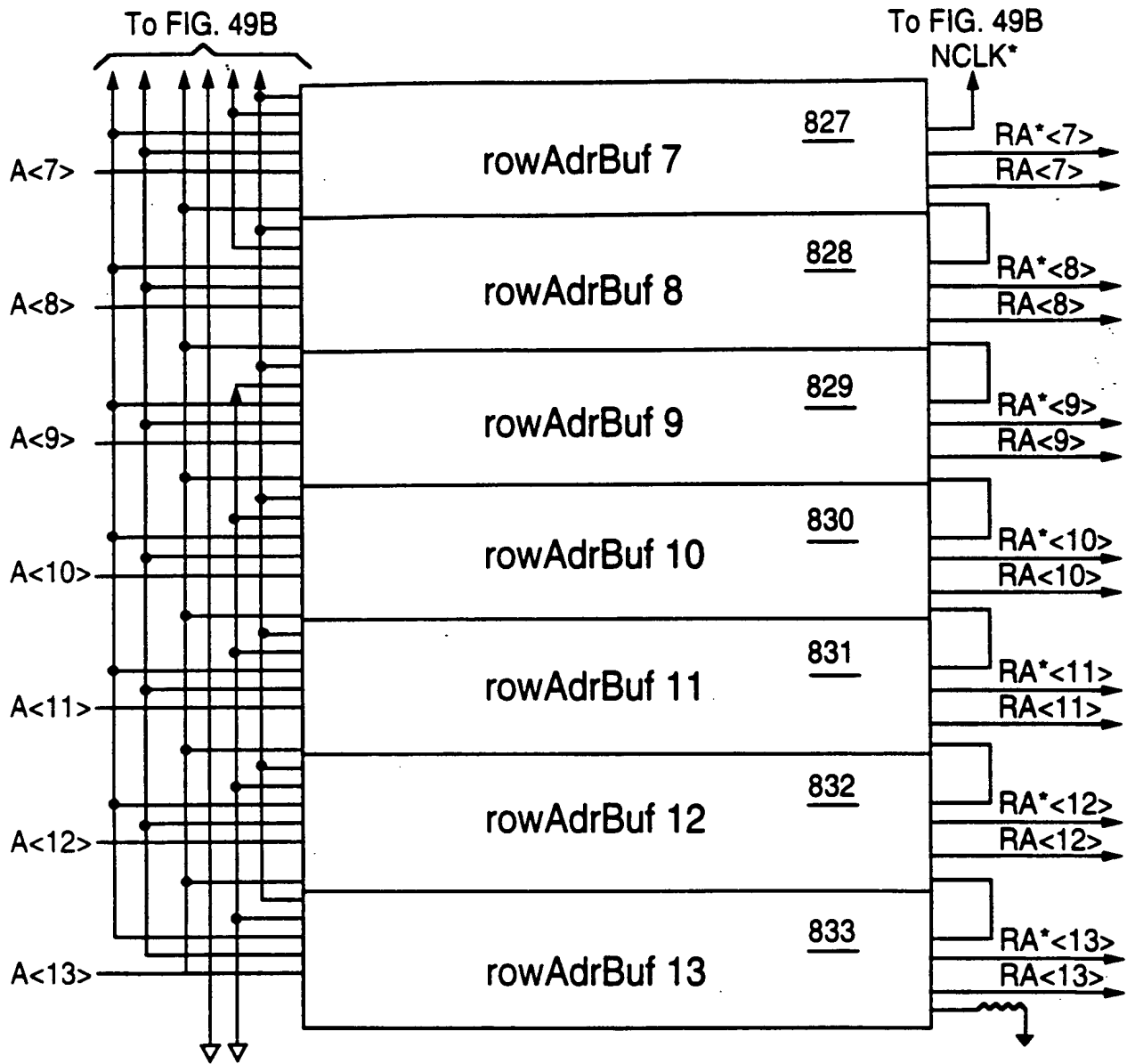


FIG. 49A

**FIG. 49B**

**FIG. 49C**

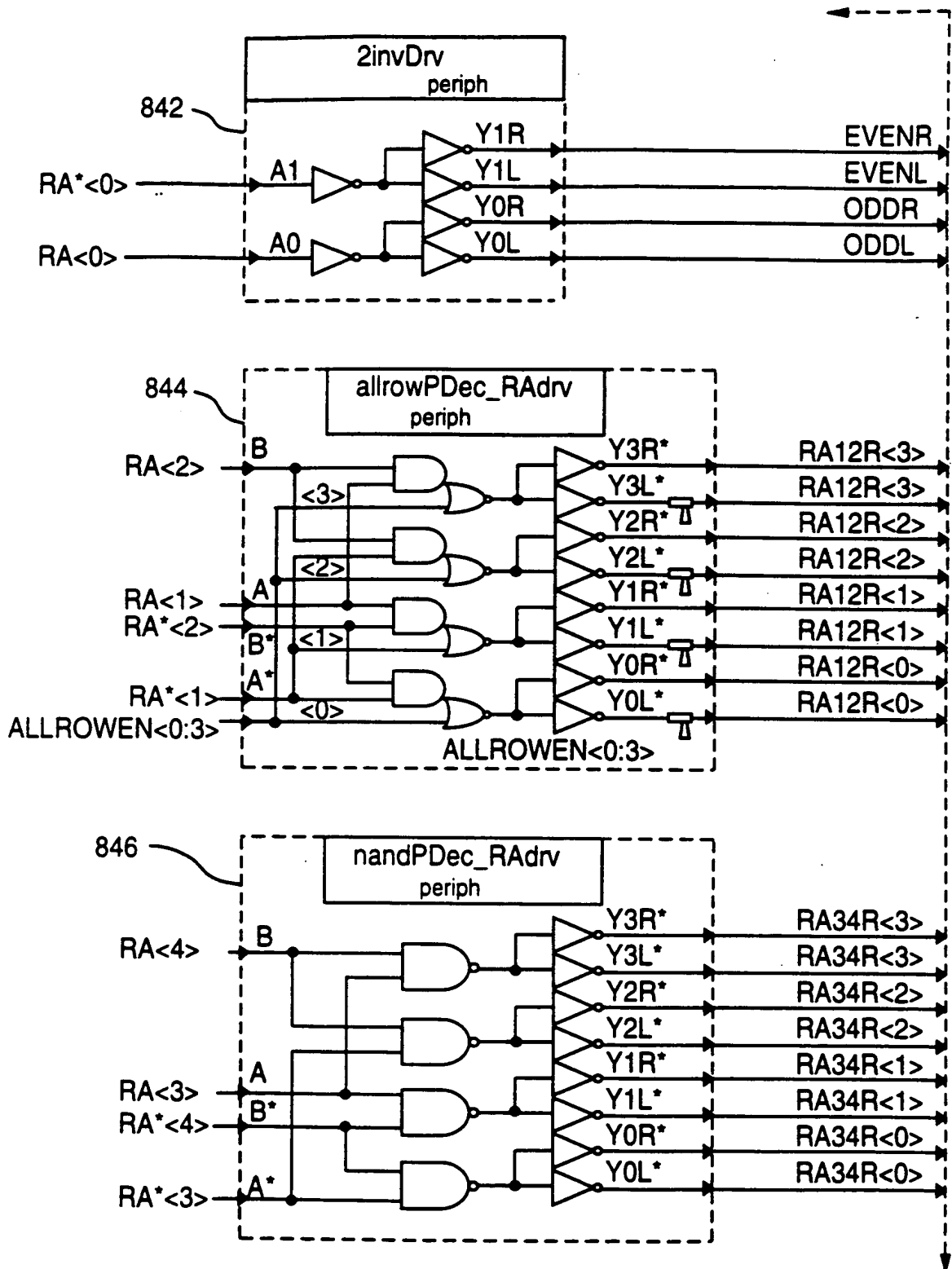
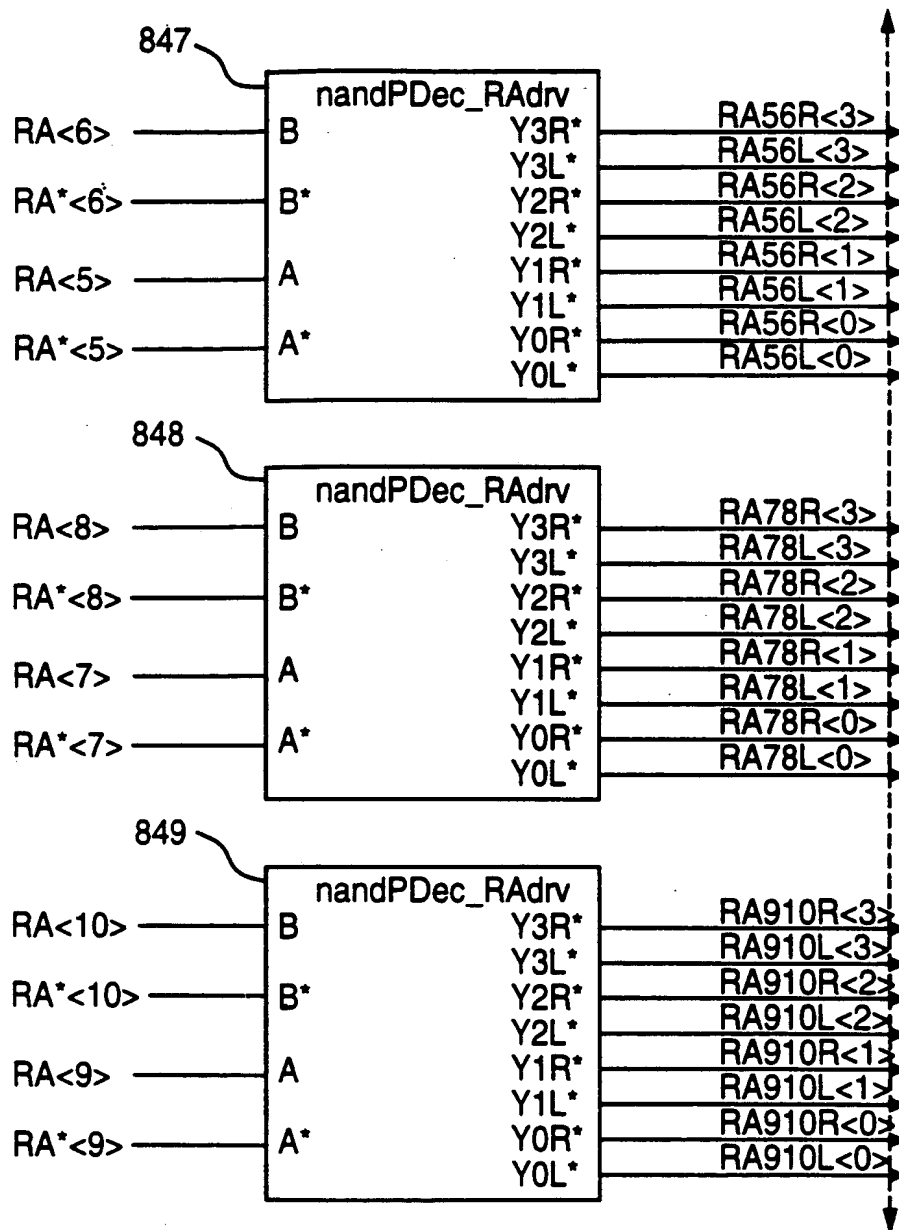


FIG. 50A

**FIG. 50B**

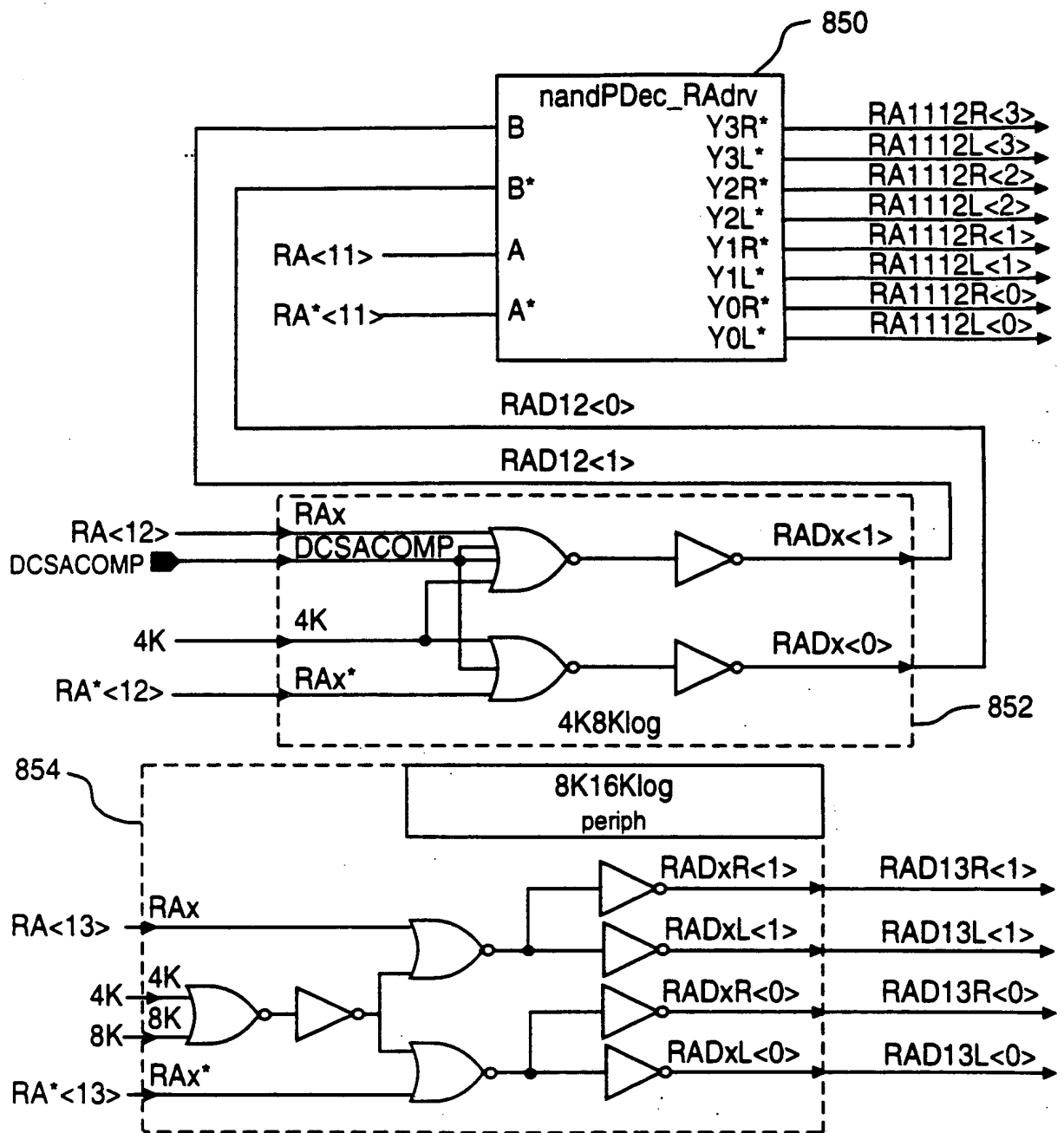


FIG. 50C

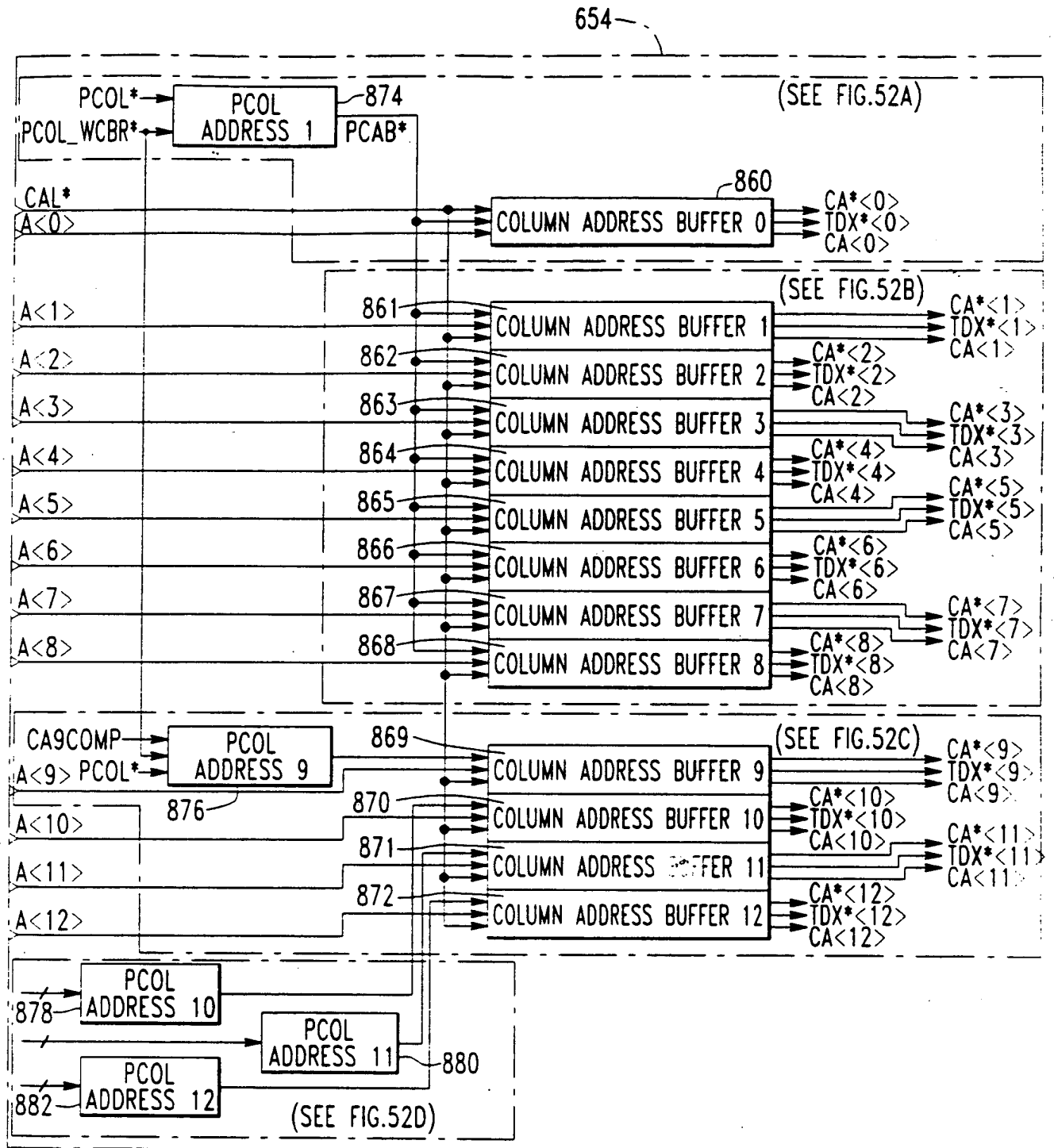


FIG. 51A

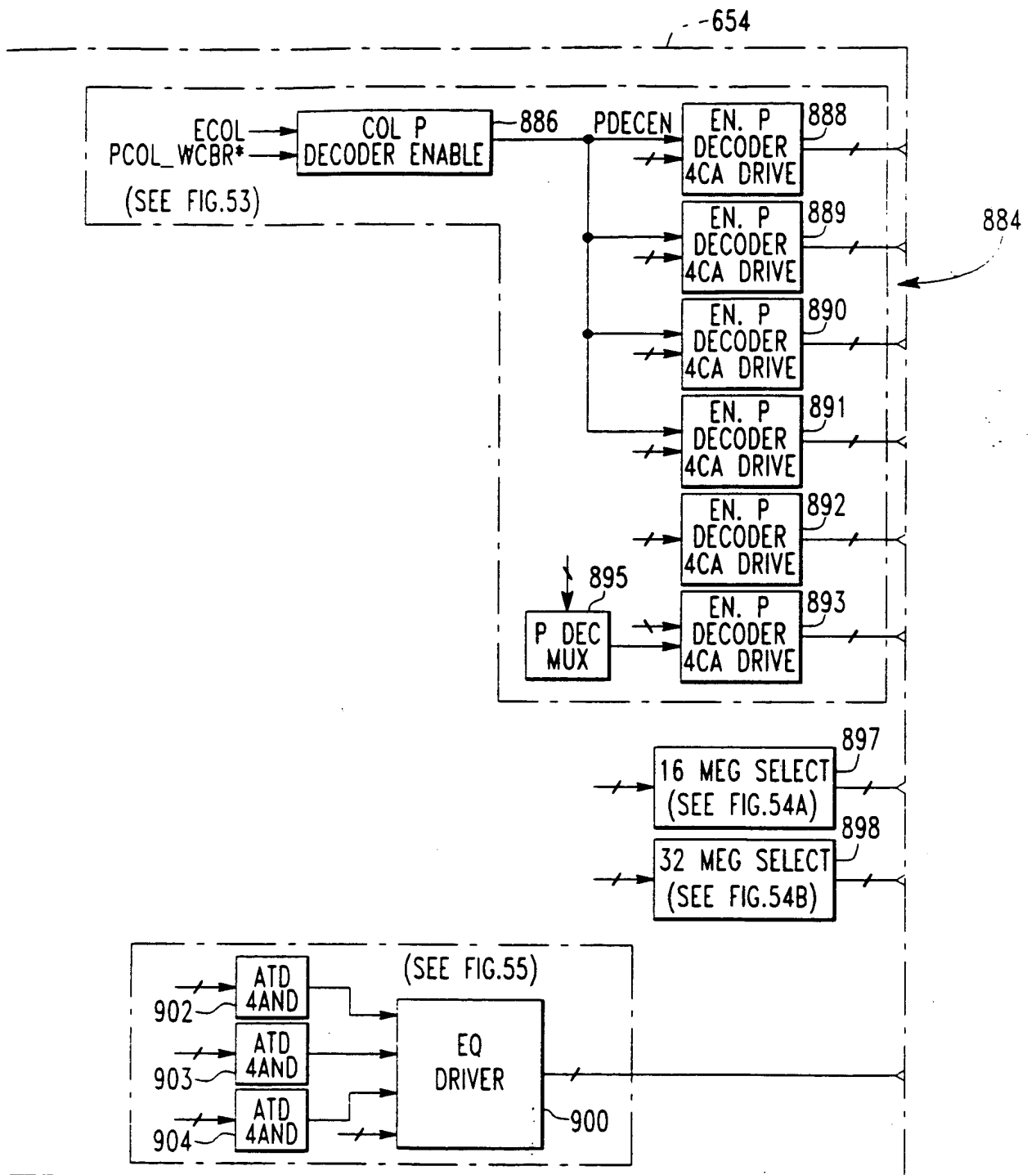


FIG. 51B

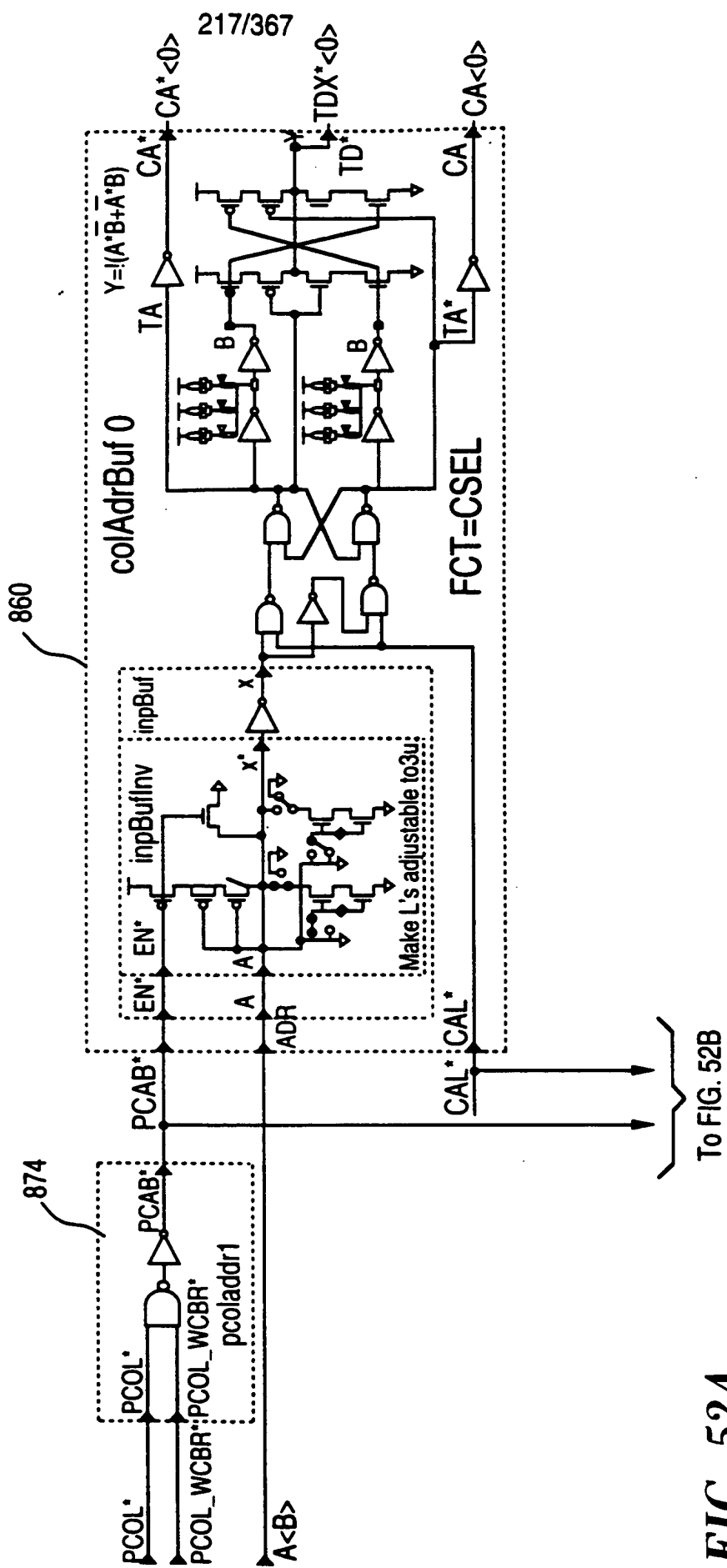
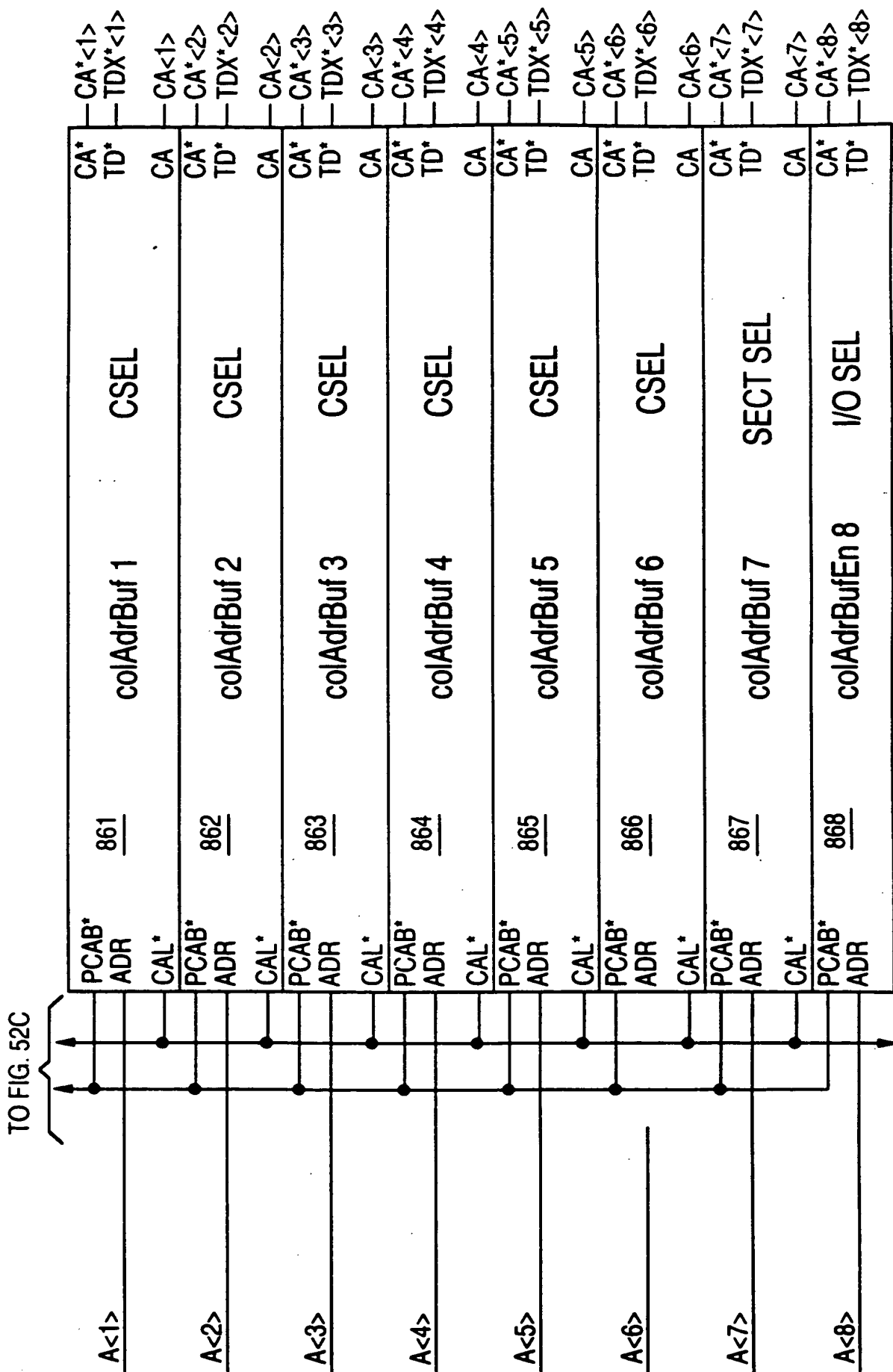


FIG. 52A



TO FIG. 52C

FIG. 52B

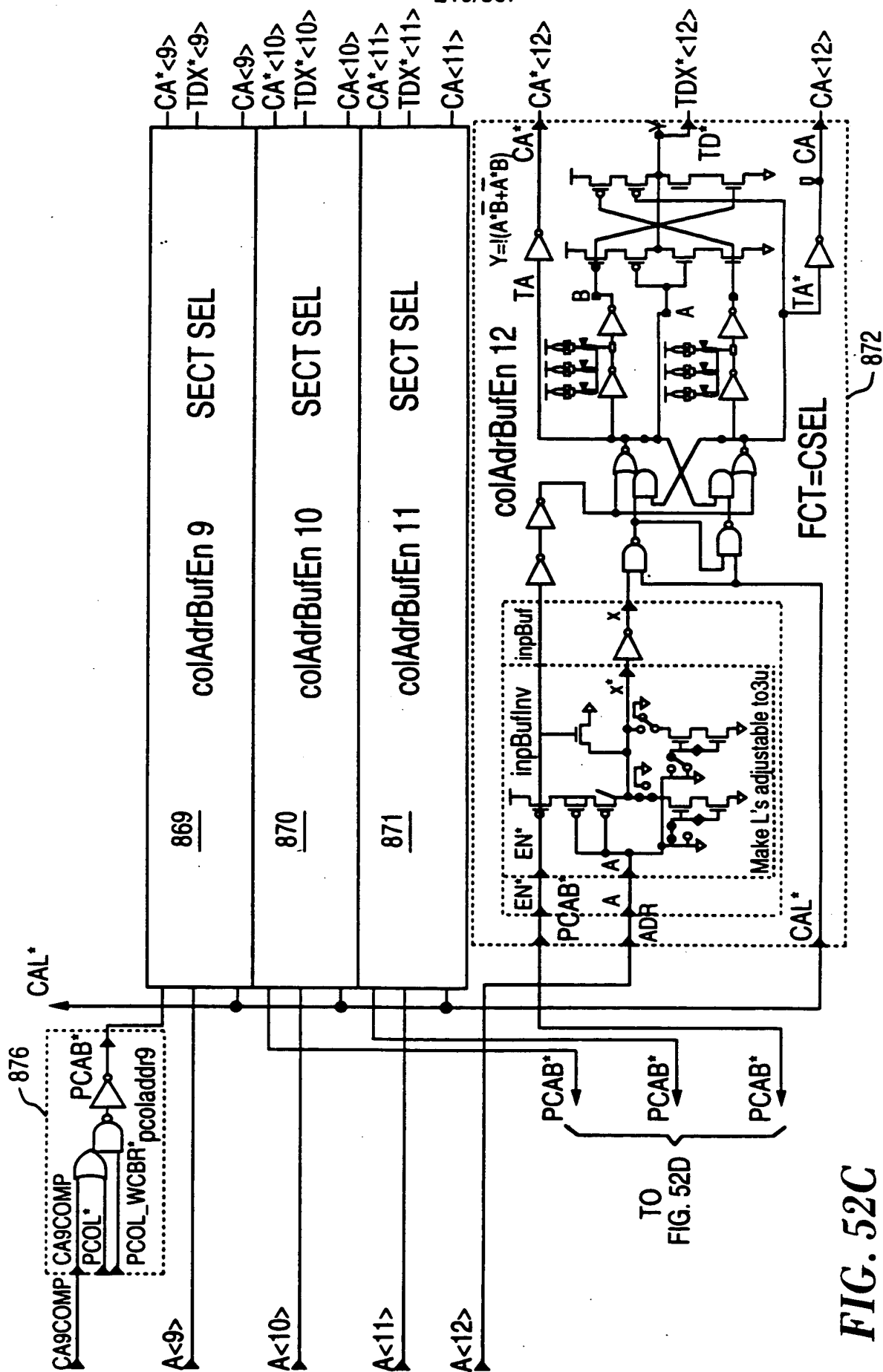


FIG. 52C

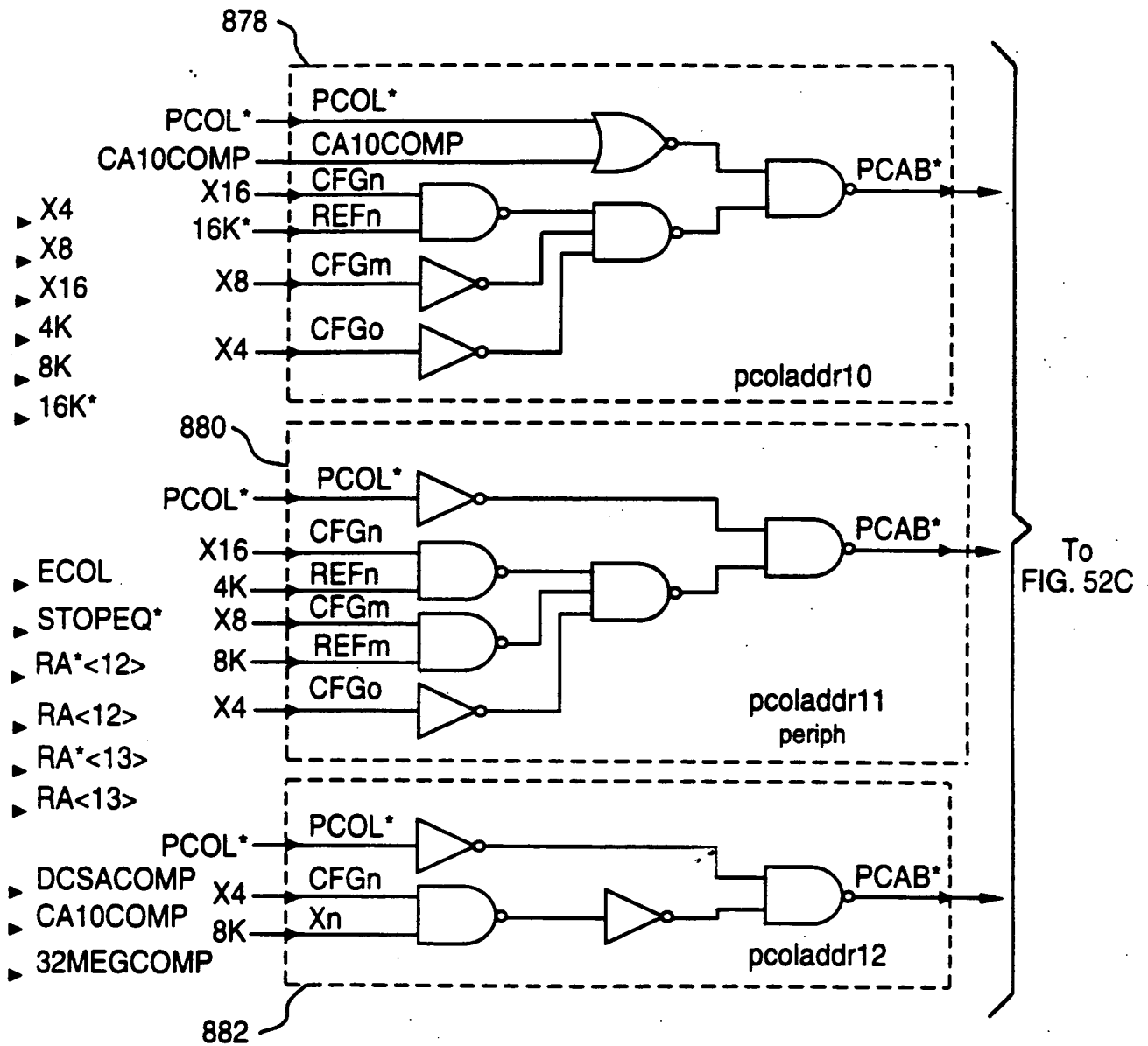


FIG. 52D

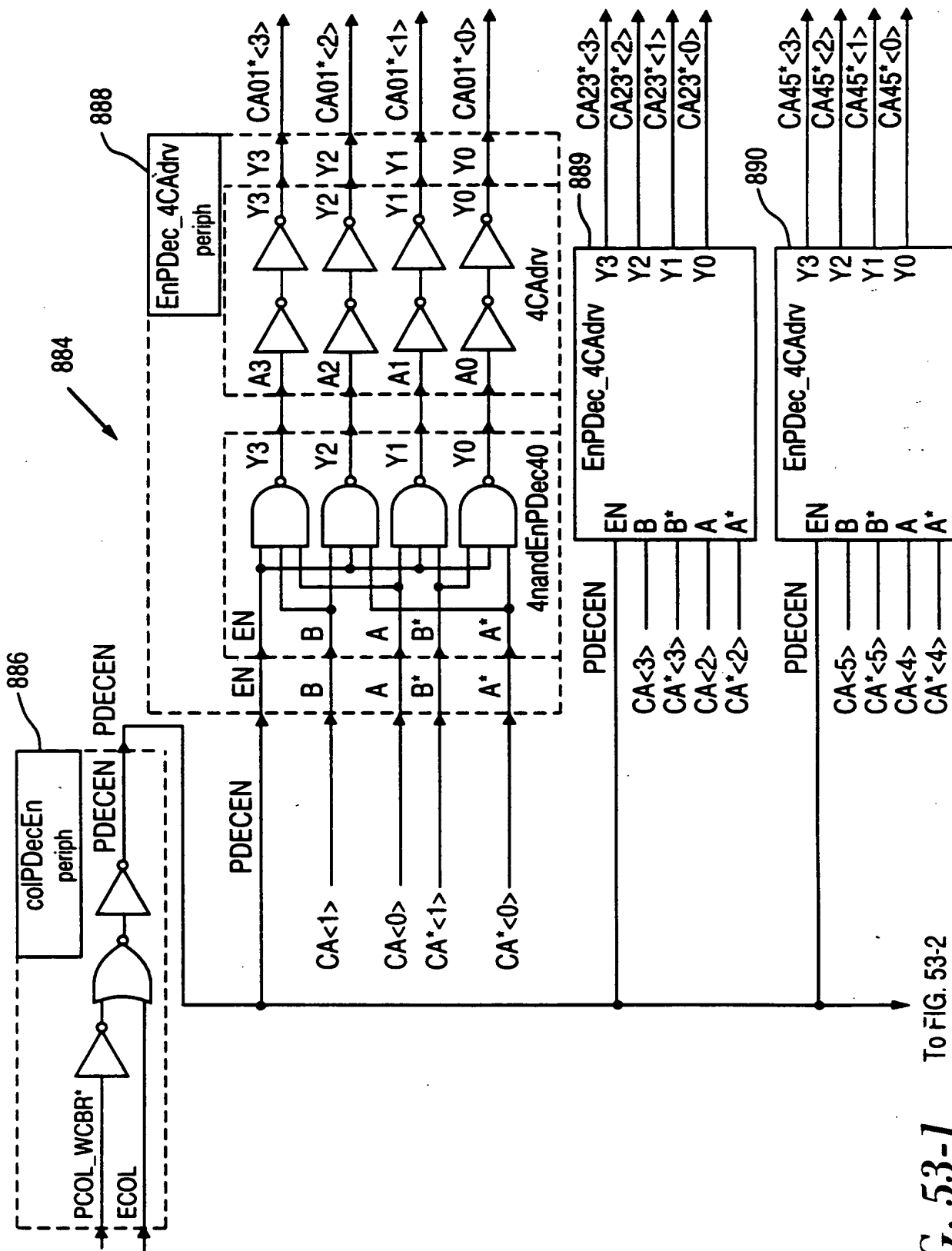


FIG. 53-1 To FIG. 53-2

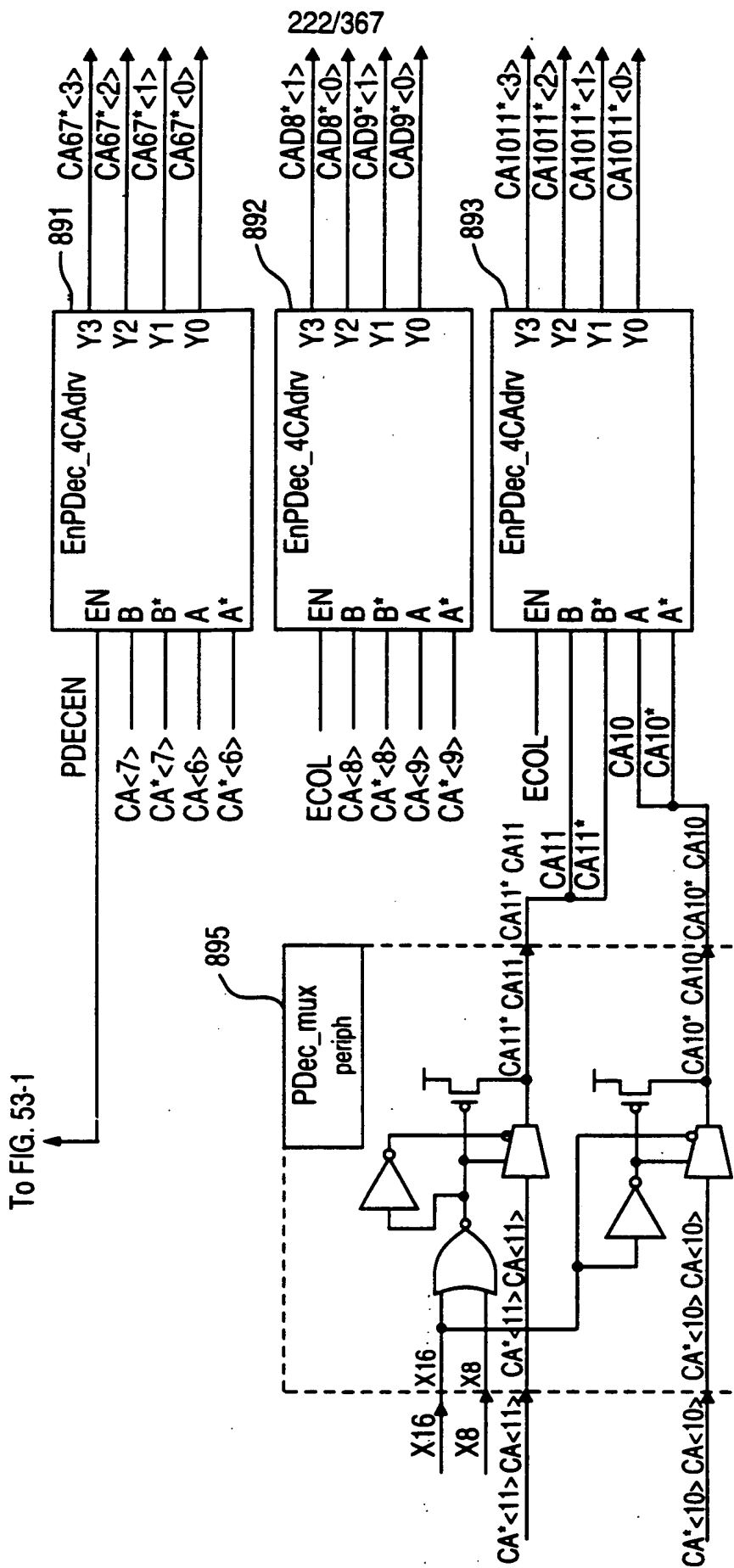


FIG. 53-2

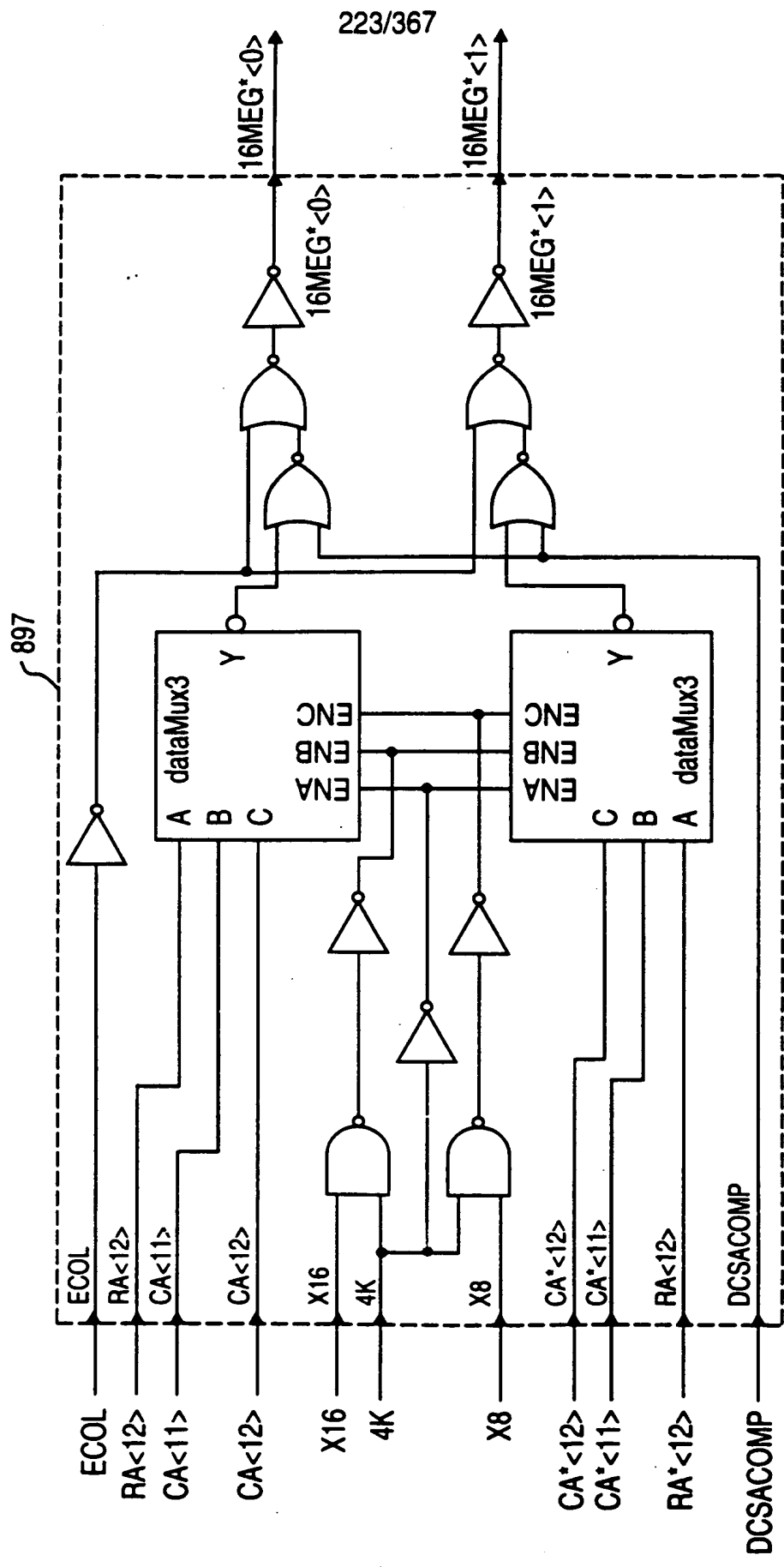
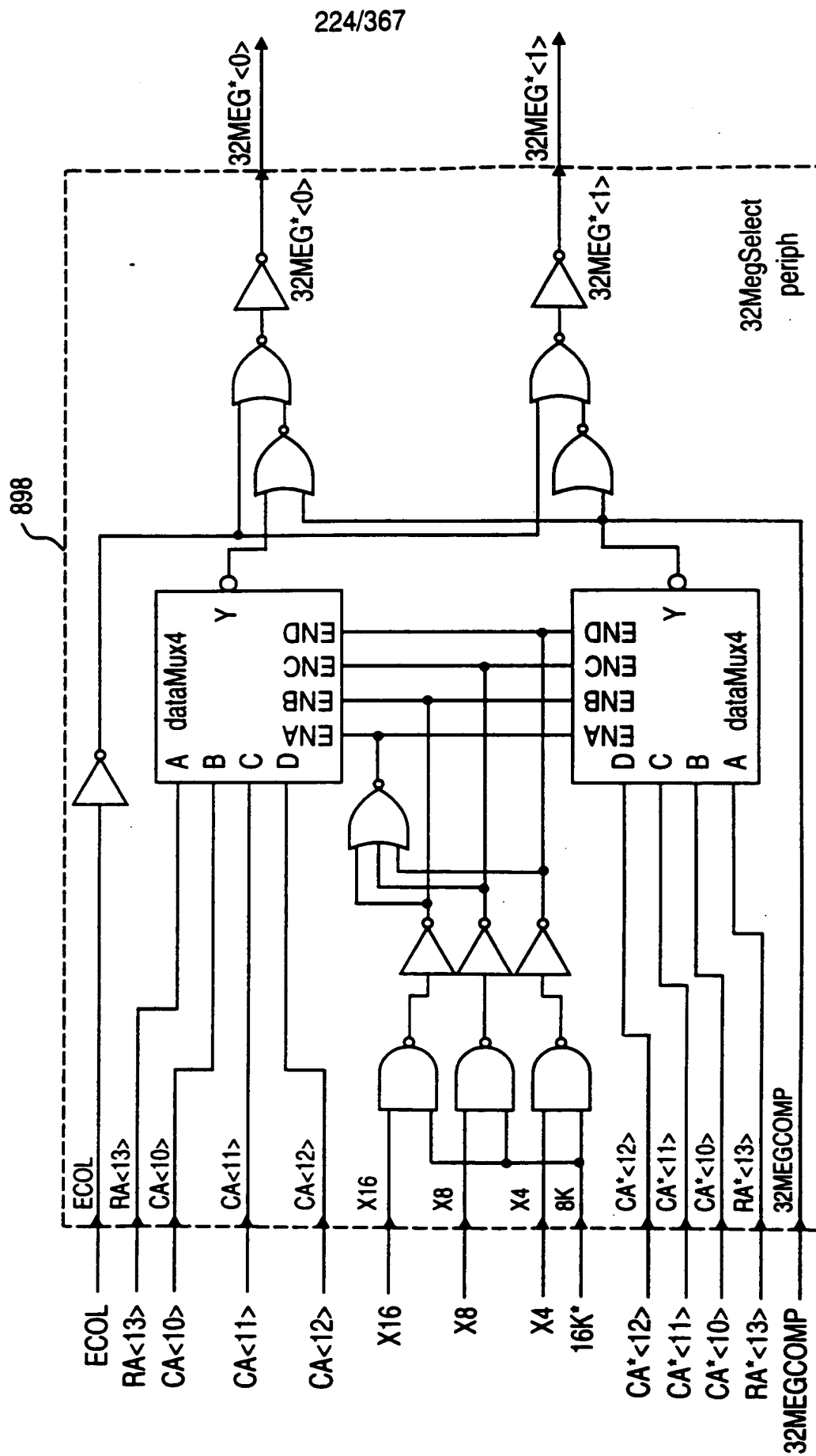


FIG. 54A





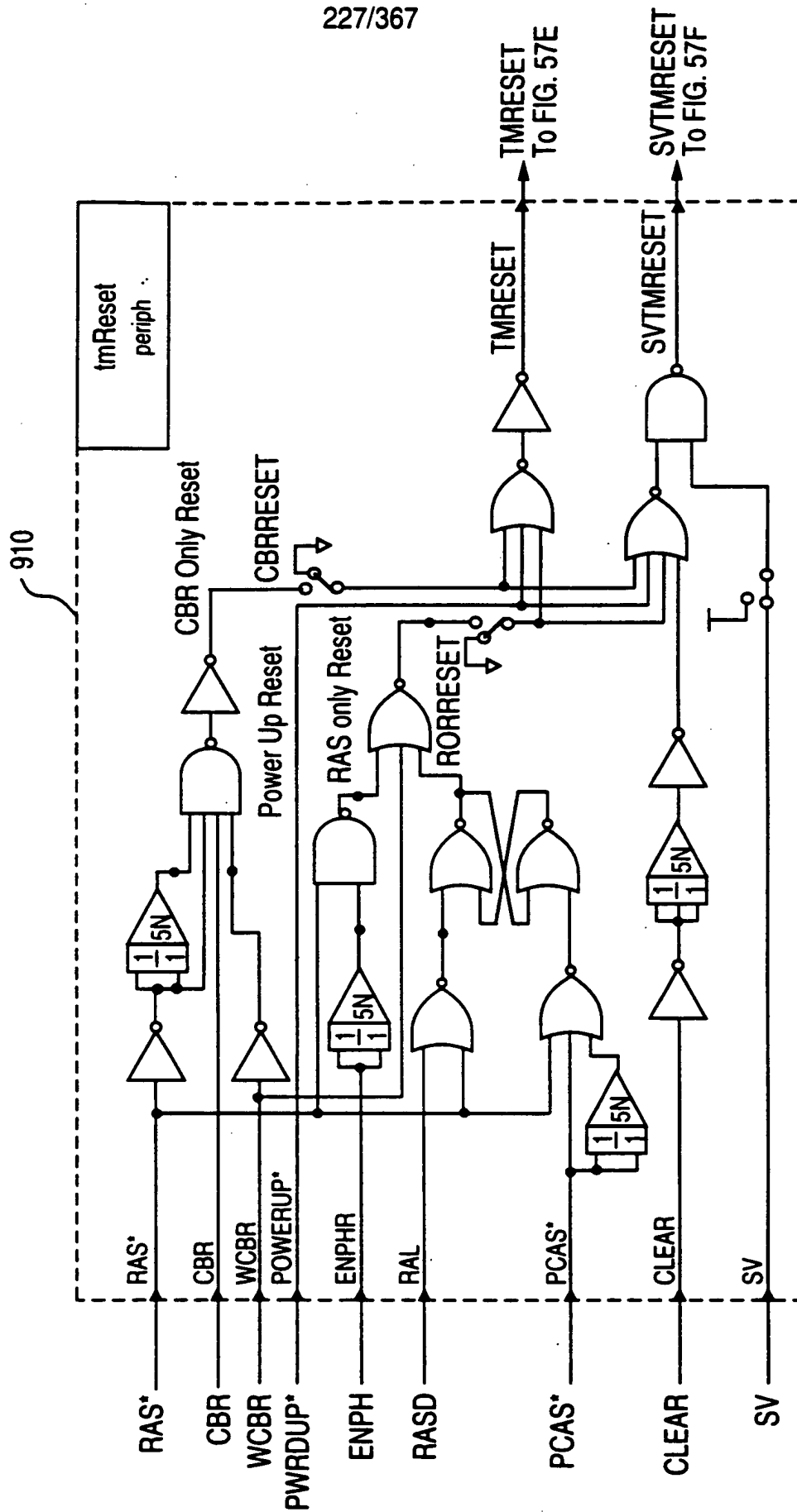


FIG. 57A

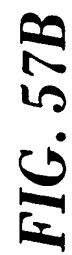


FIG. 57B

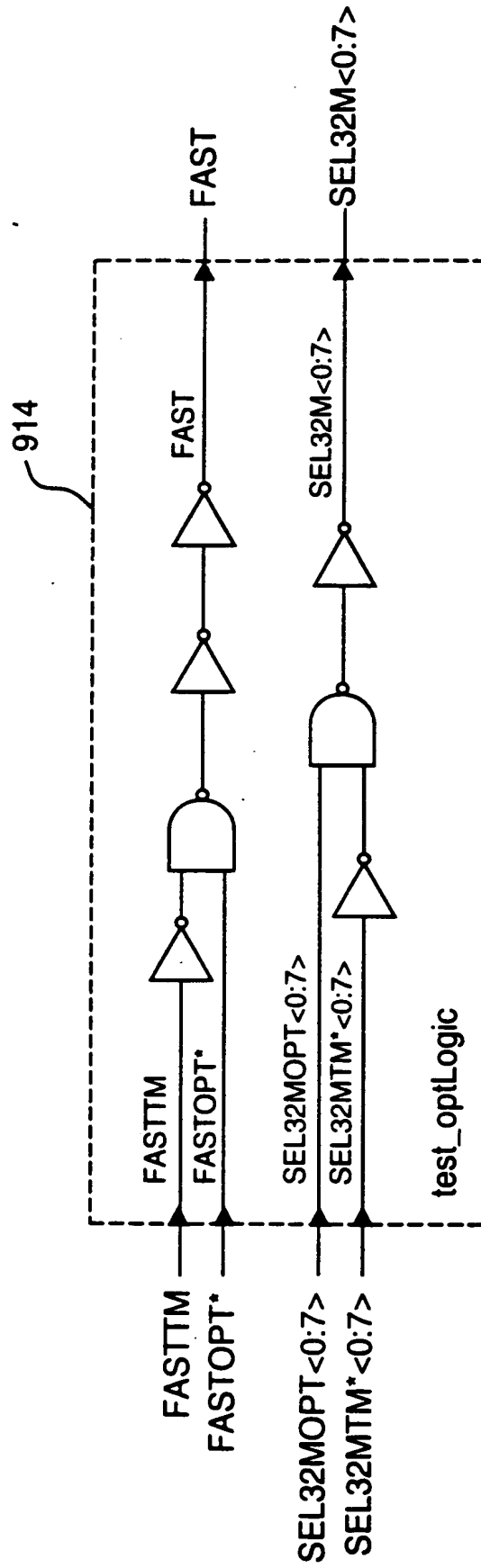


FIG. 57C

FIG. 57E

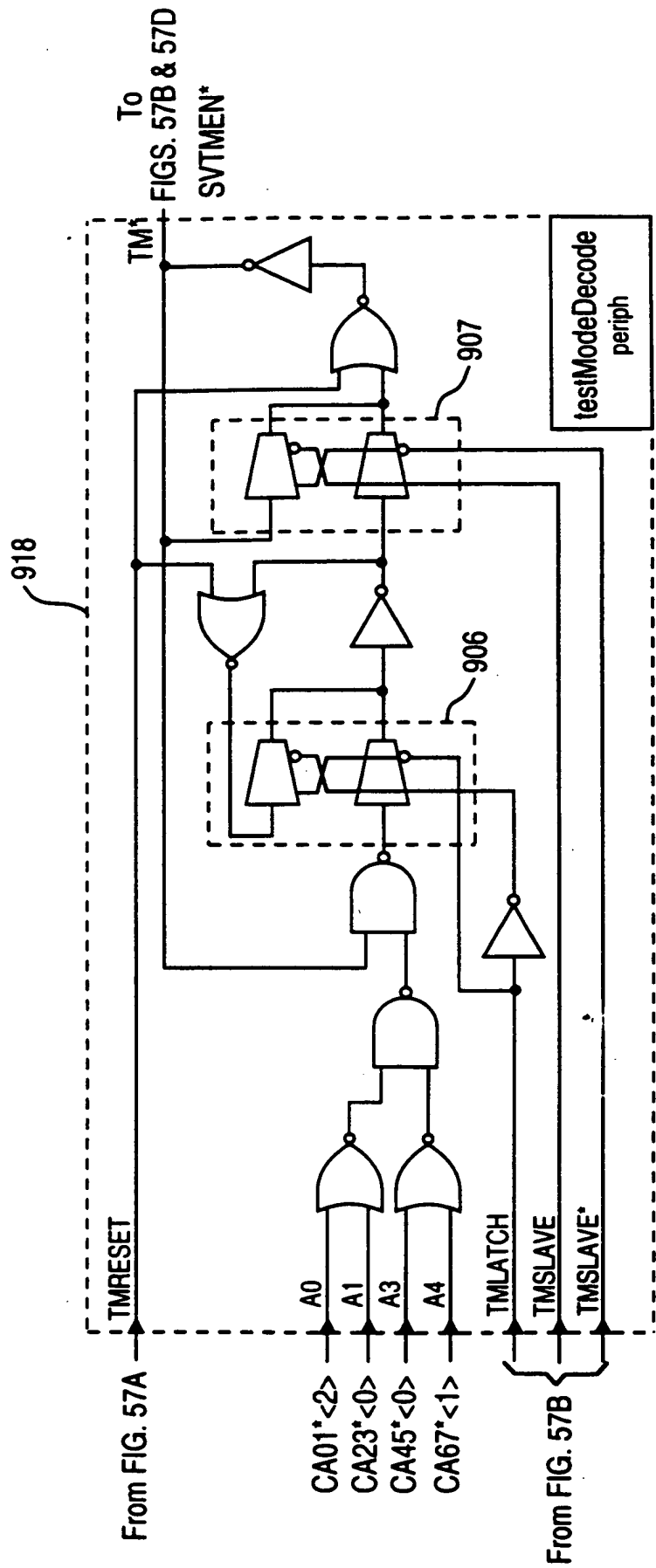


FIG. 57E

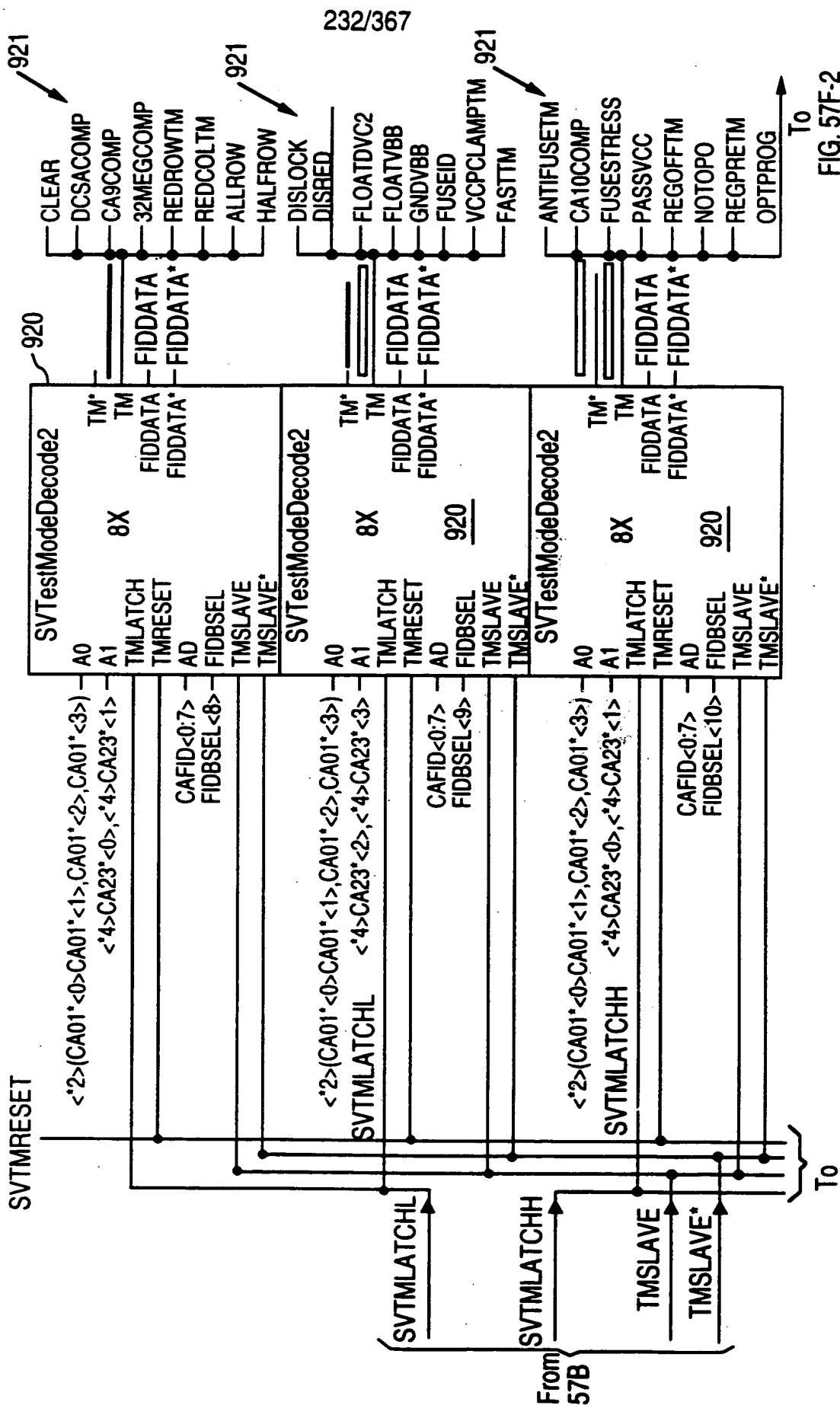
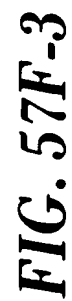


FIG. 57F-1

FIG. 57F-3

FIG. 57F-2



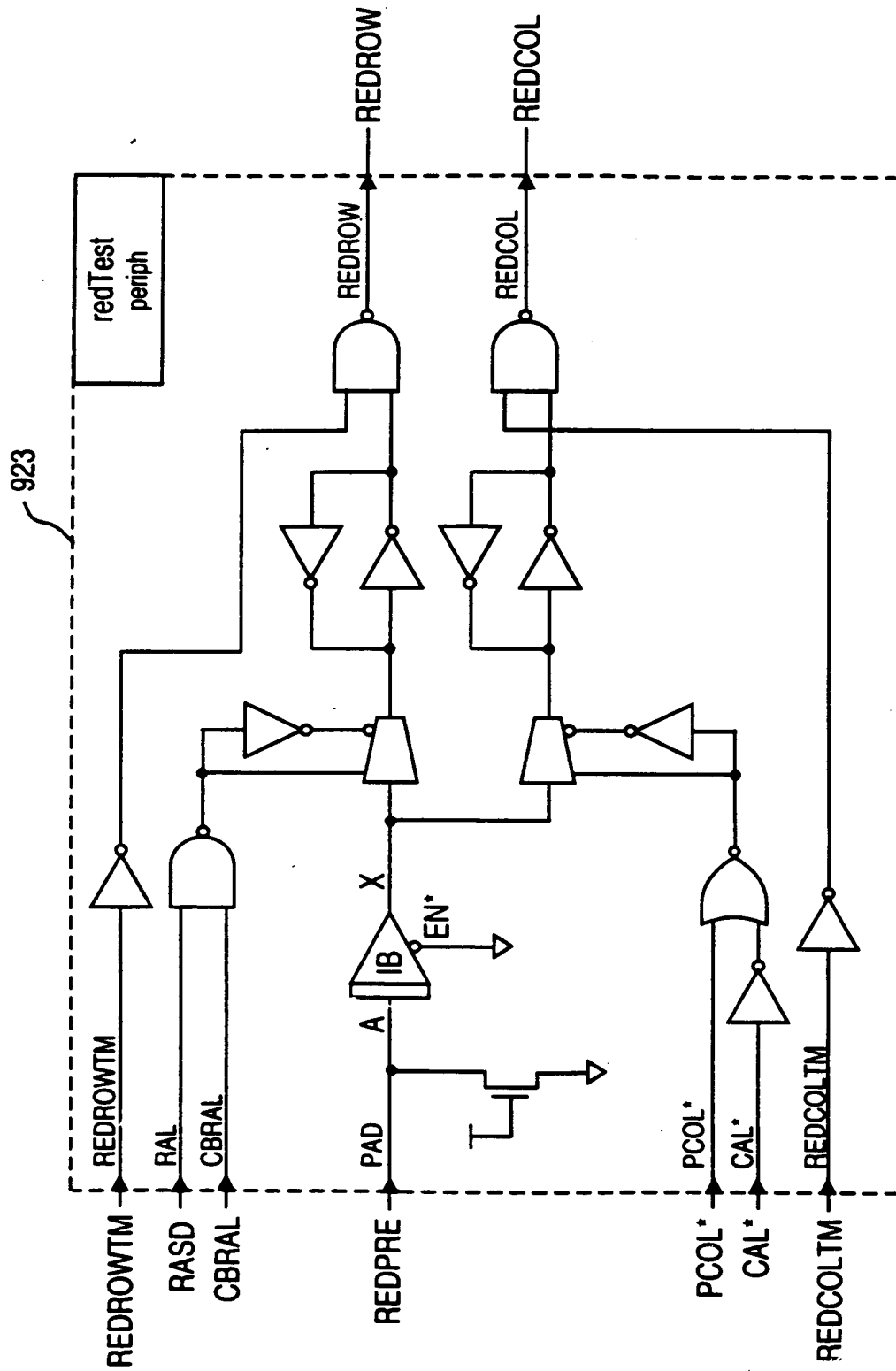
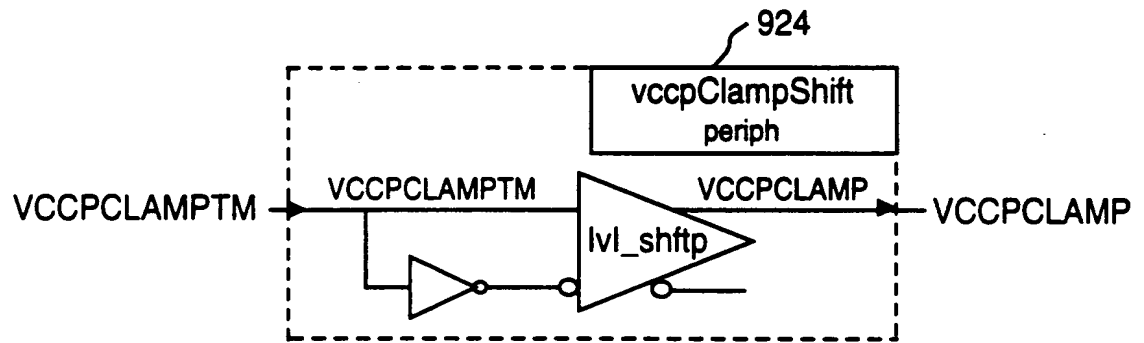
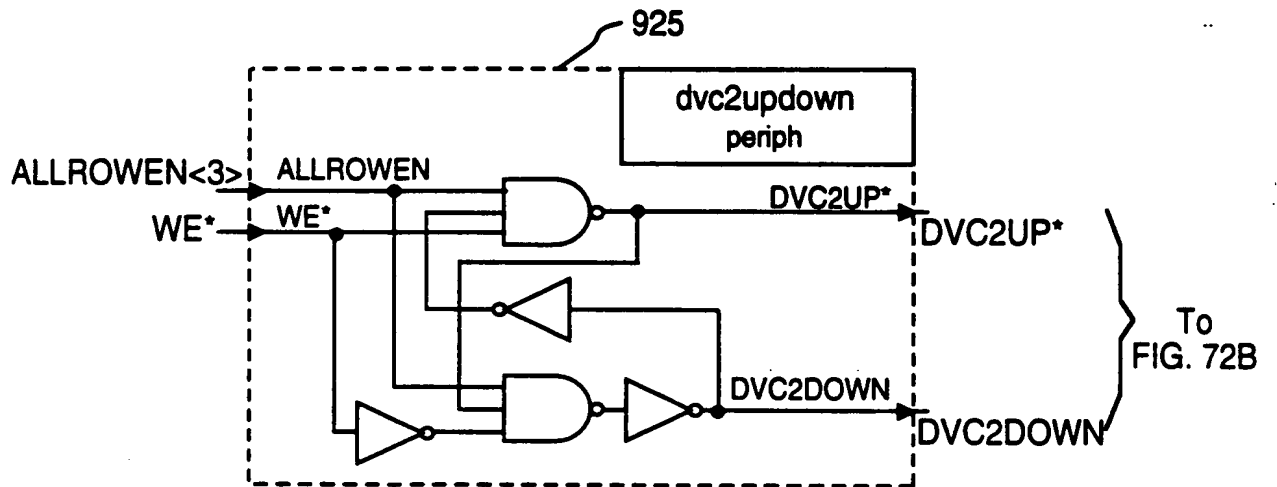
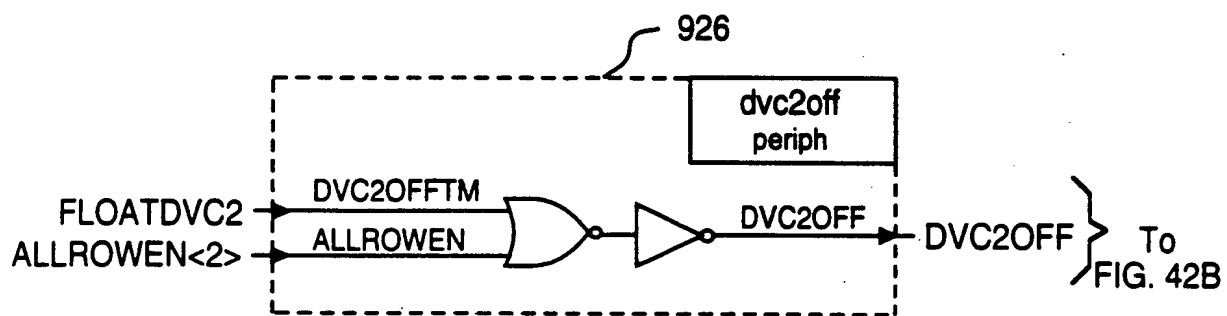
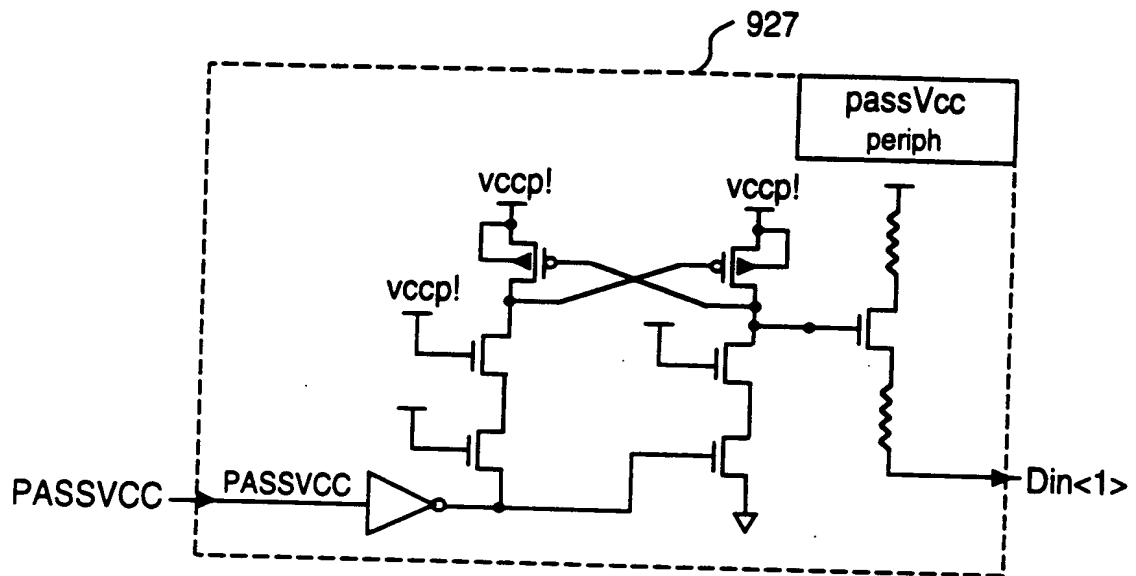


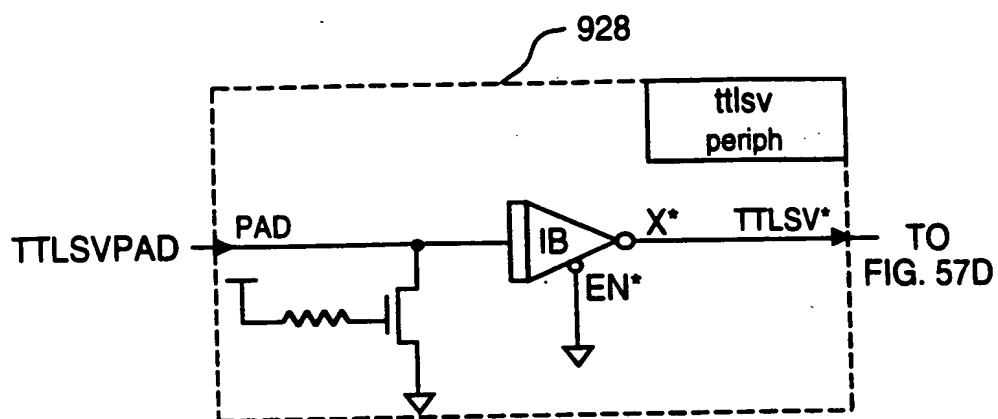
FIG. 57G

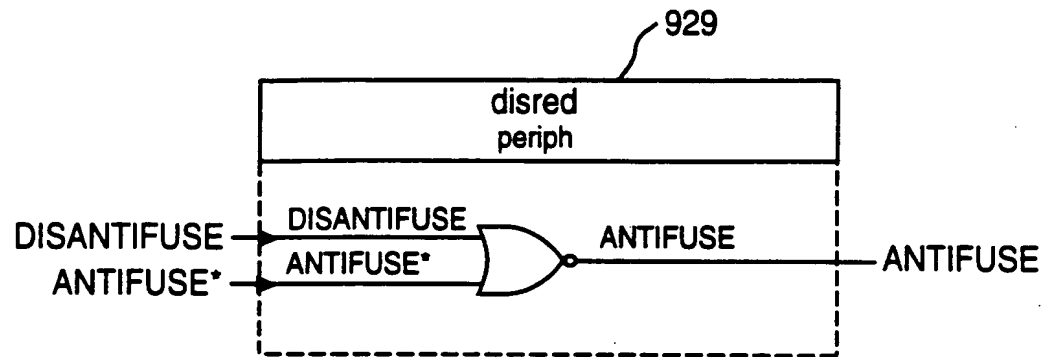
**FIG. 57H**

**FIG. 57I**

**FIG. 57J**

**FIG. 57K**

**FIG. 57L**

**FIG. 57M**

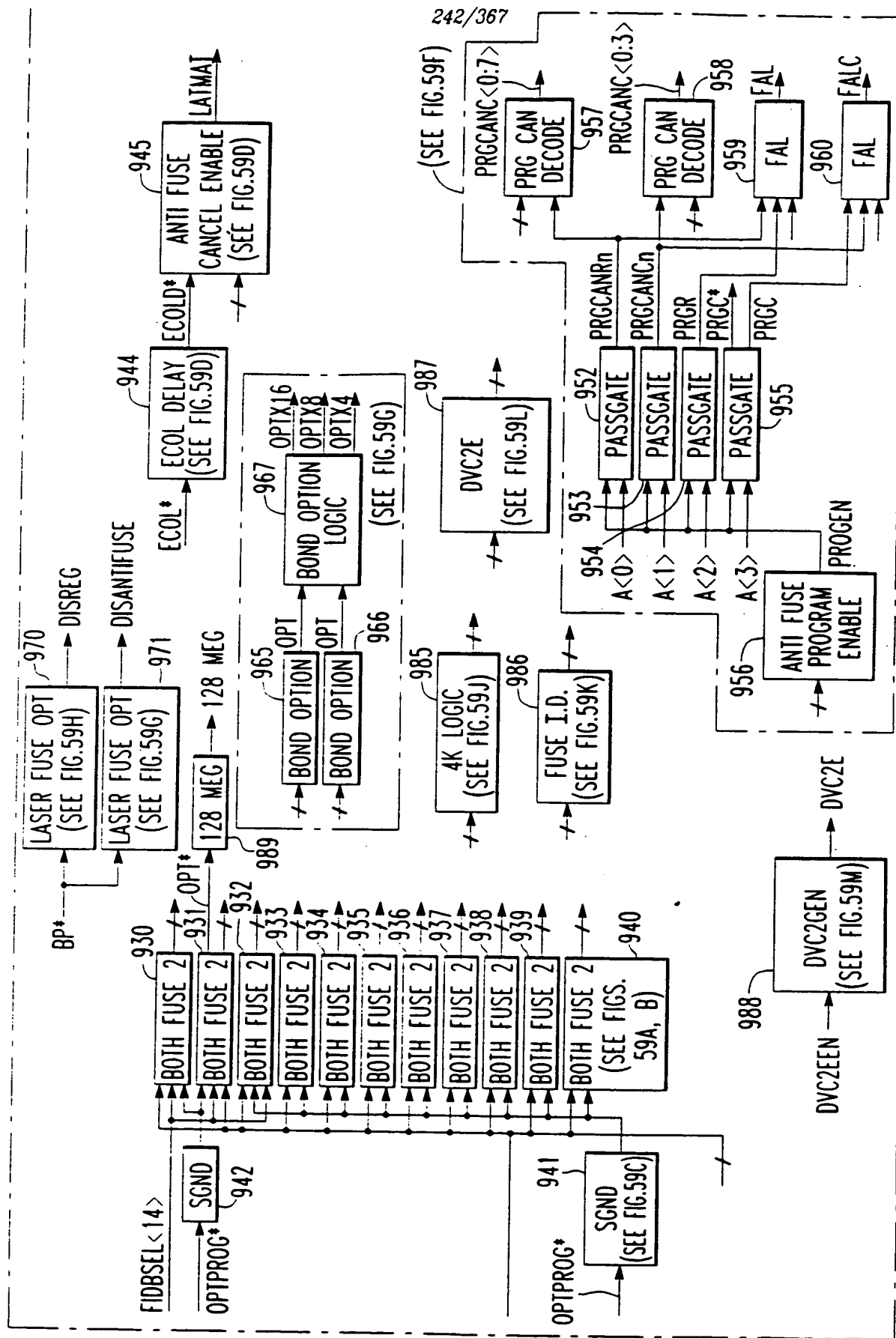


FIG. 58A

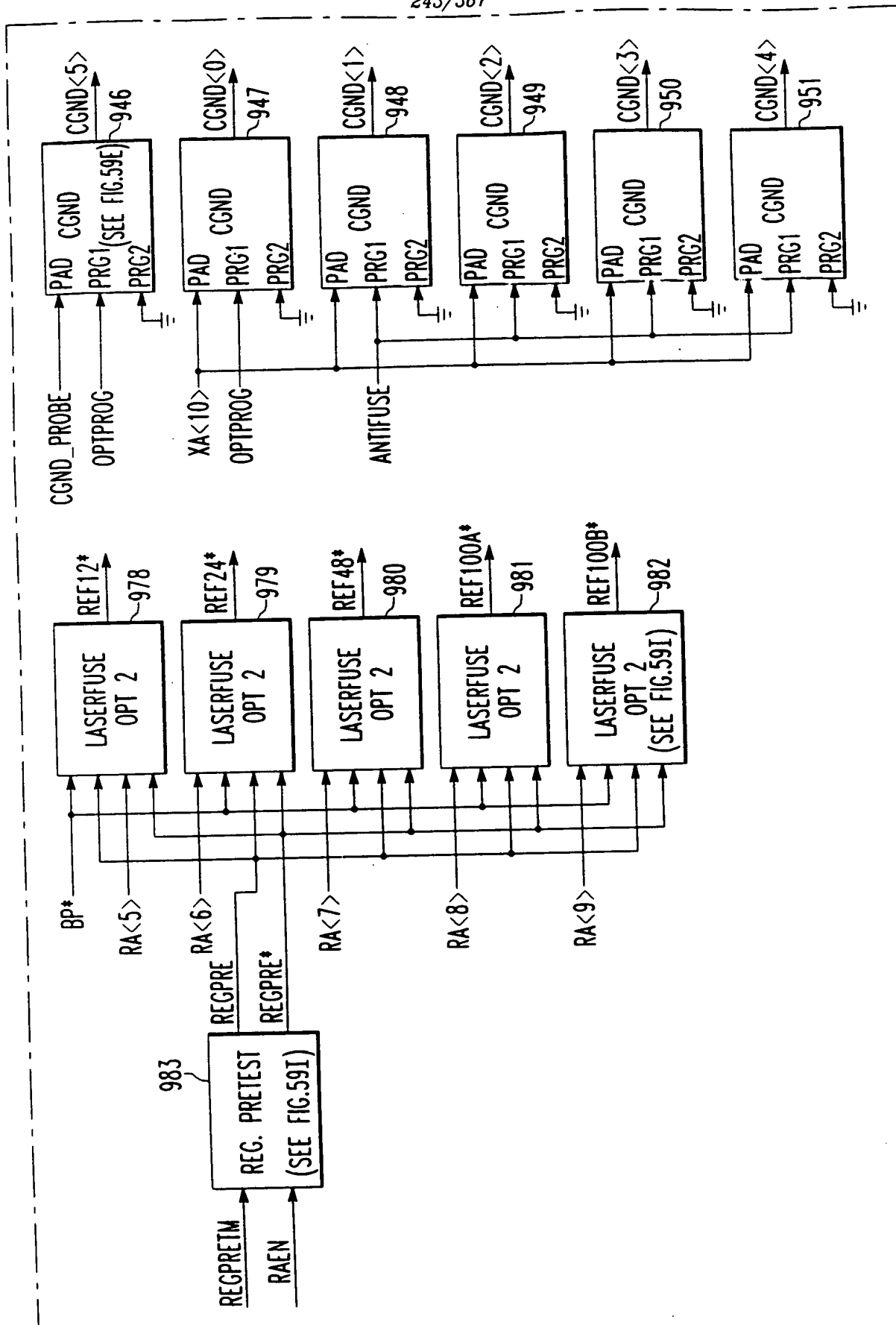


FIG. 58B

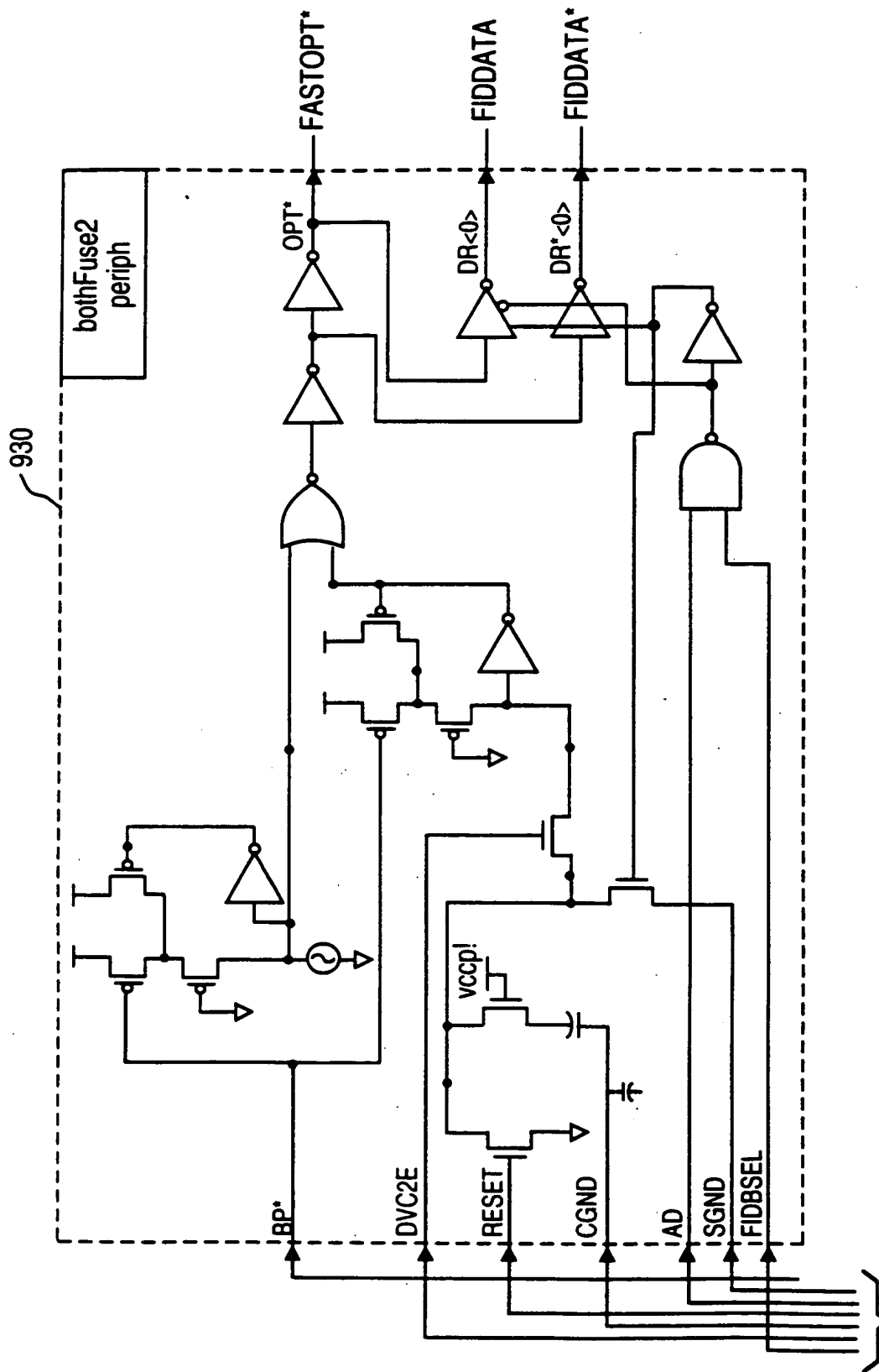


FIG. 59B

FIG. 59A

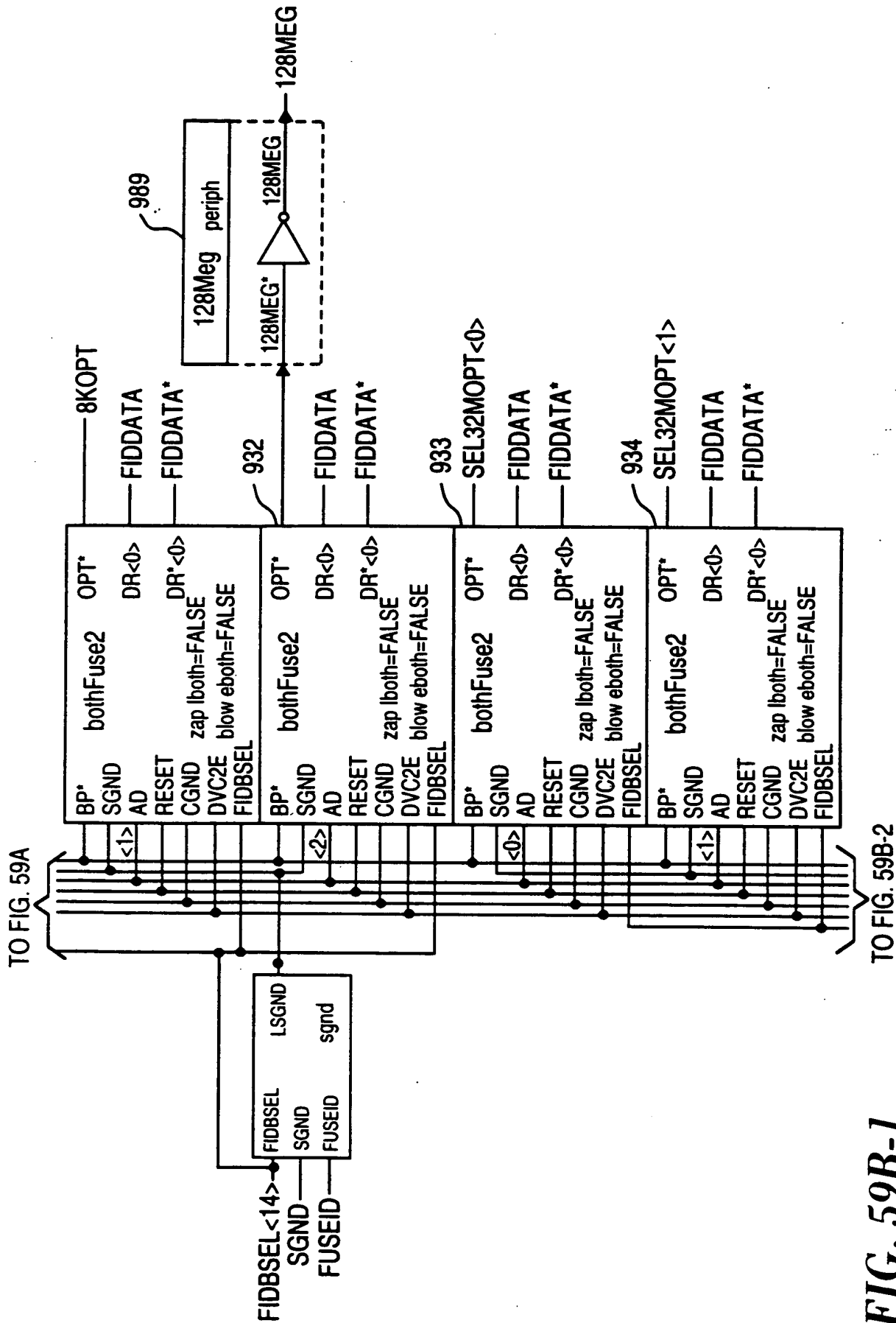


FIG. 59B-1

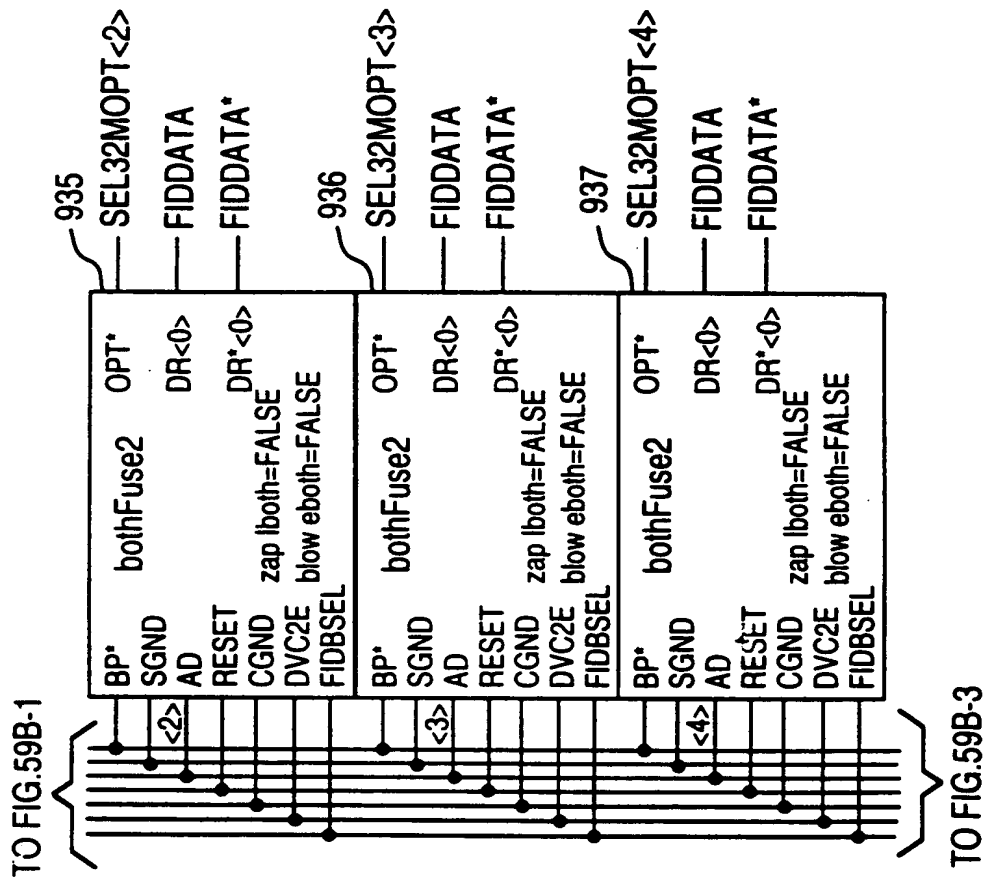


FIG. 59B-2

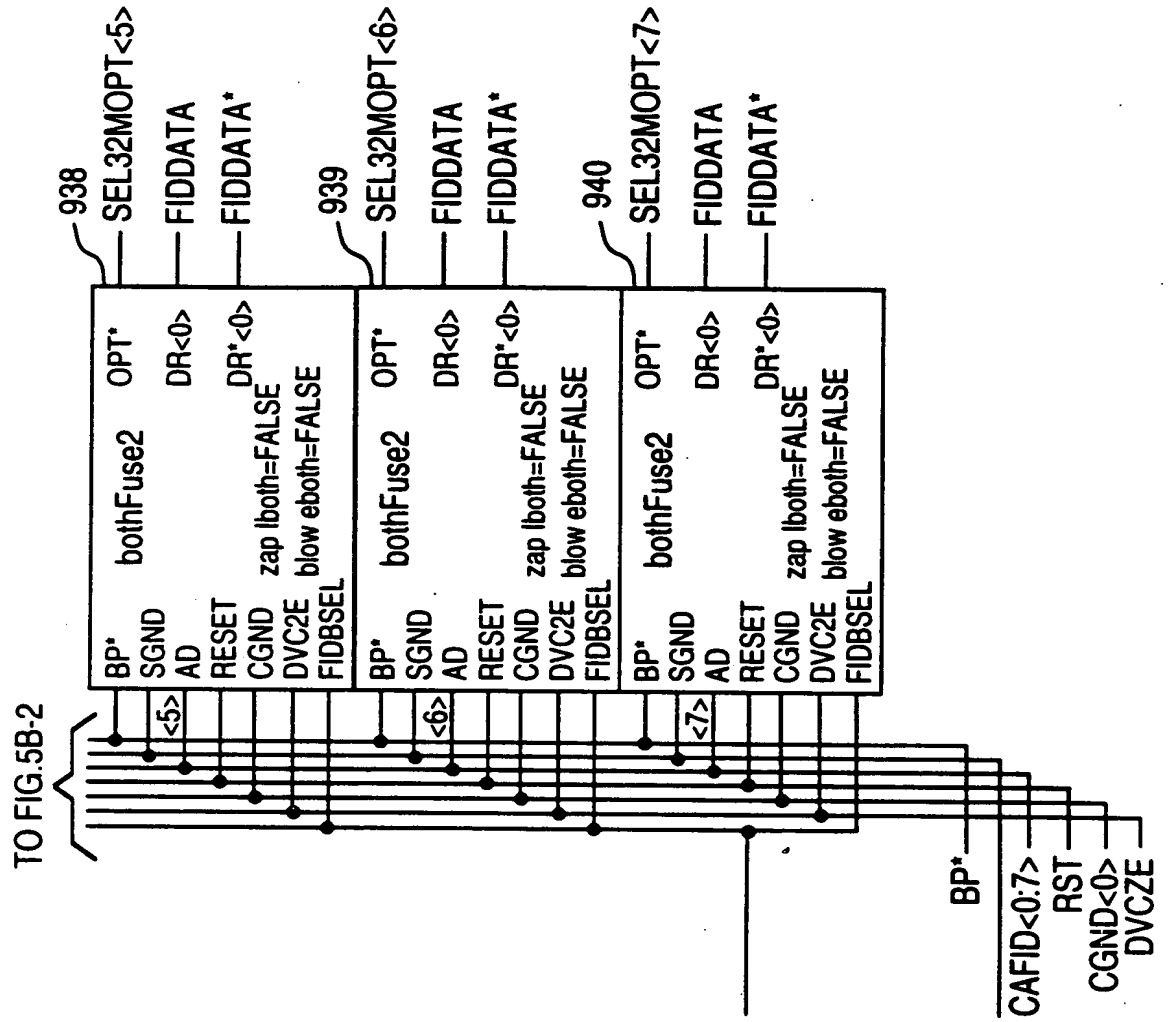
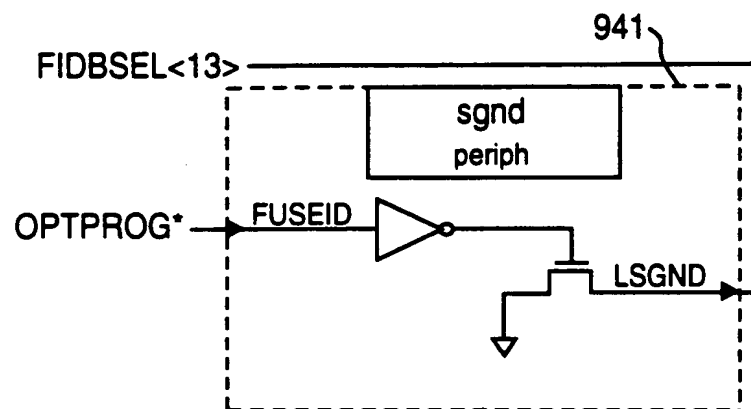


FIG. 59B-3

**FIG. 59C**

103230" 652660

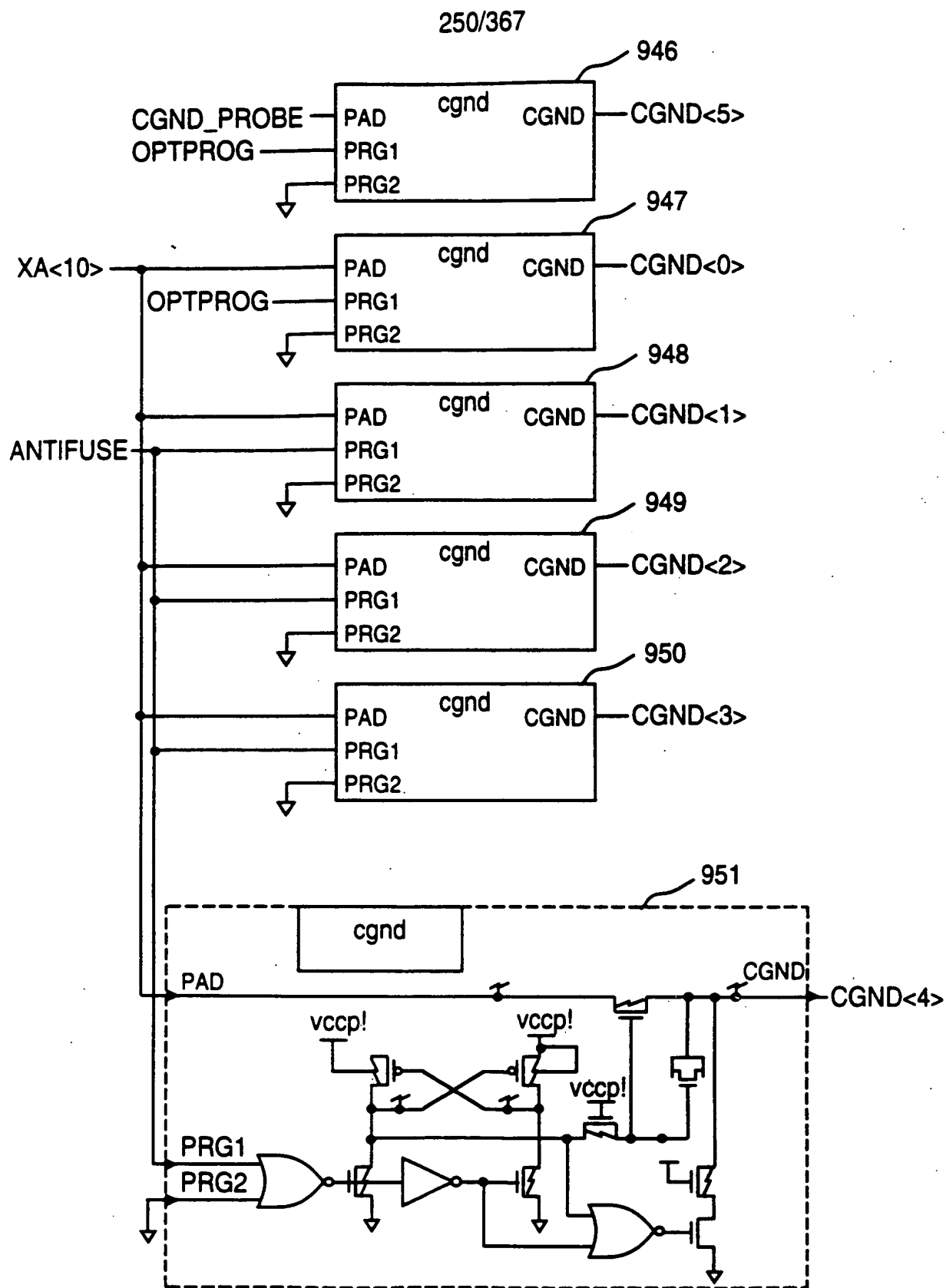
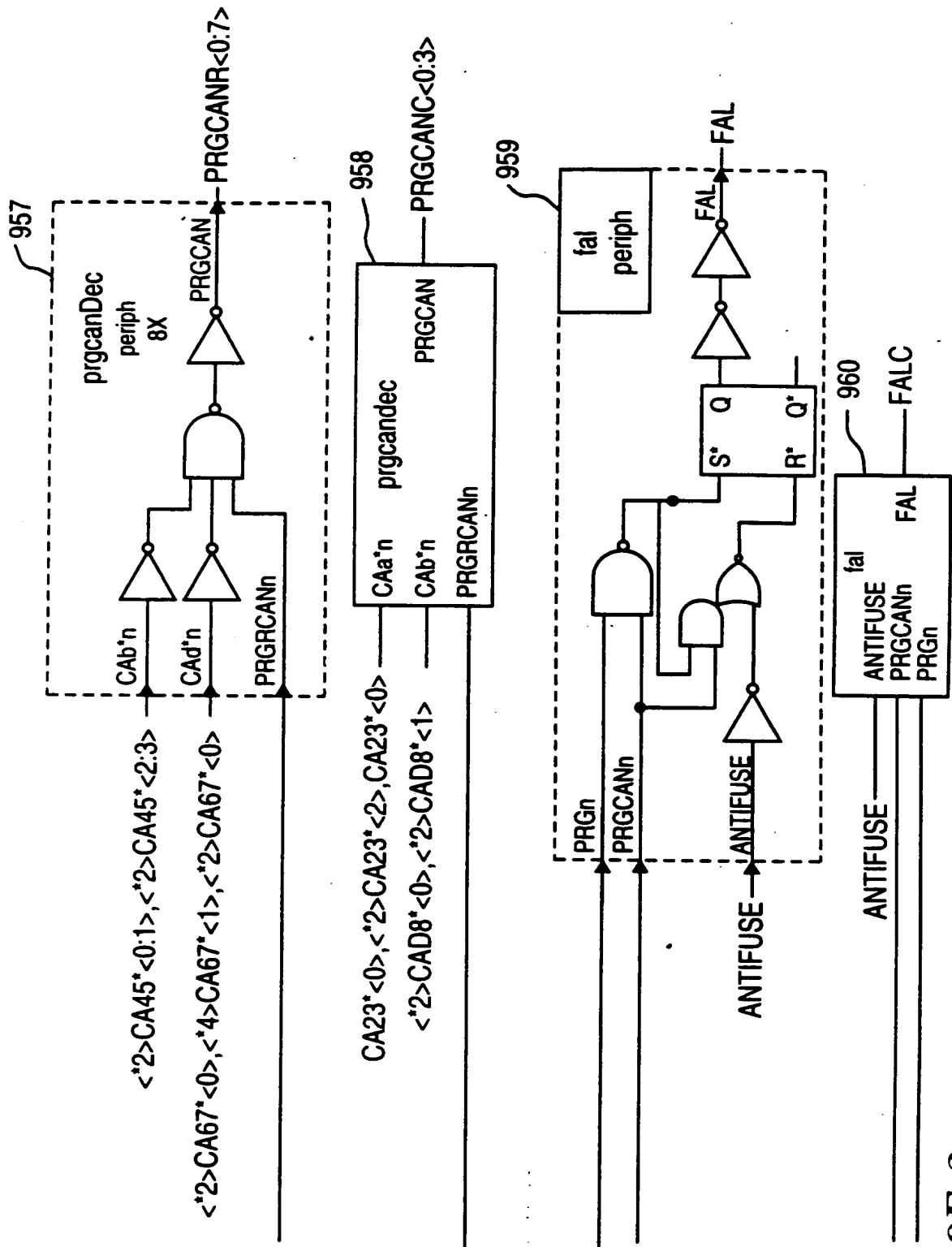


FIG. 59E



THE DEEDS OF THE THREE



To
FIG.
59F-1

FIG. 59F-2

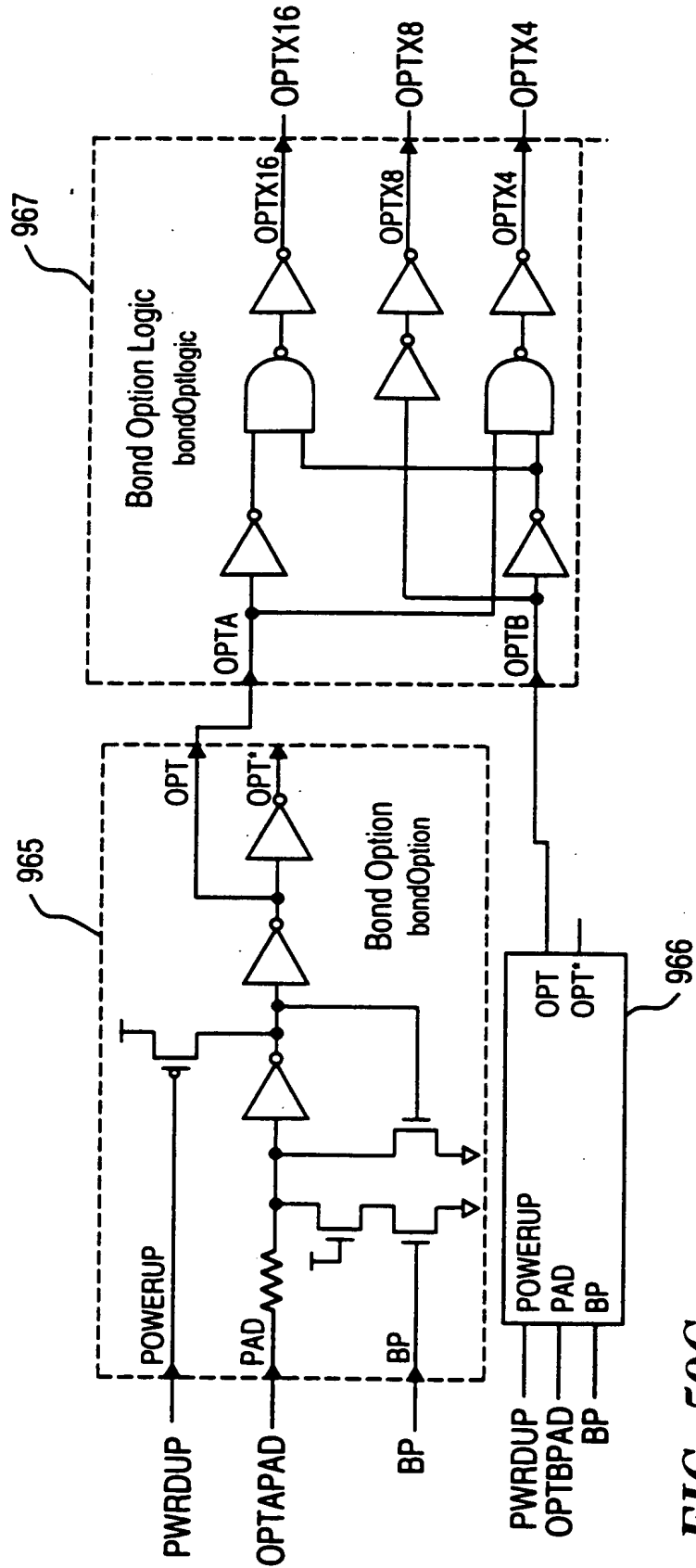


FIG. 59C

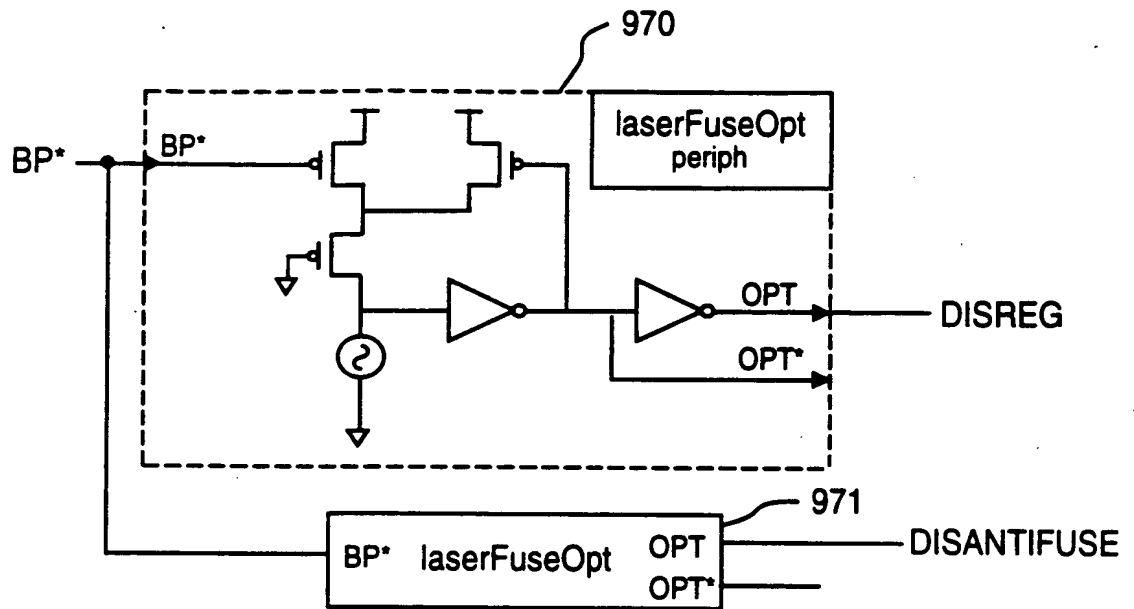


FIG. 59H

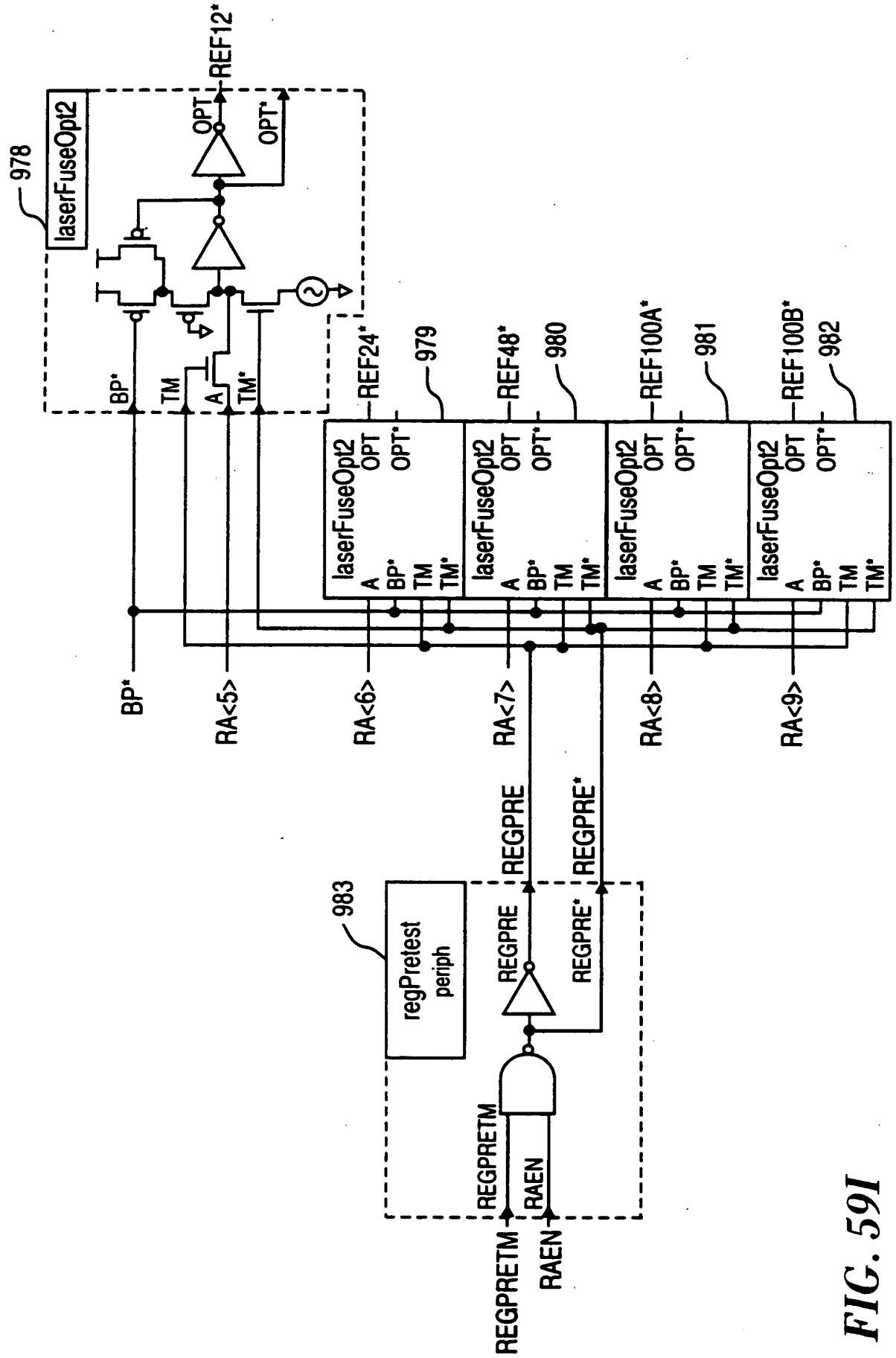
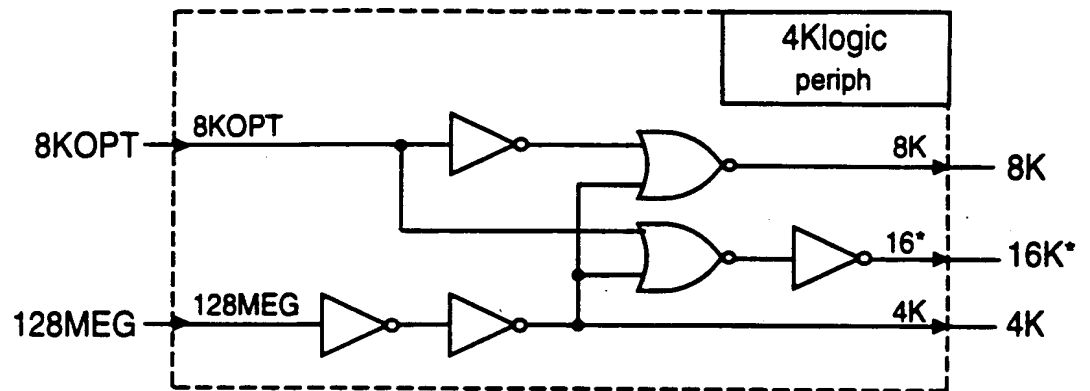
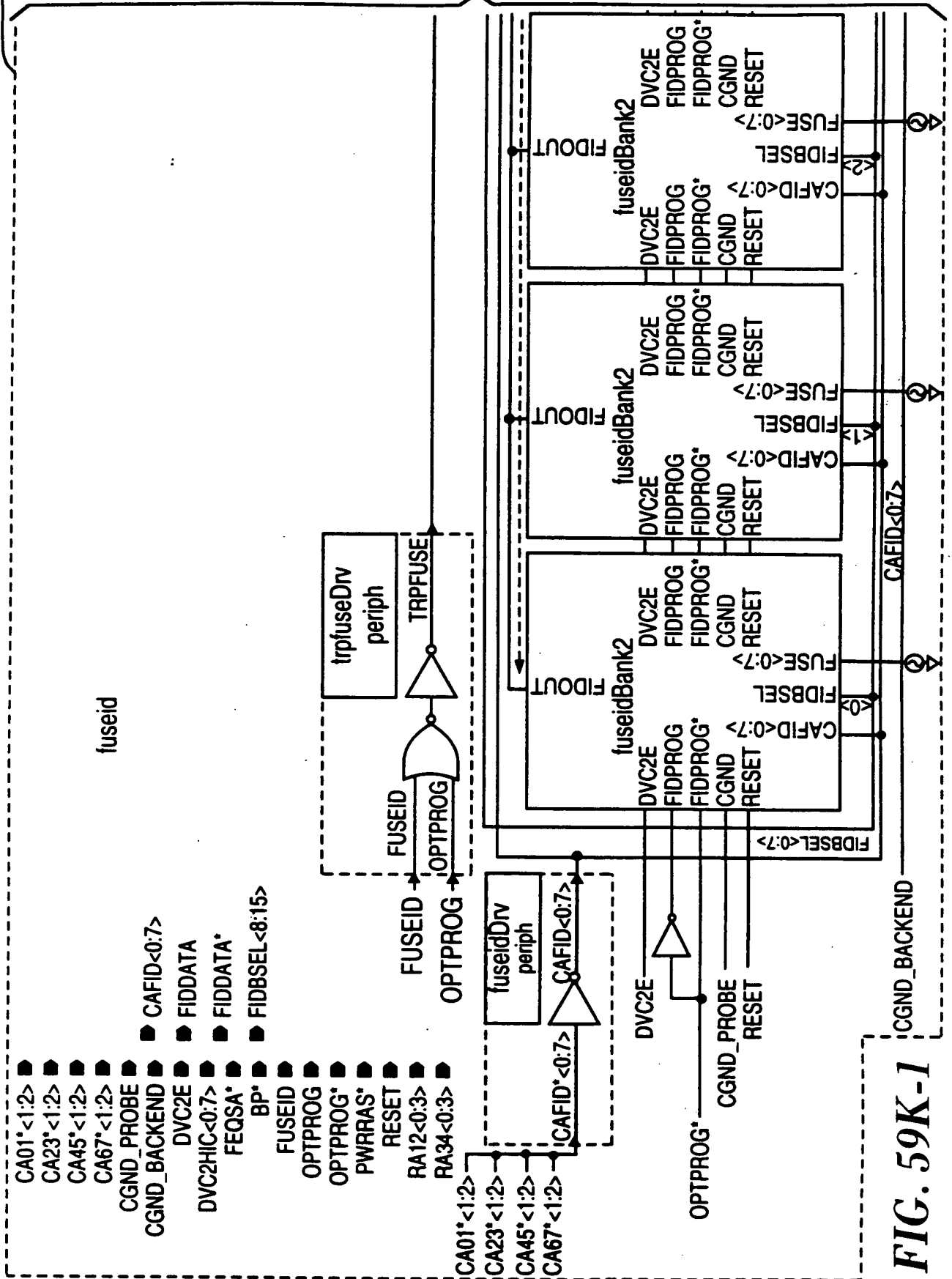


FIG. 59I

**FIG. 59J**





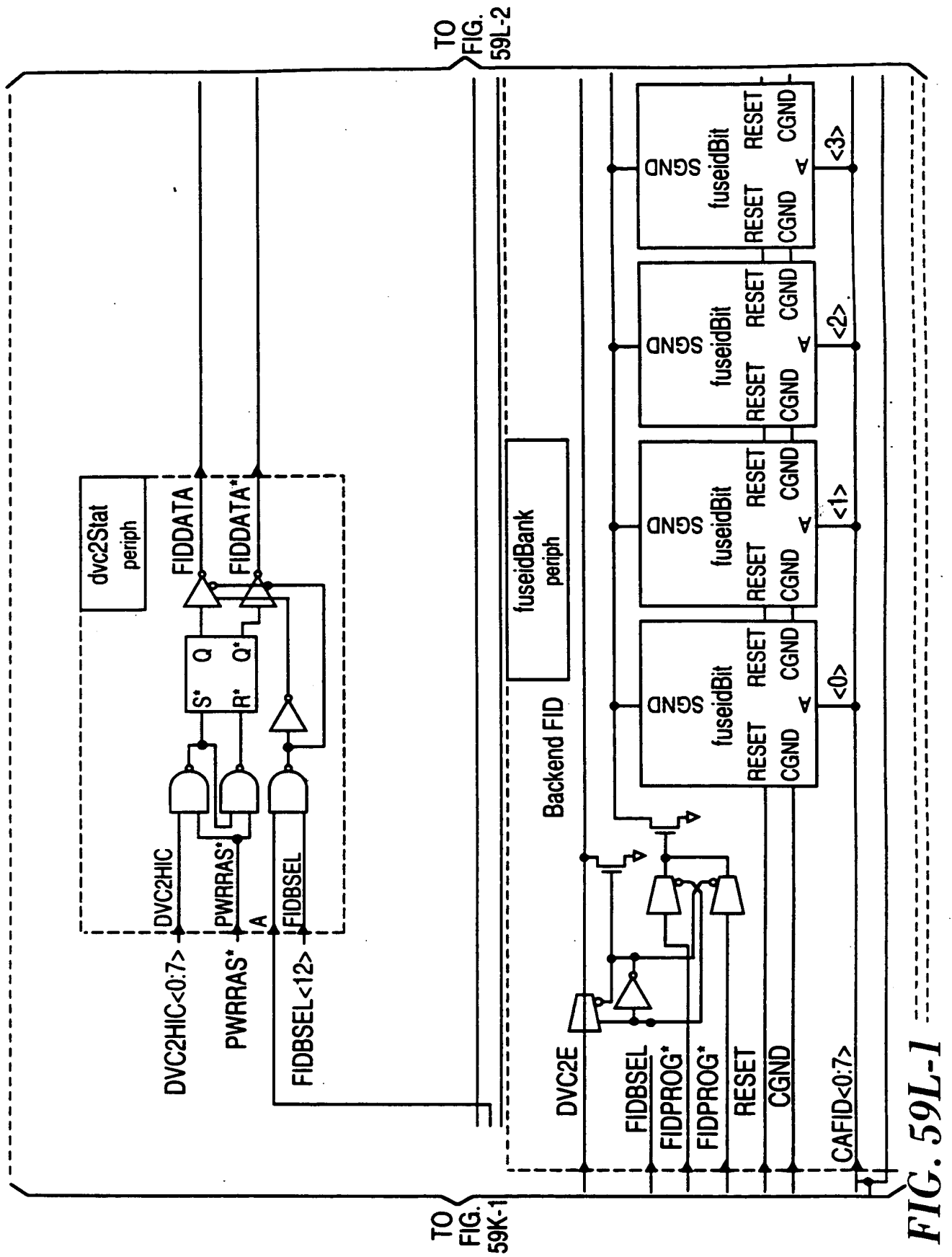
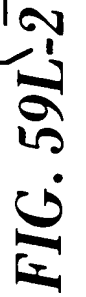


FIG. 59L-1



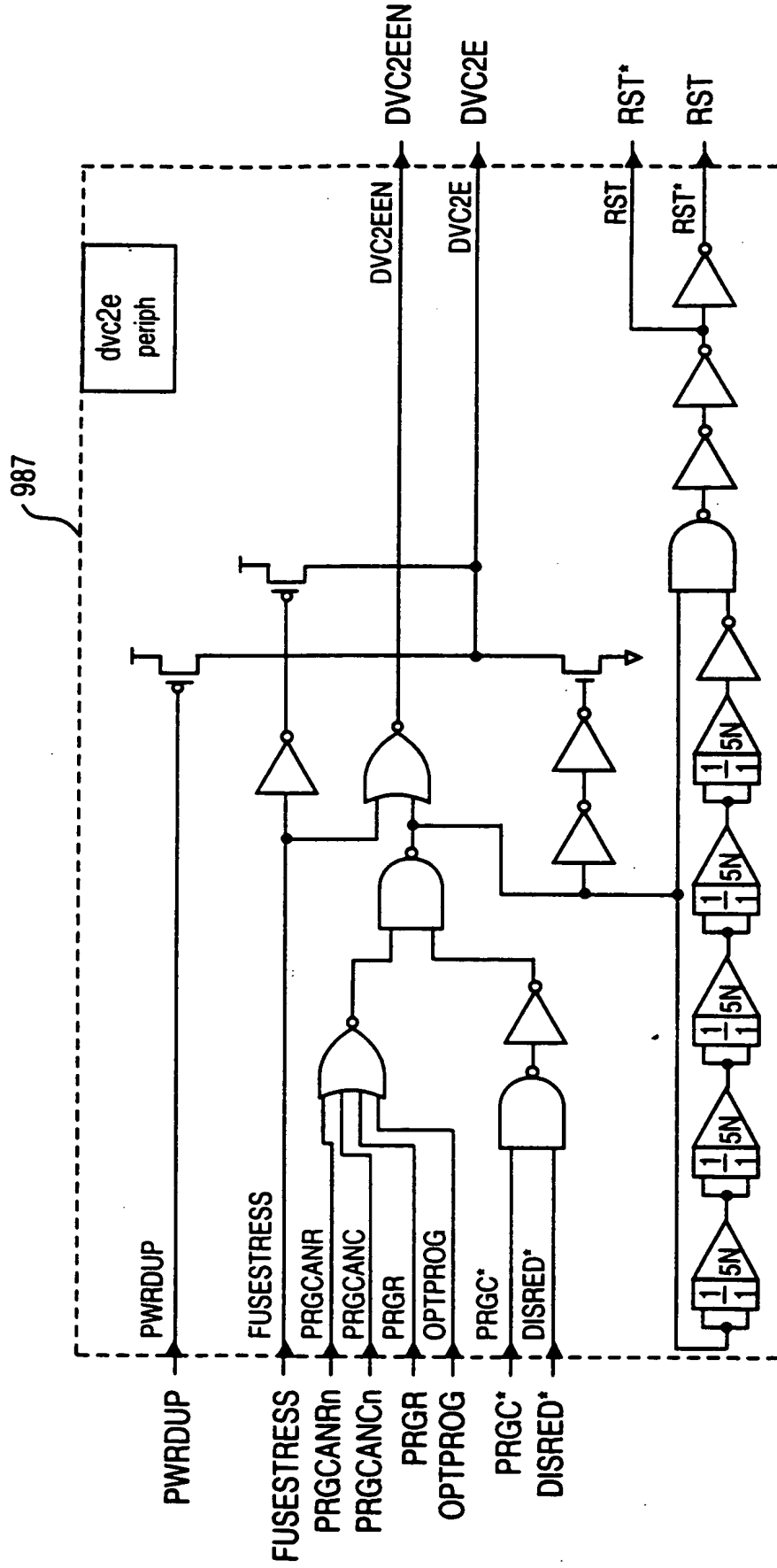
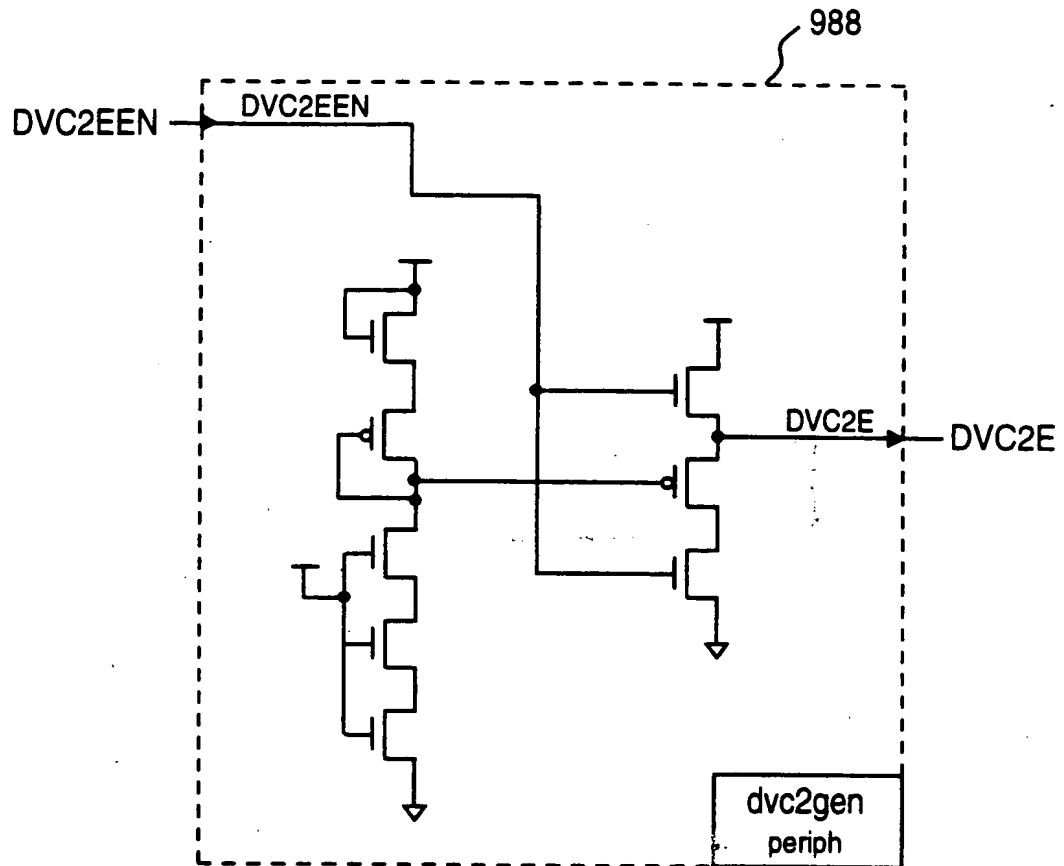


FIG. 59M

**FIG. 59N**

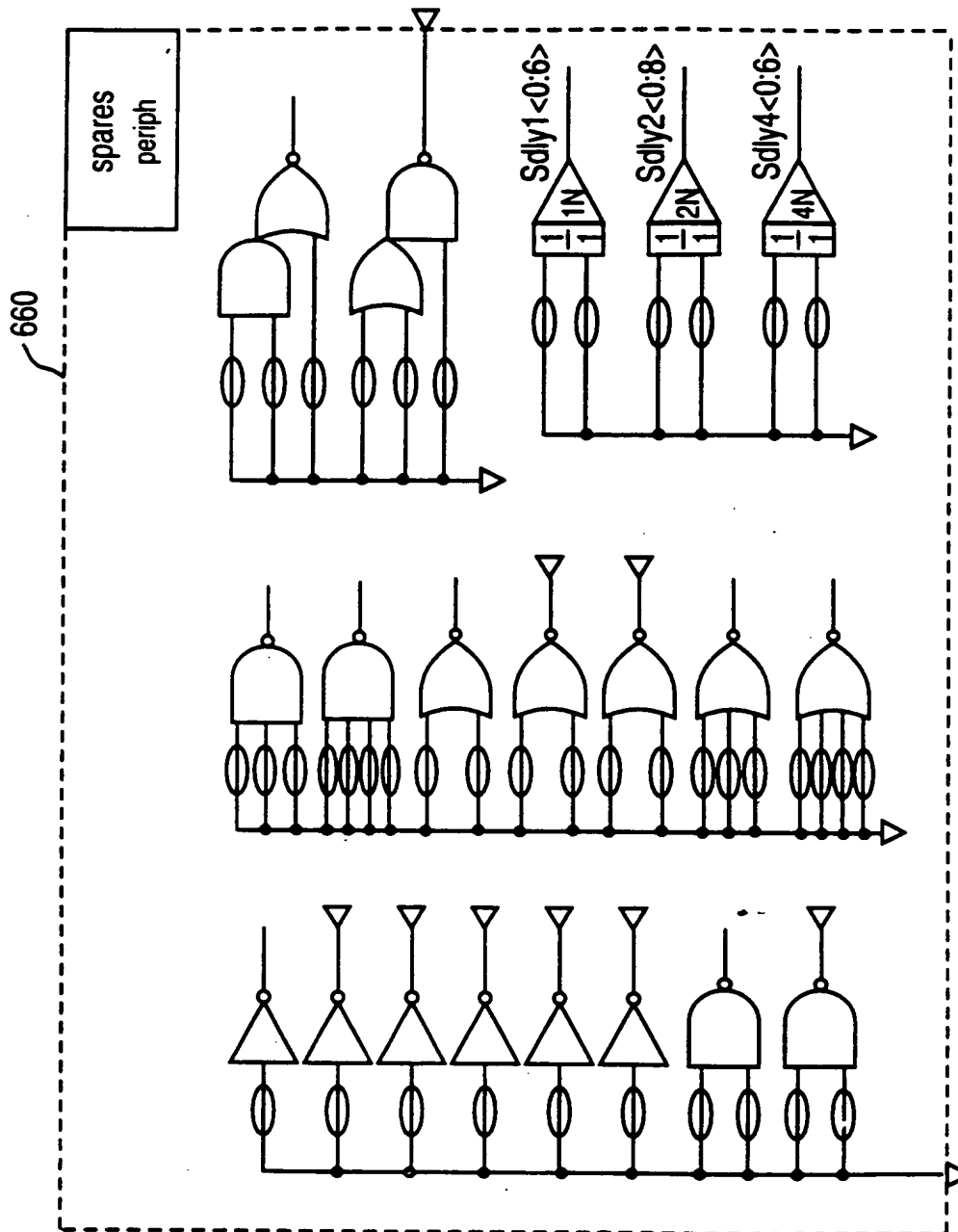
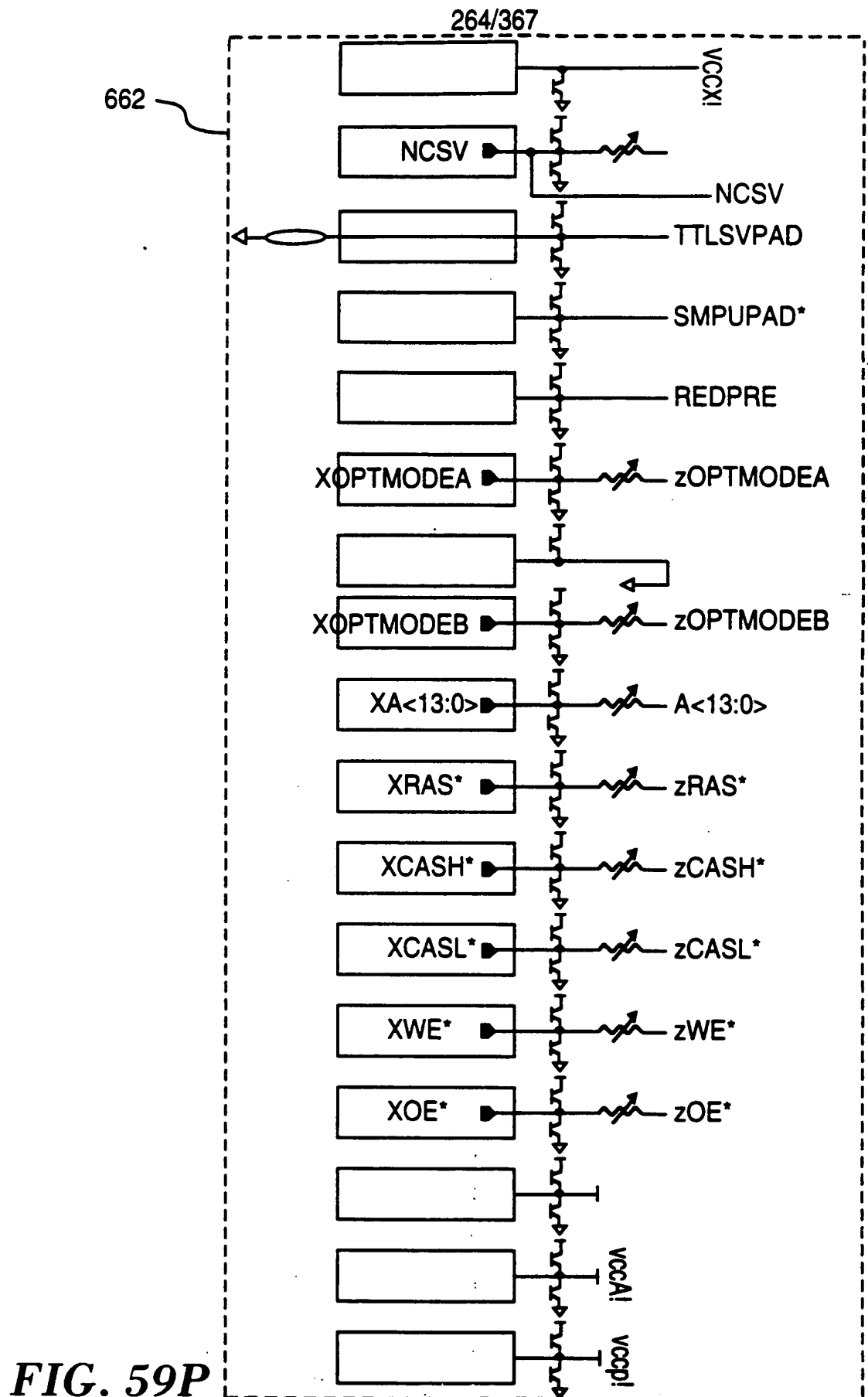


FIG. 590



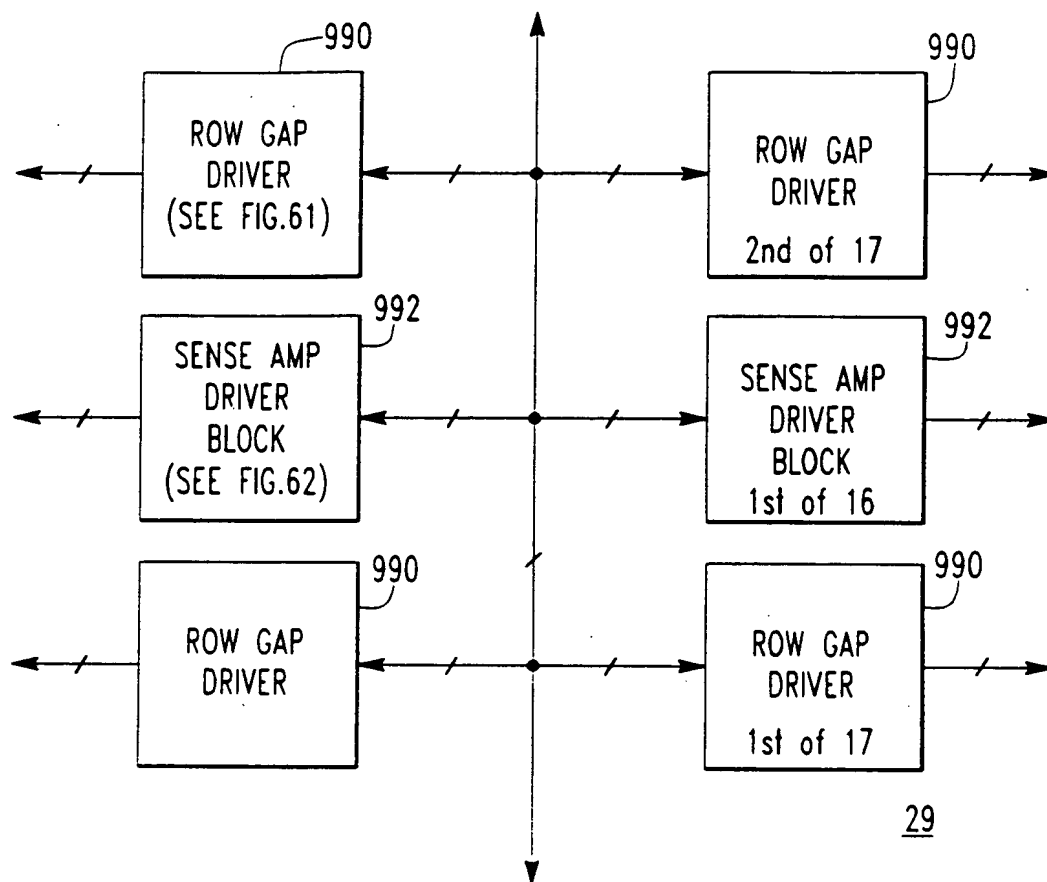


FIG. 60

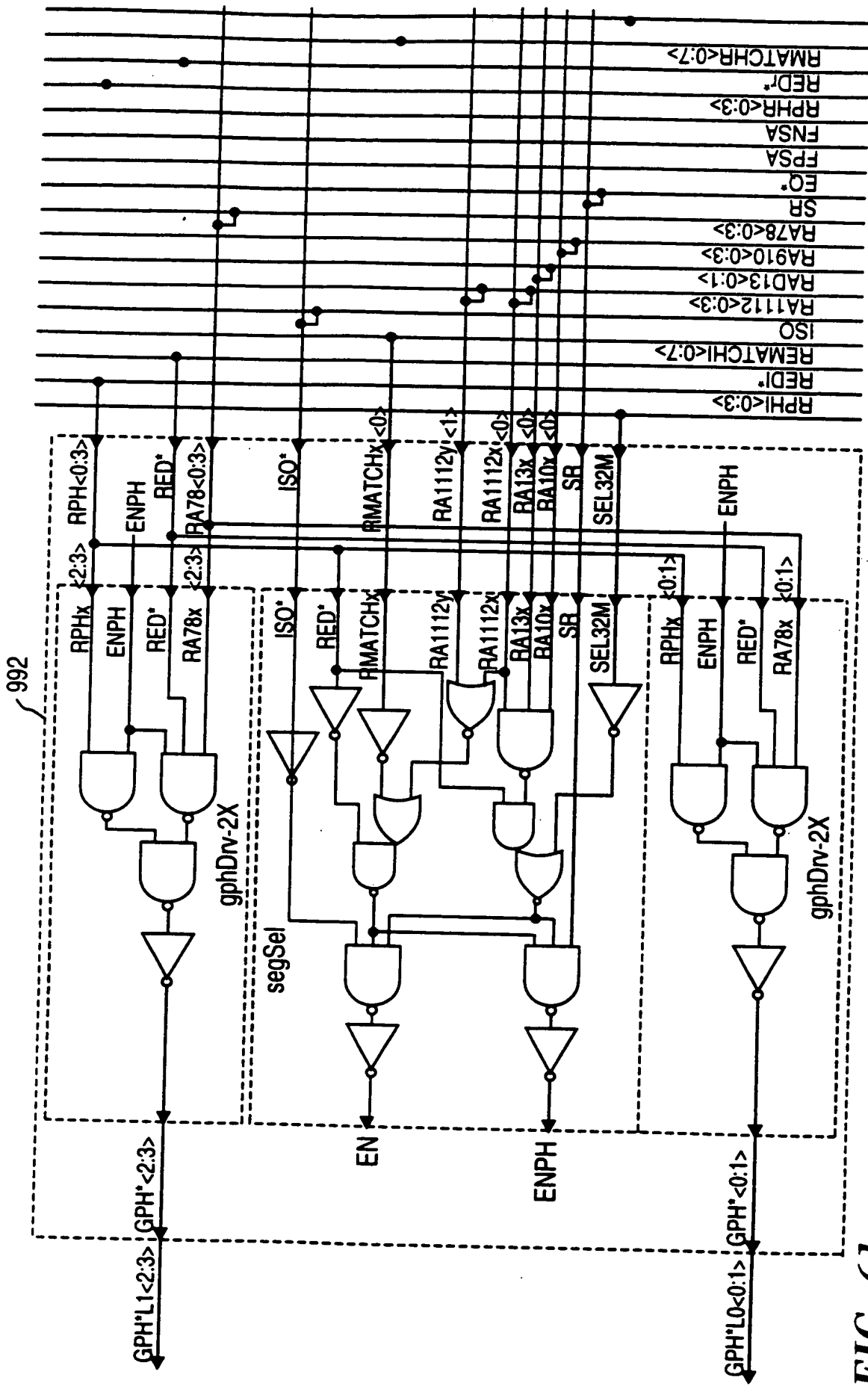


FIG. 61

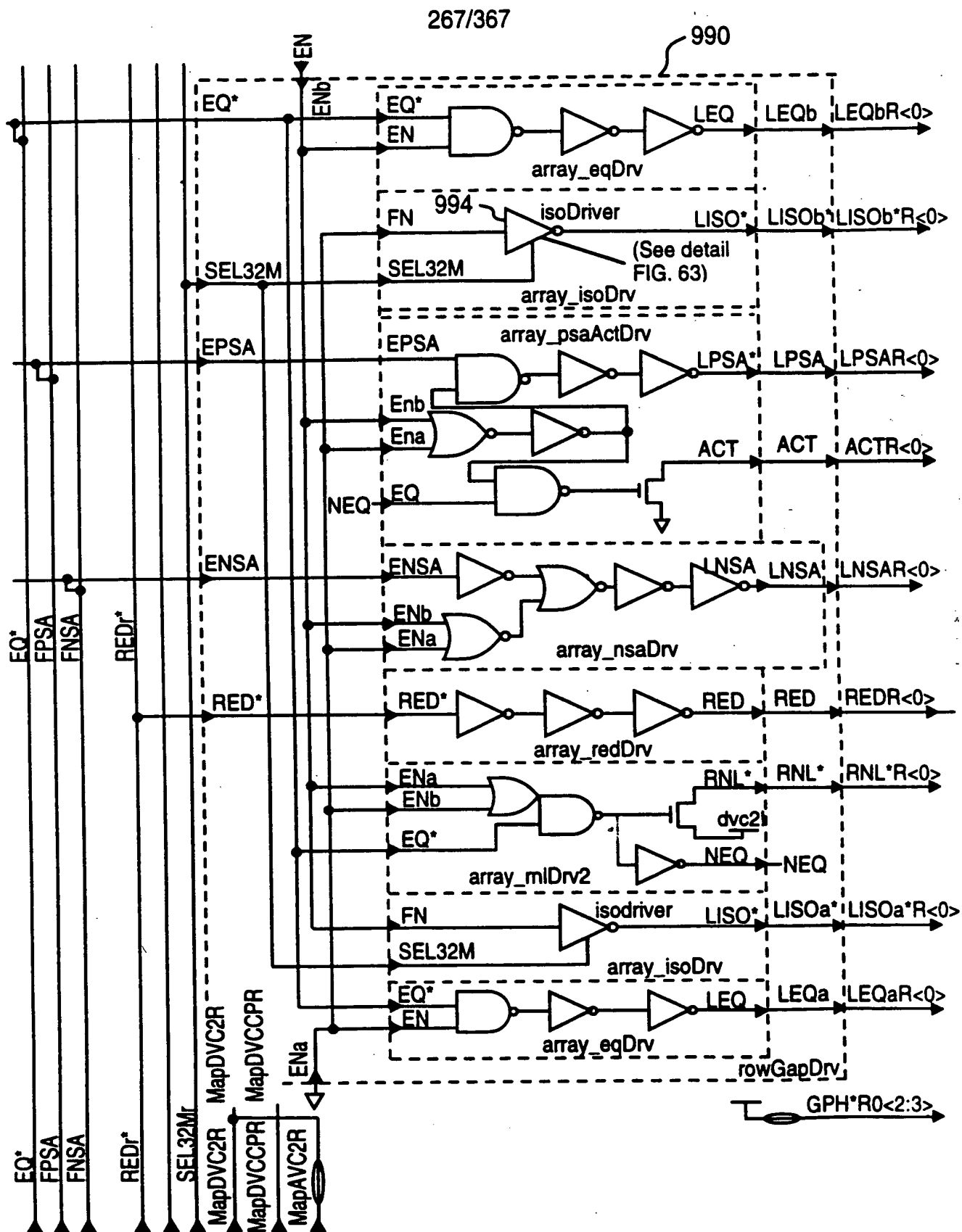


FIG. 62

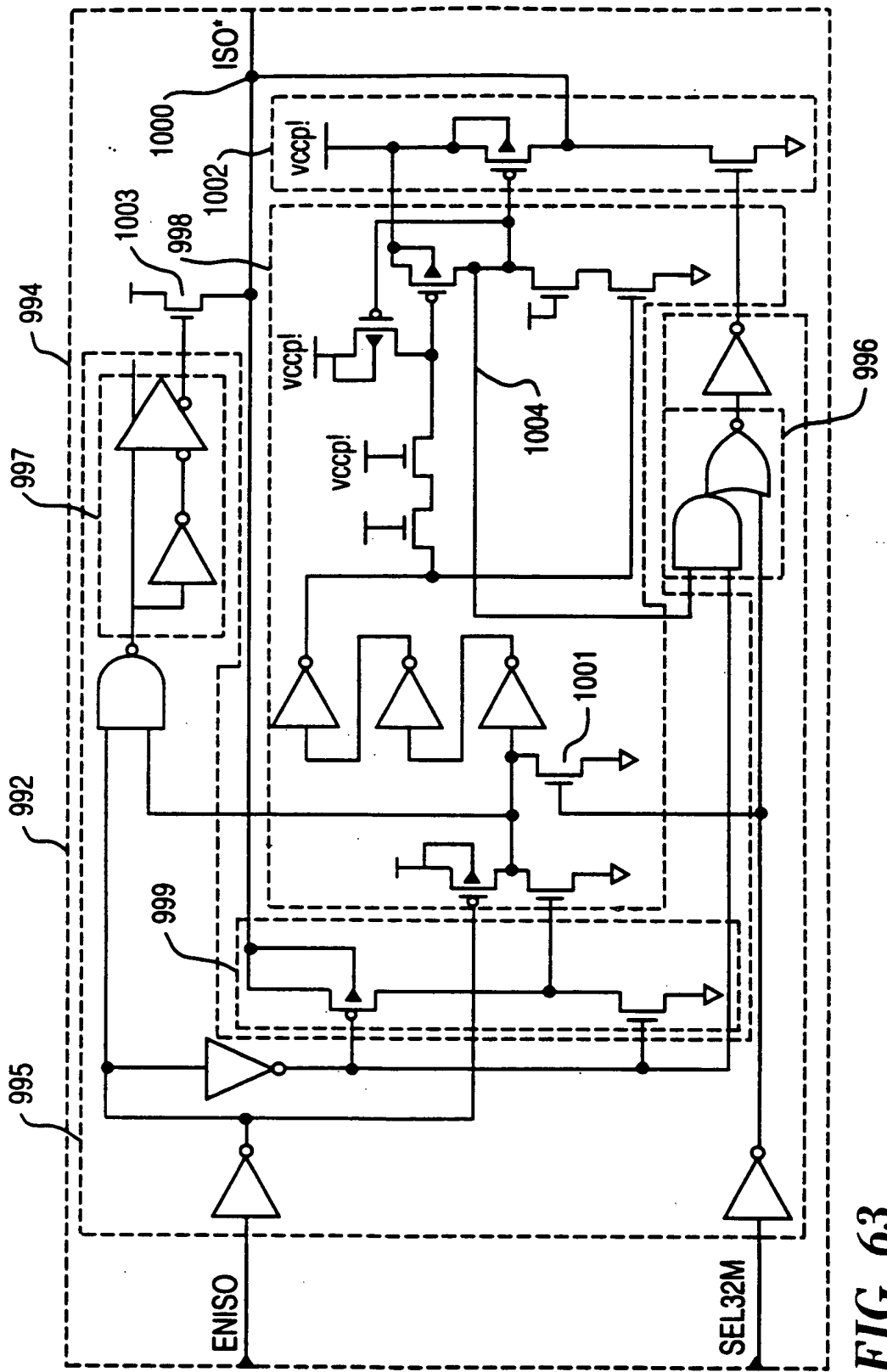


FIG. 63

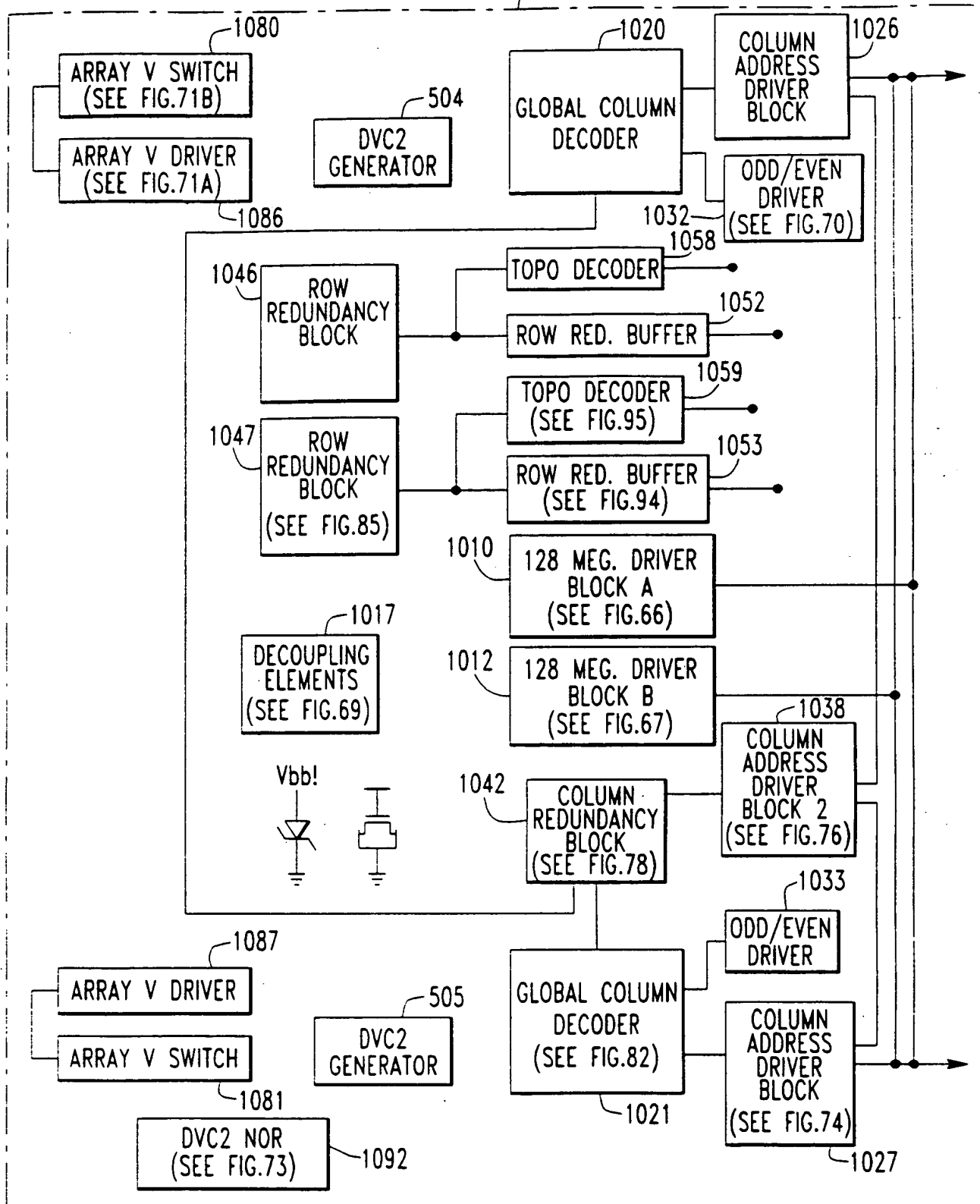
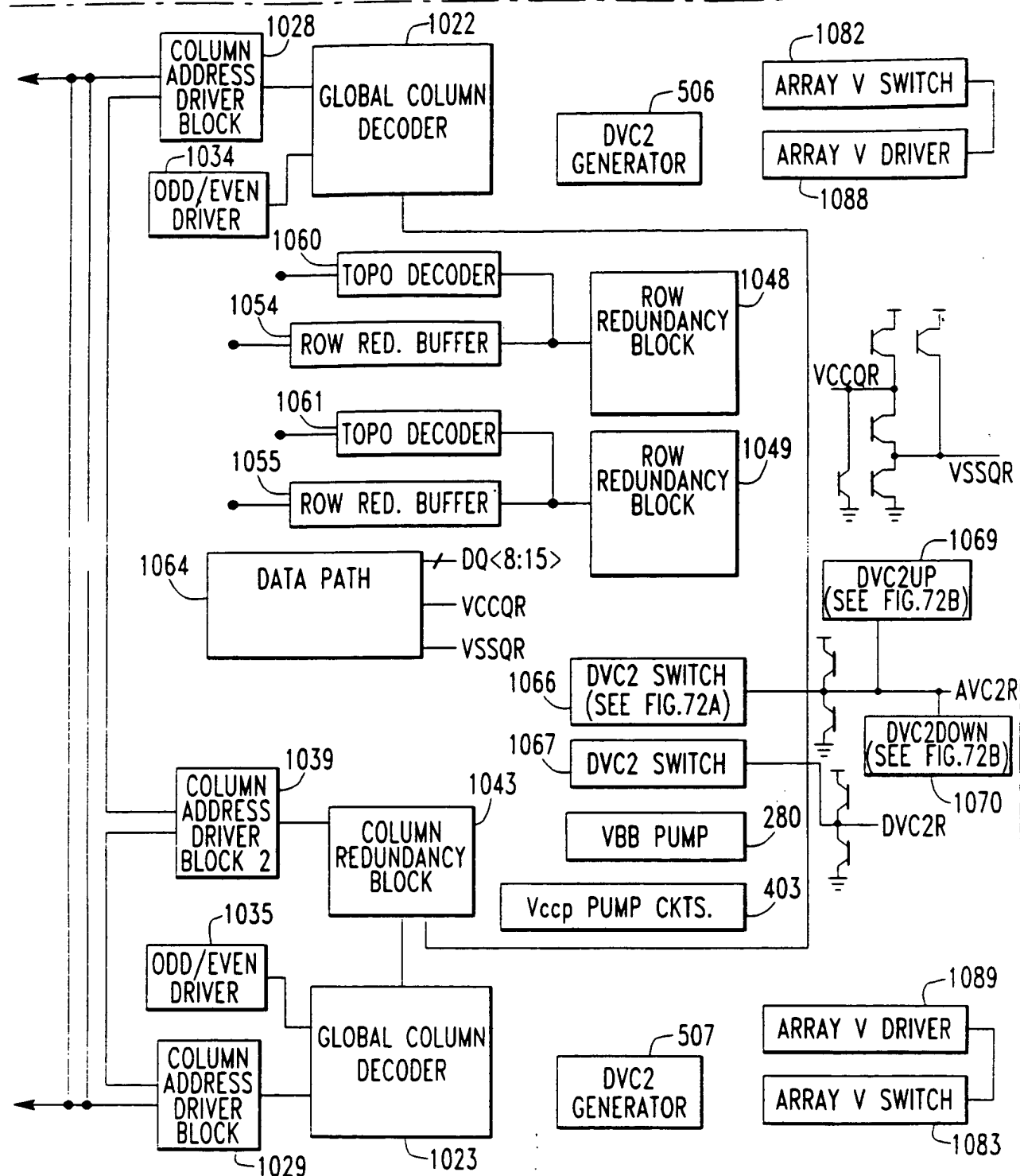


FIG. 64A



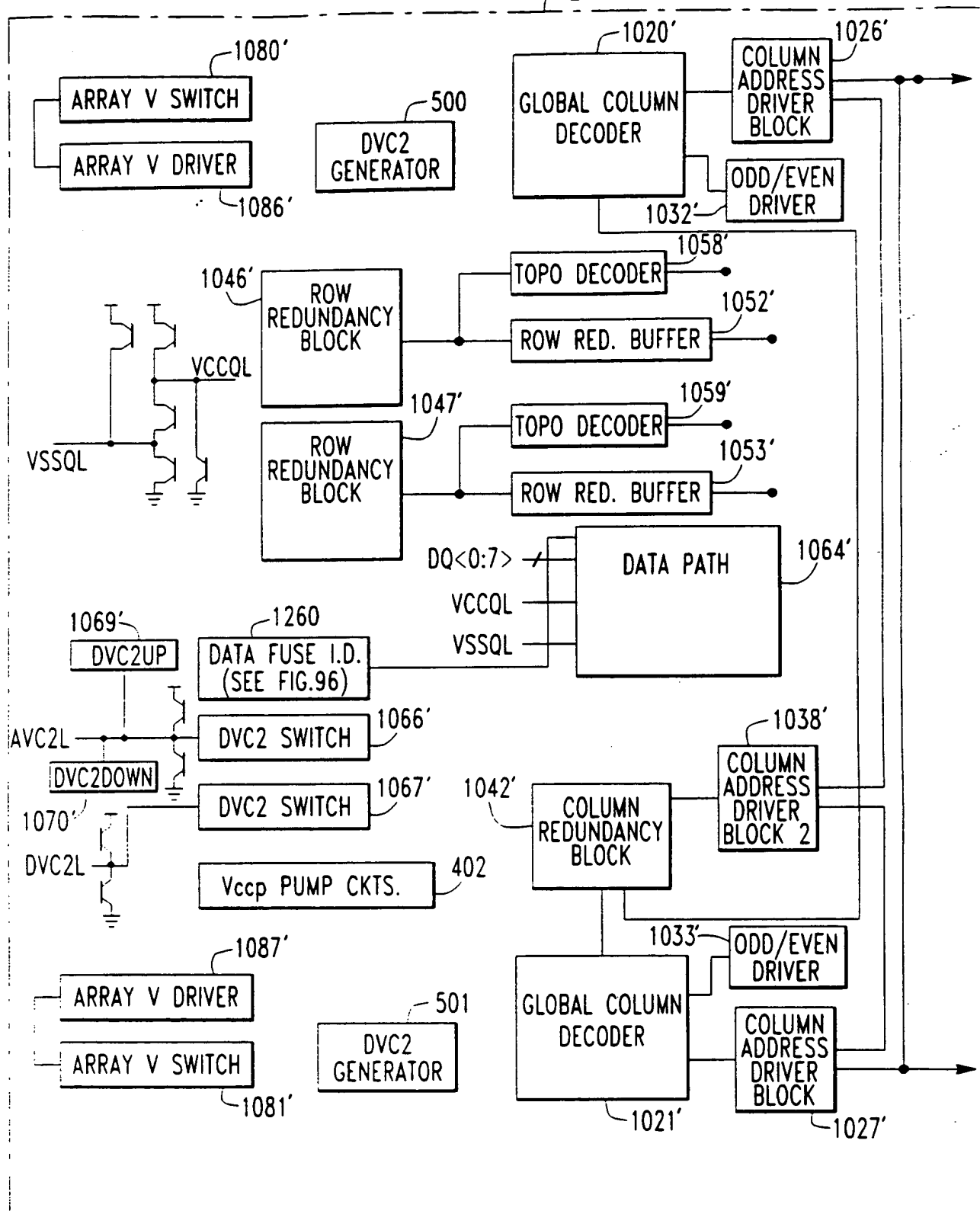


FIG. 65A

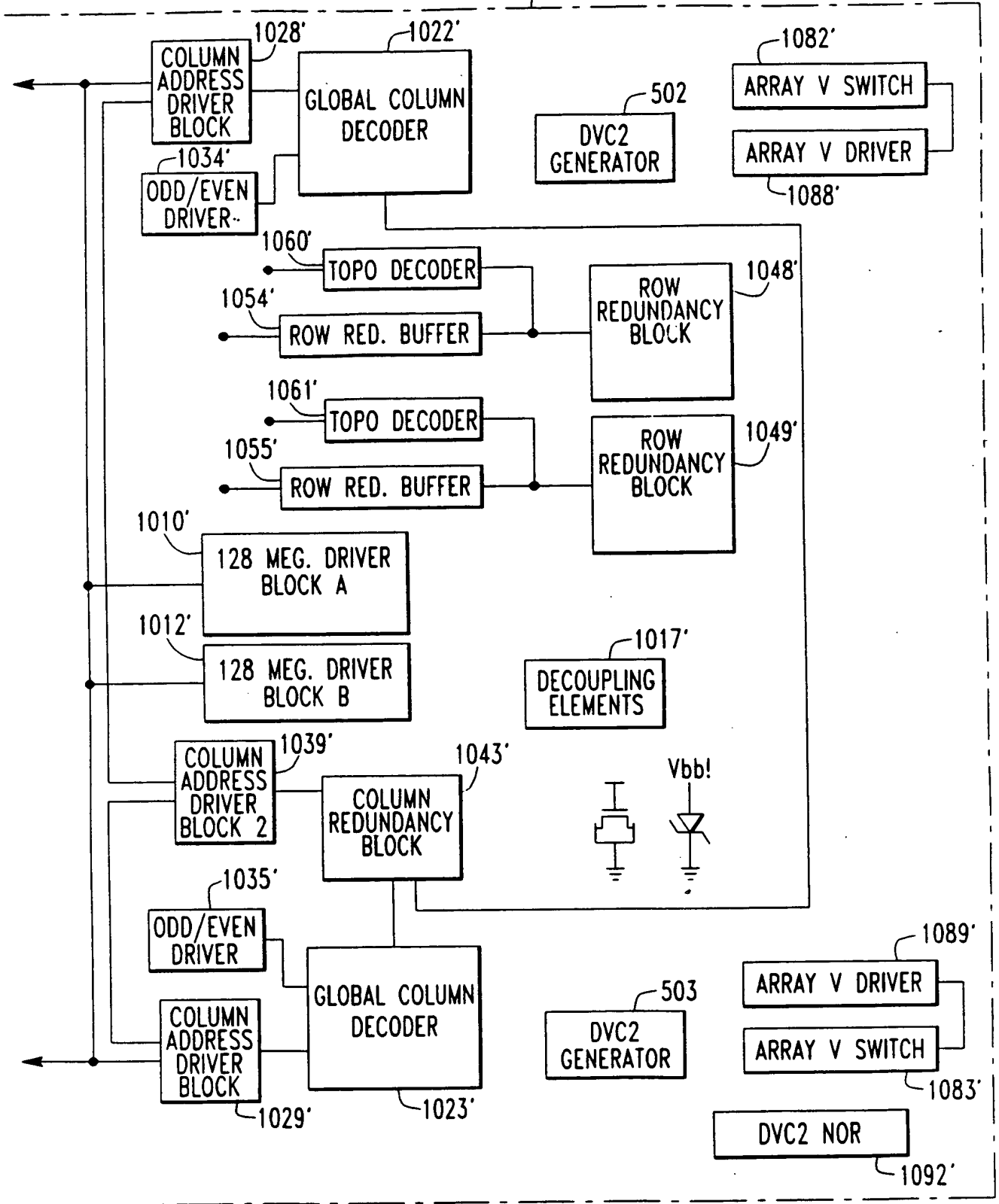
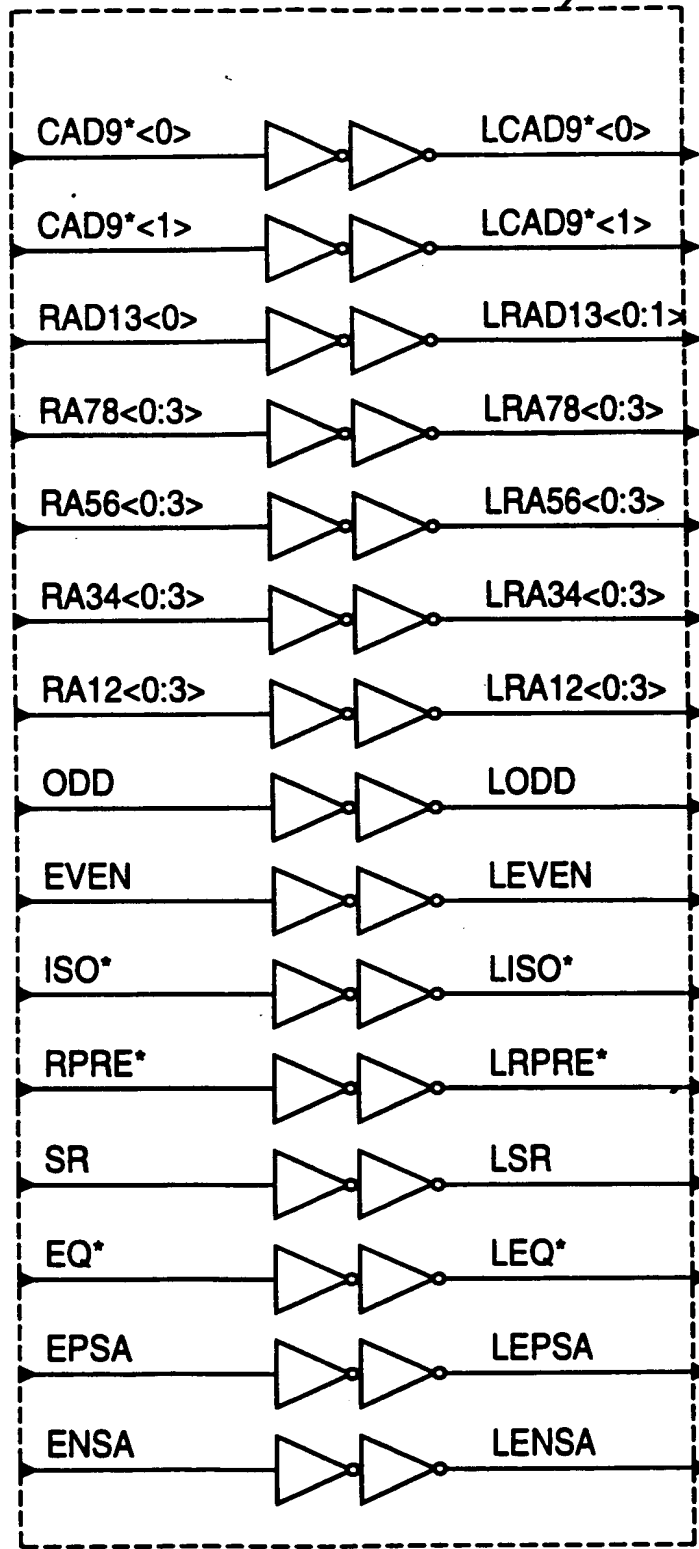


FIG. 65B

**FIG. 66**

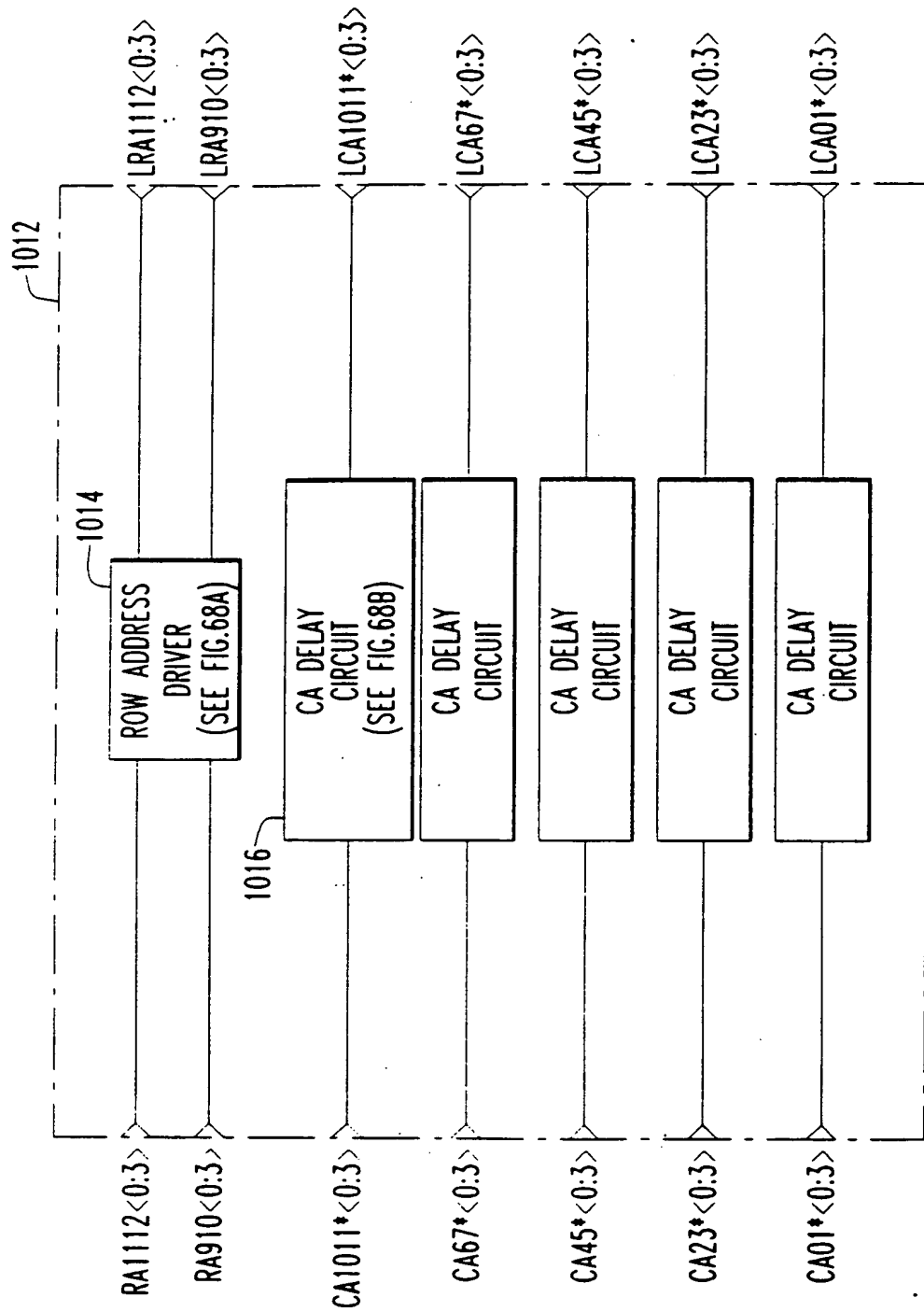
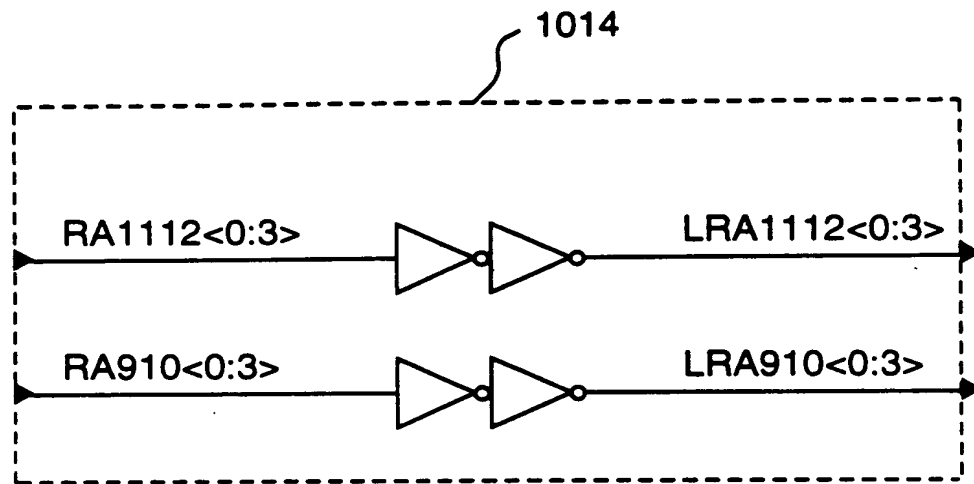
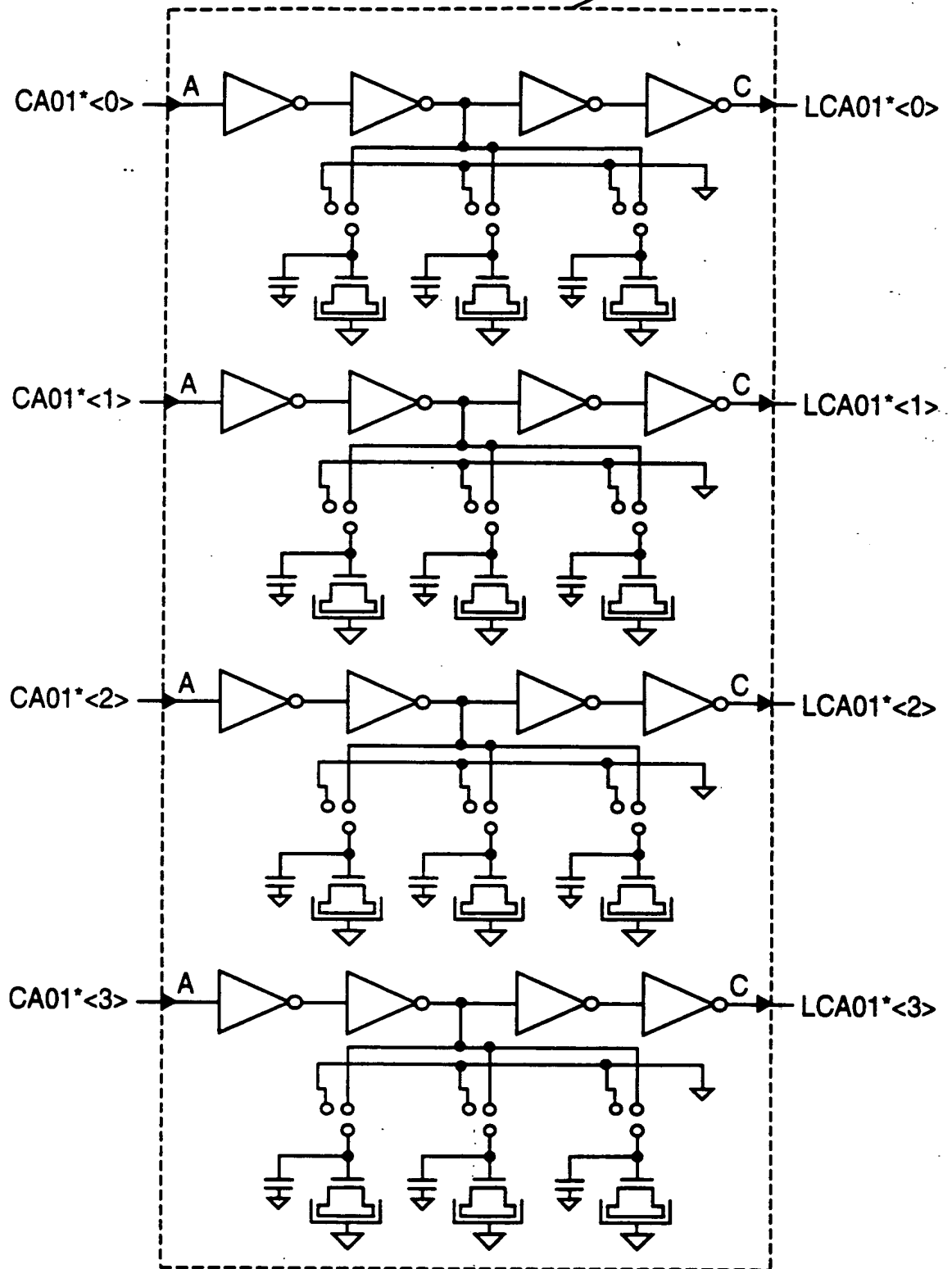
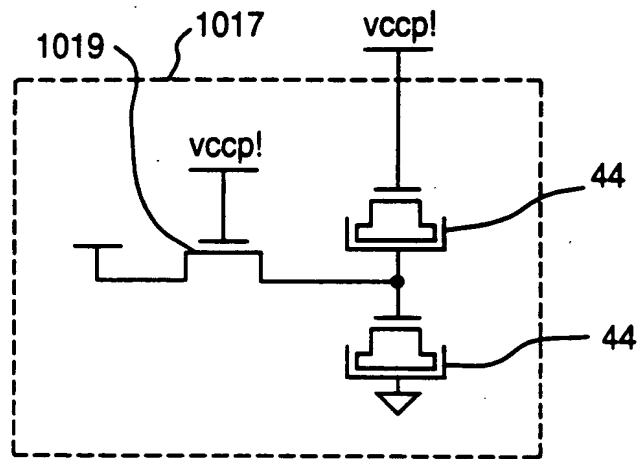
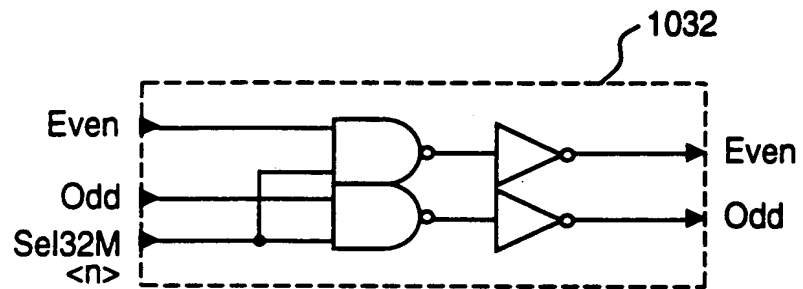


FIG. 67

**FIG. 68A**

**FIG. 68B**

**FIG. 69**

**FIG. 70**

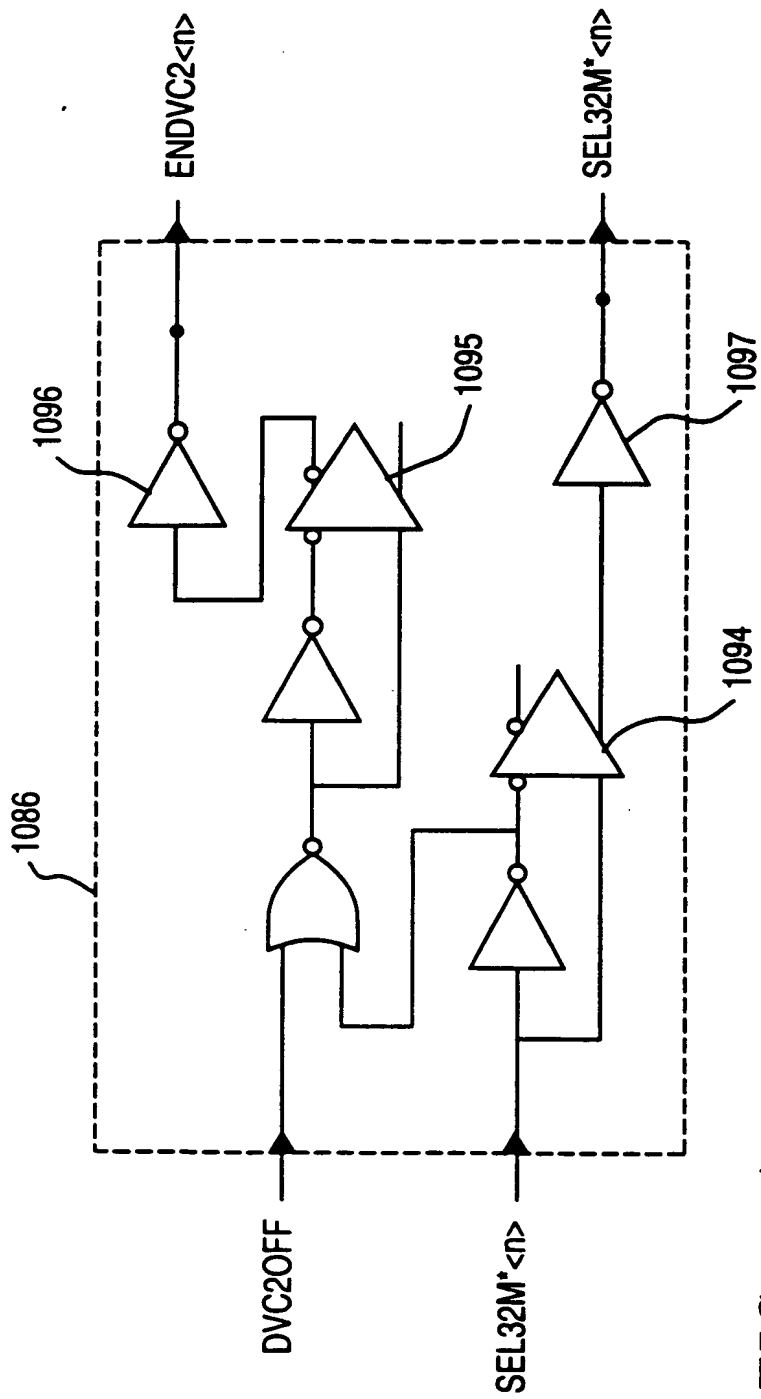
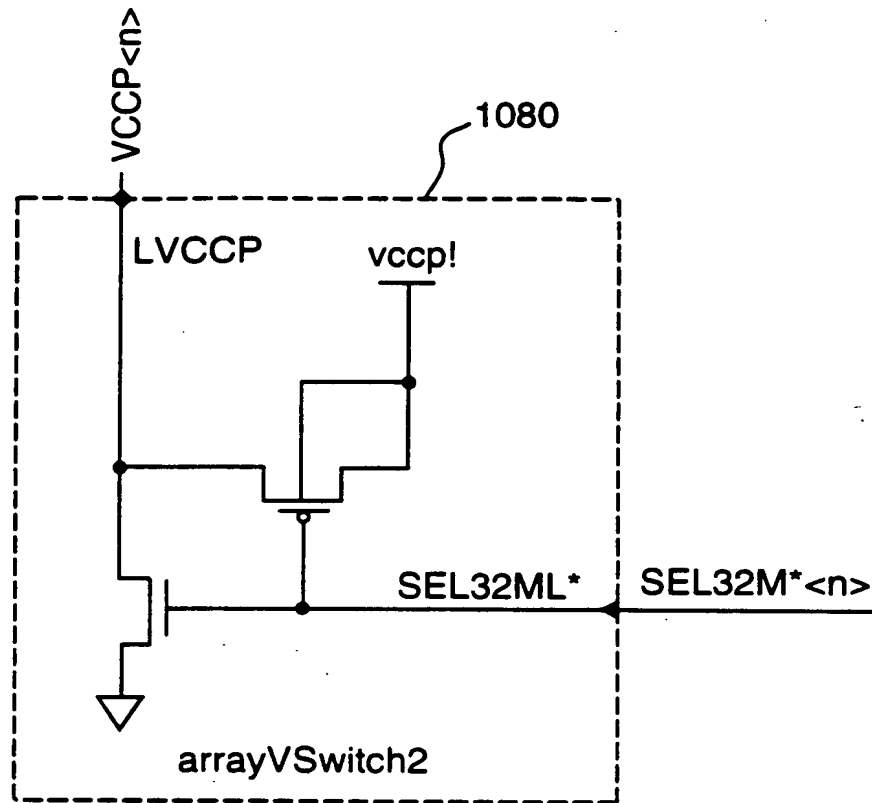


FIG. 71A

**FIG. 71B**

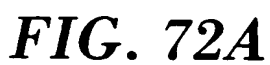
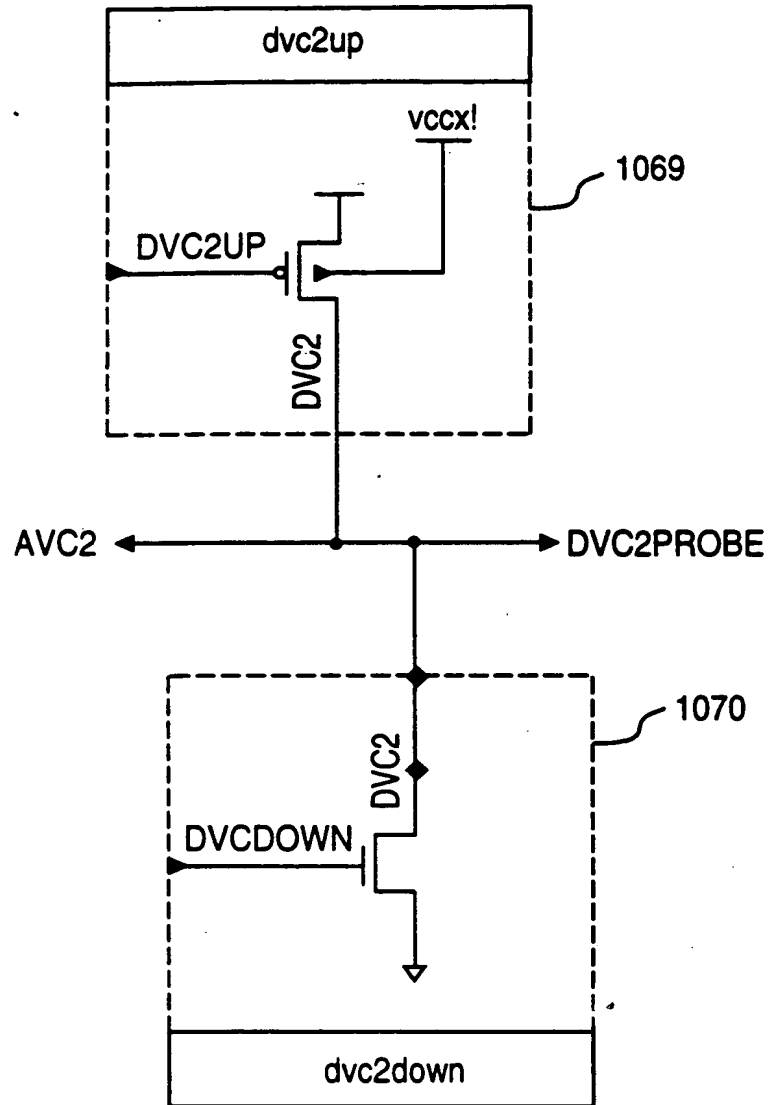
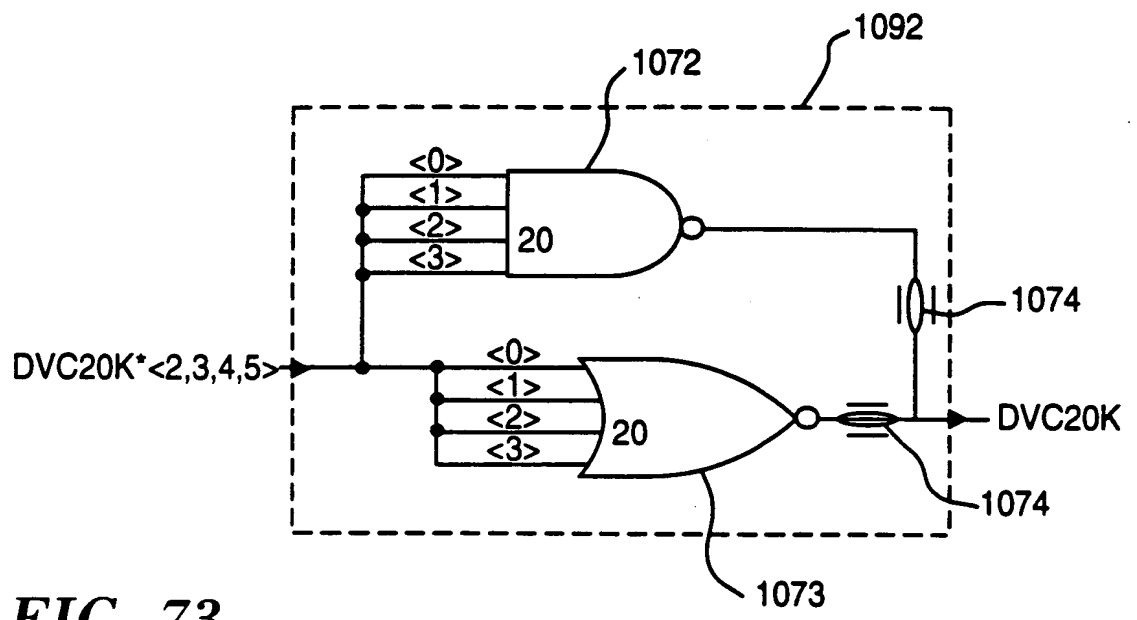


FIG. 72A

**FIG. 72B**

**FIG. 73**

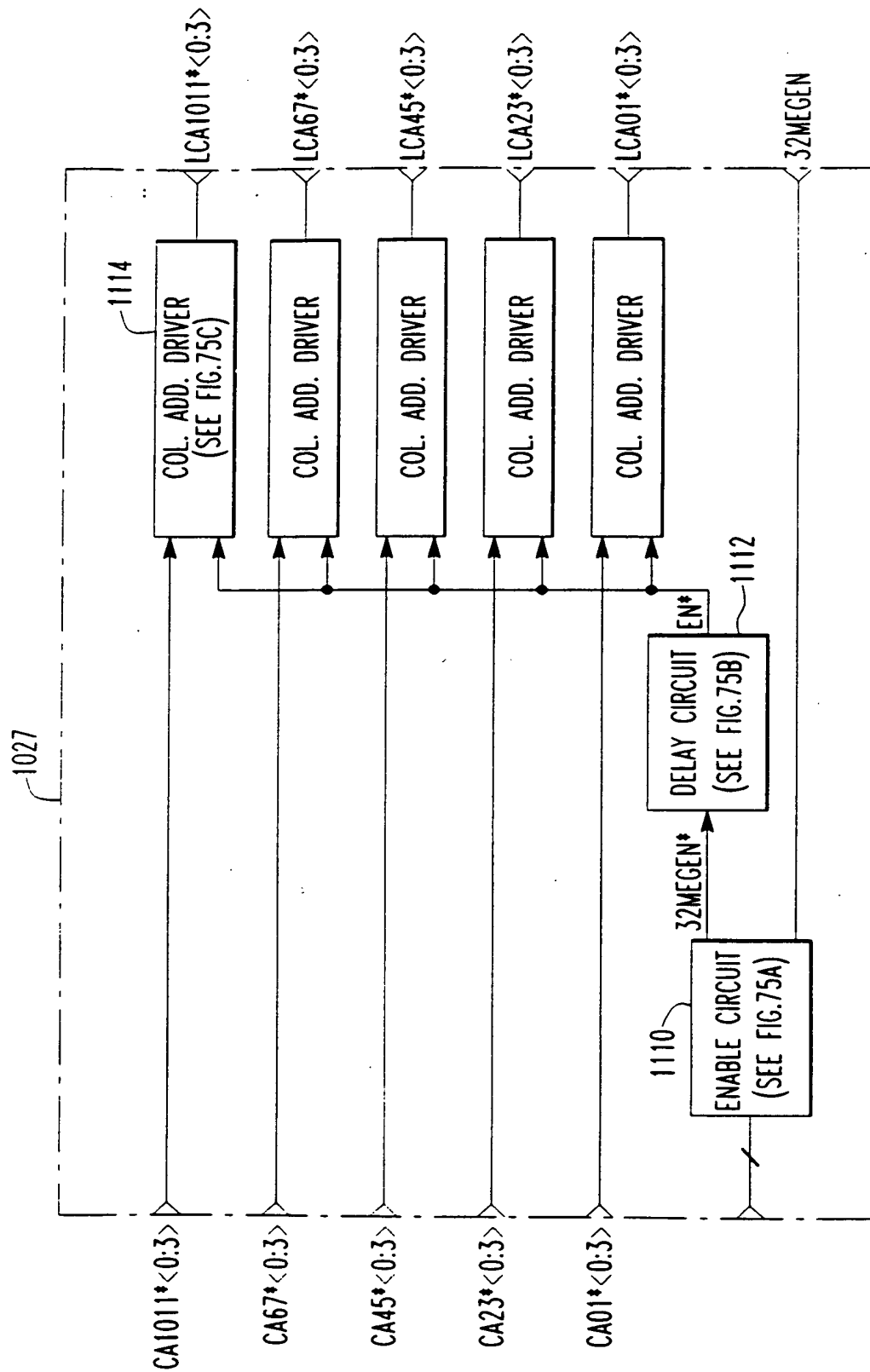


FIG. 74

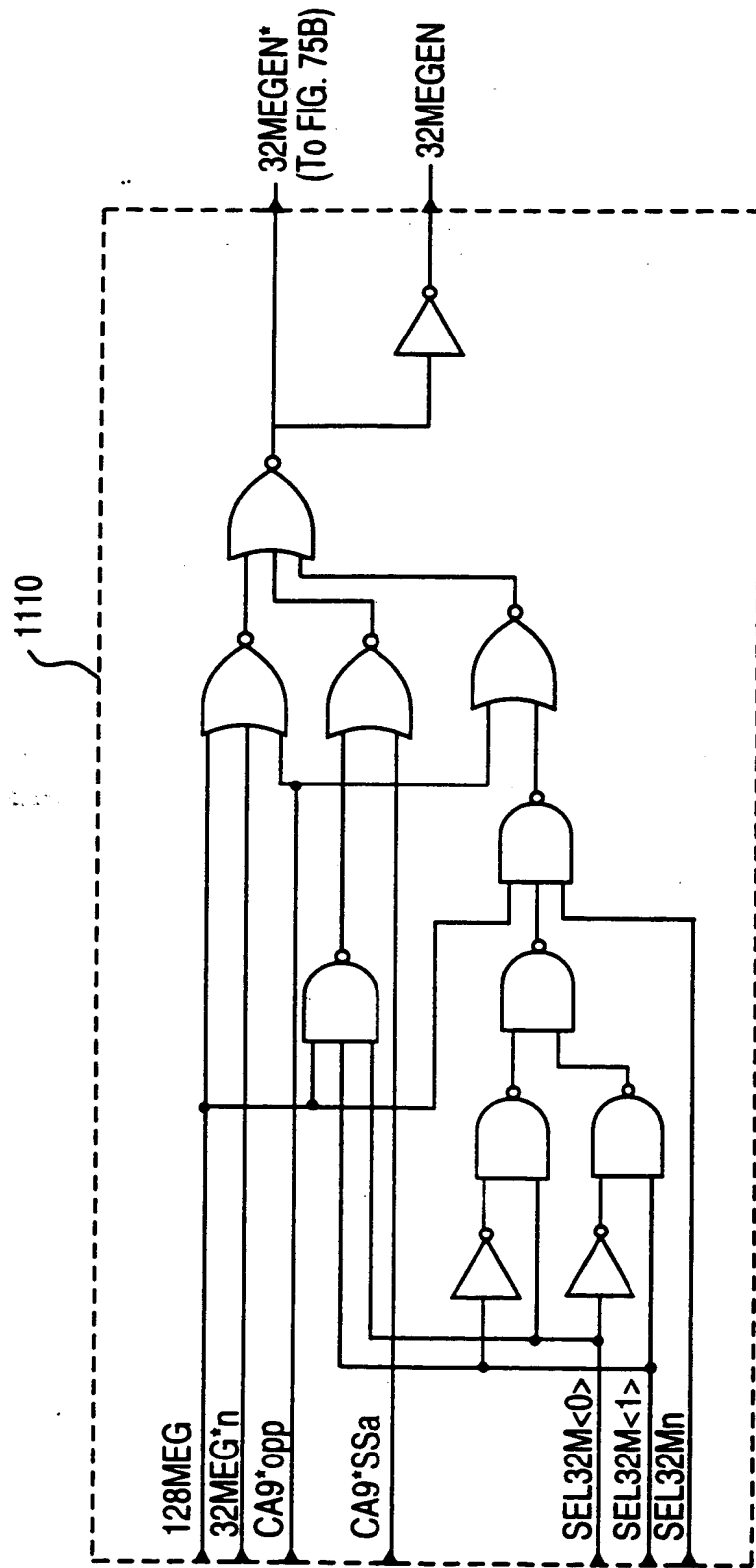


FIG. 75A

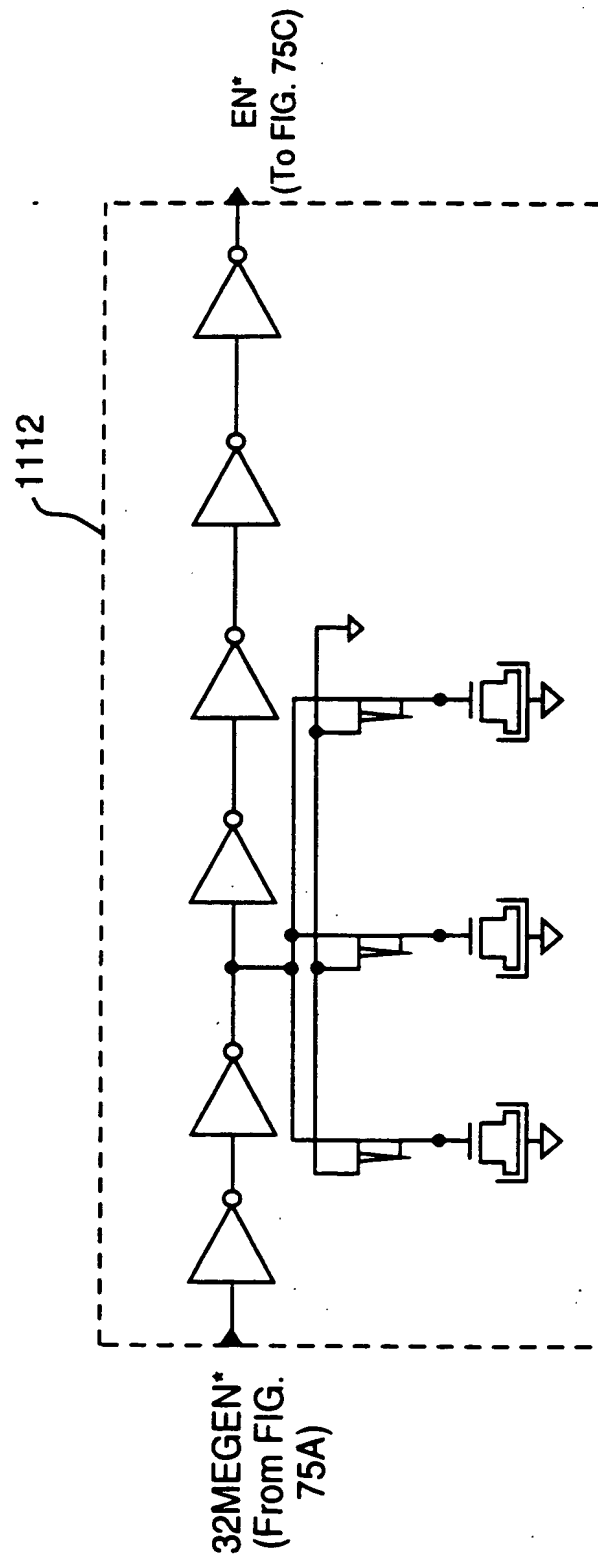


FIG. 75B

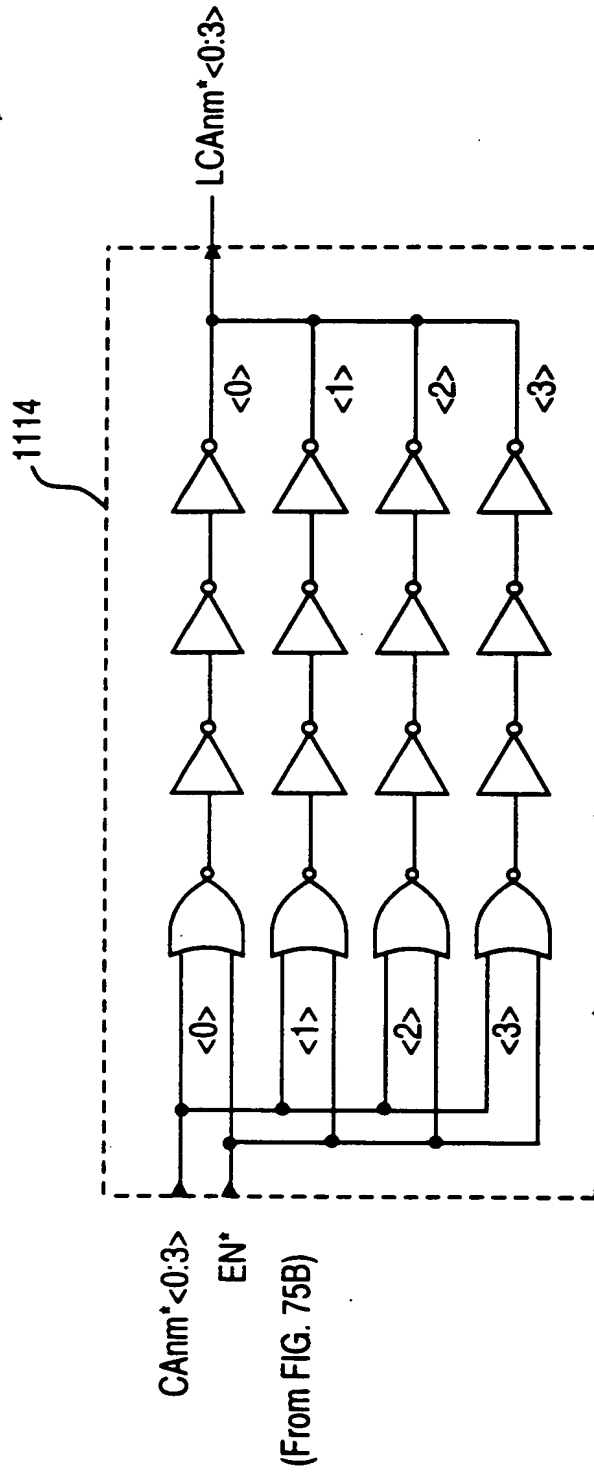


FIG. 75C

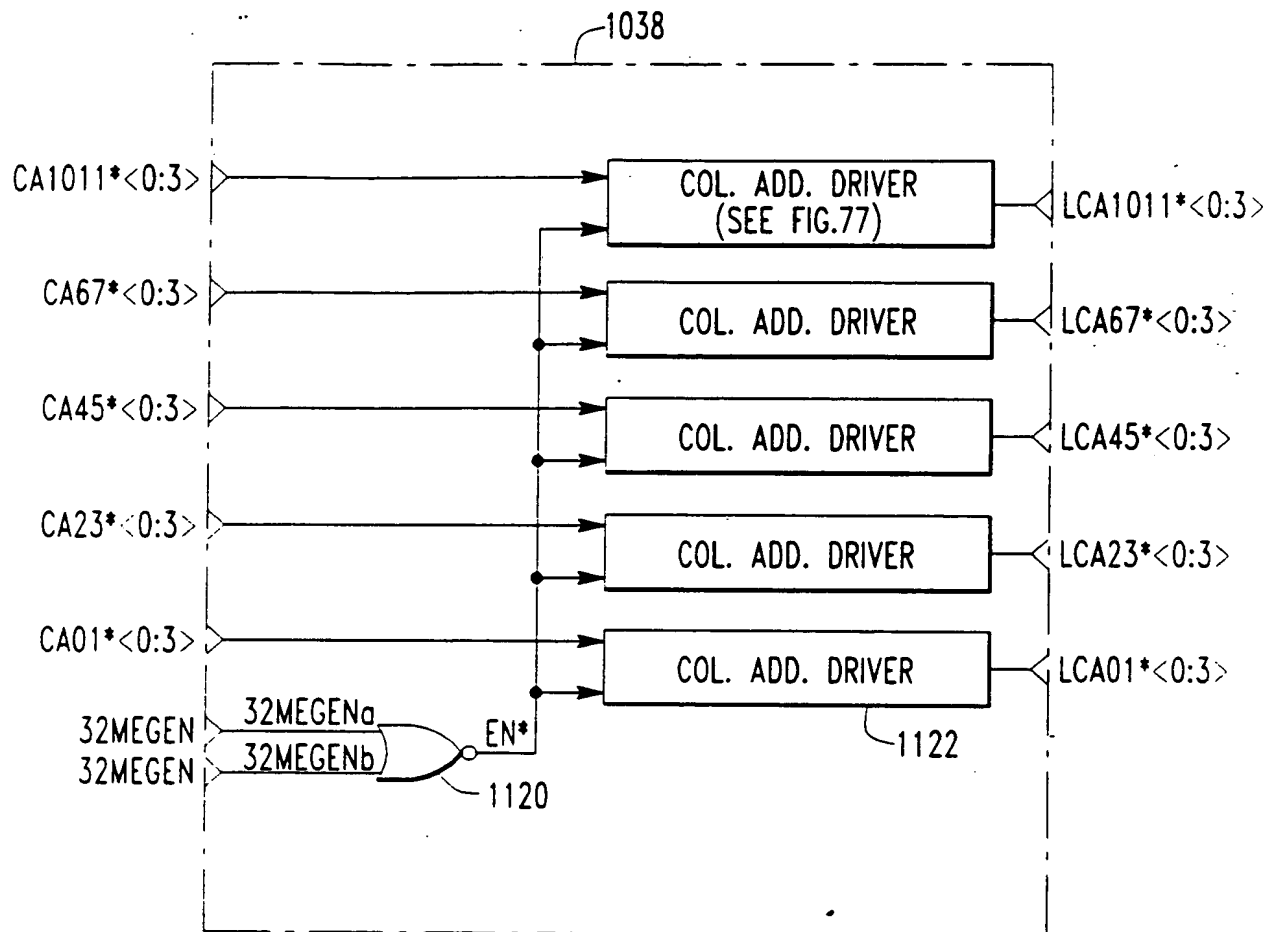


FIG. 76

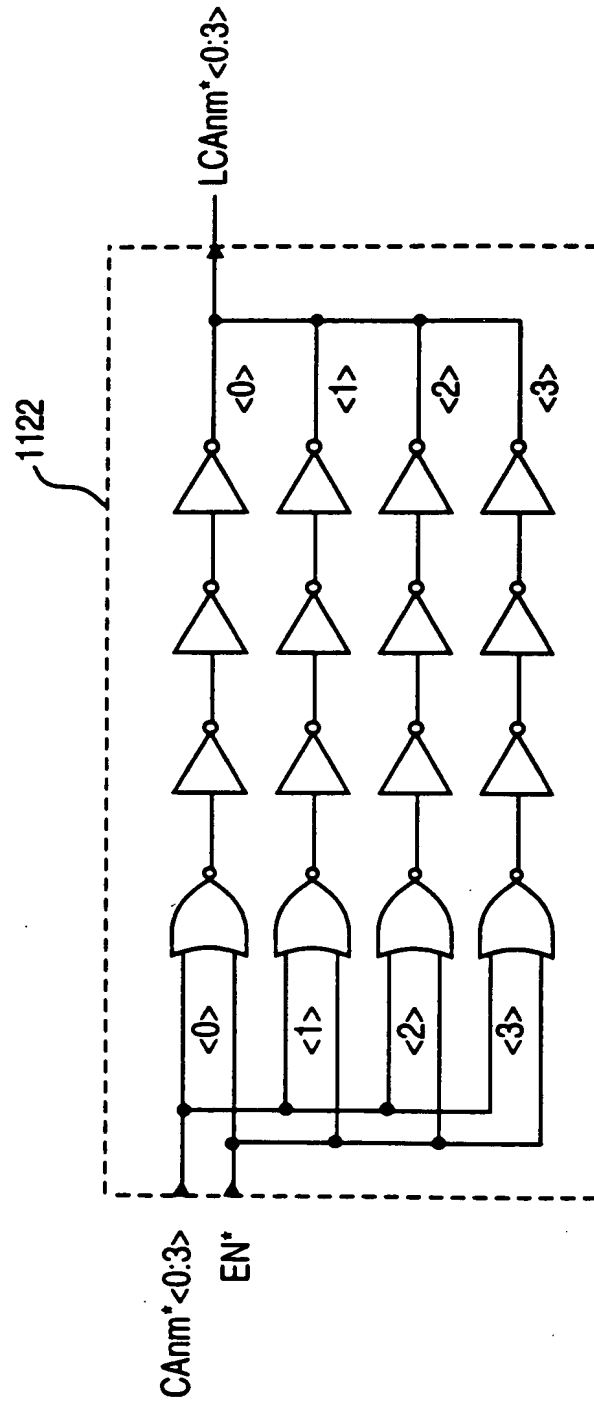


FIG. 77

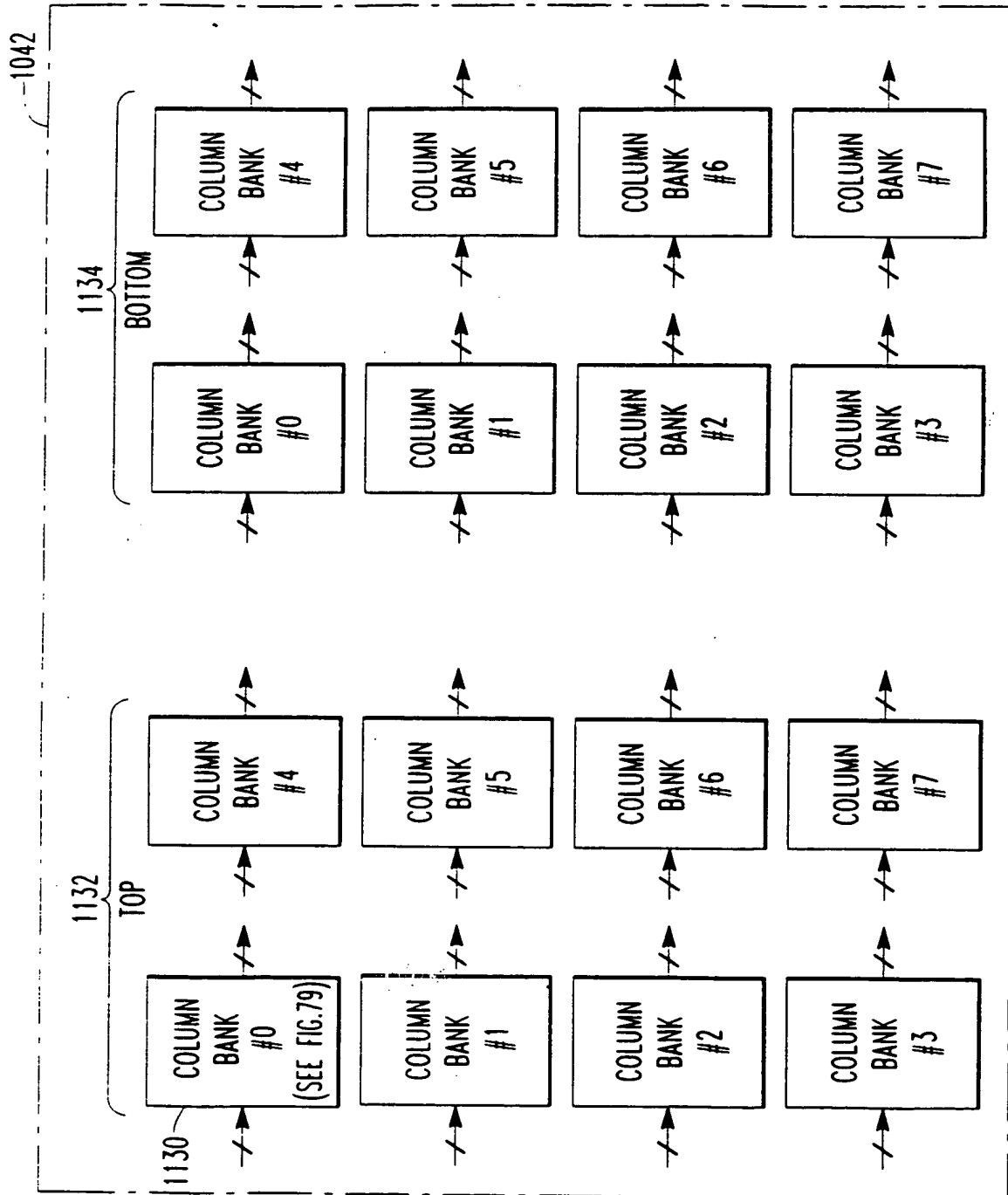
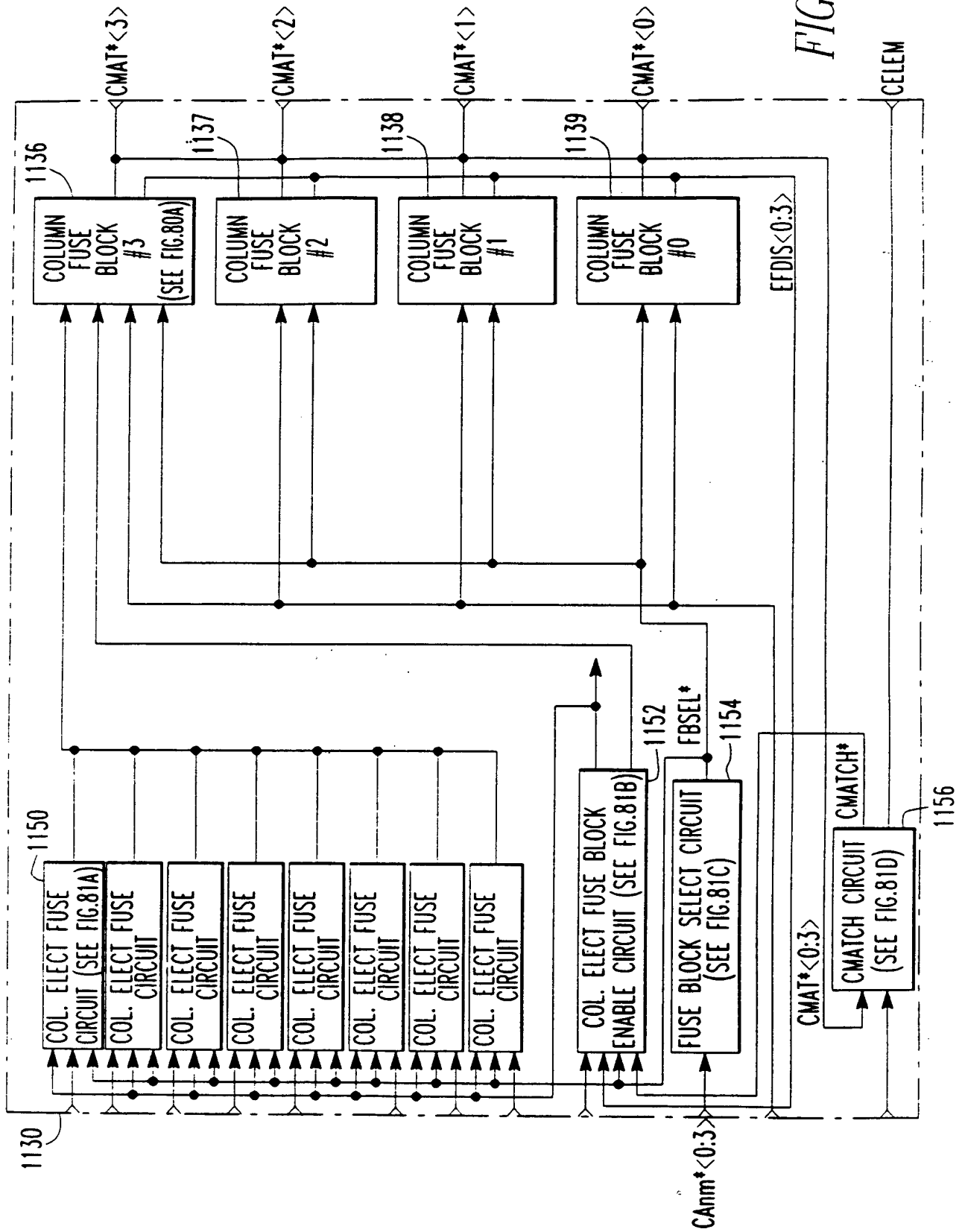


FIG. 78



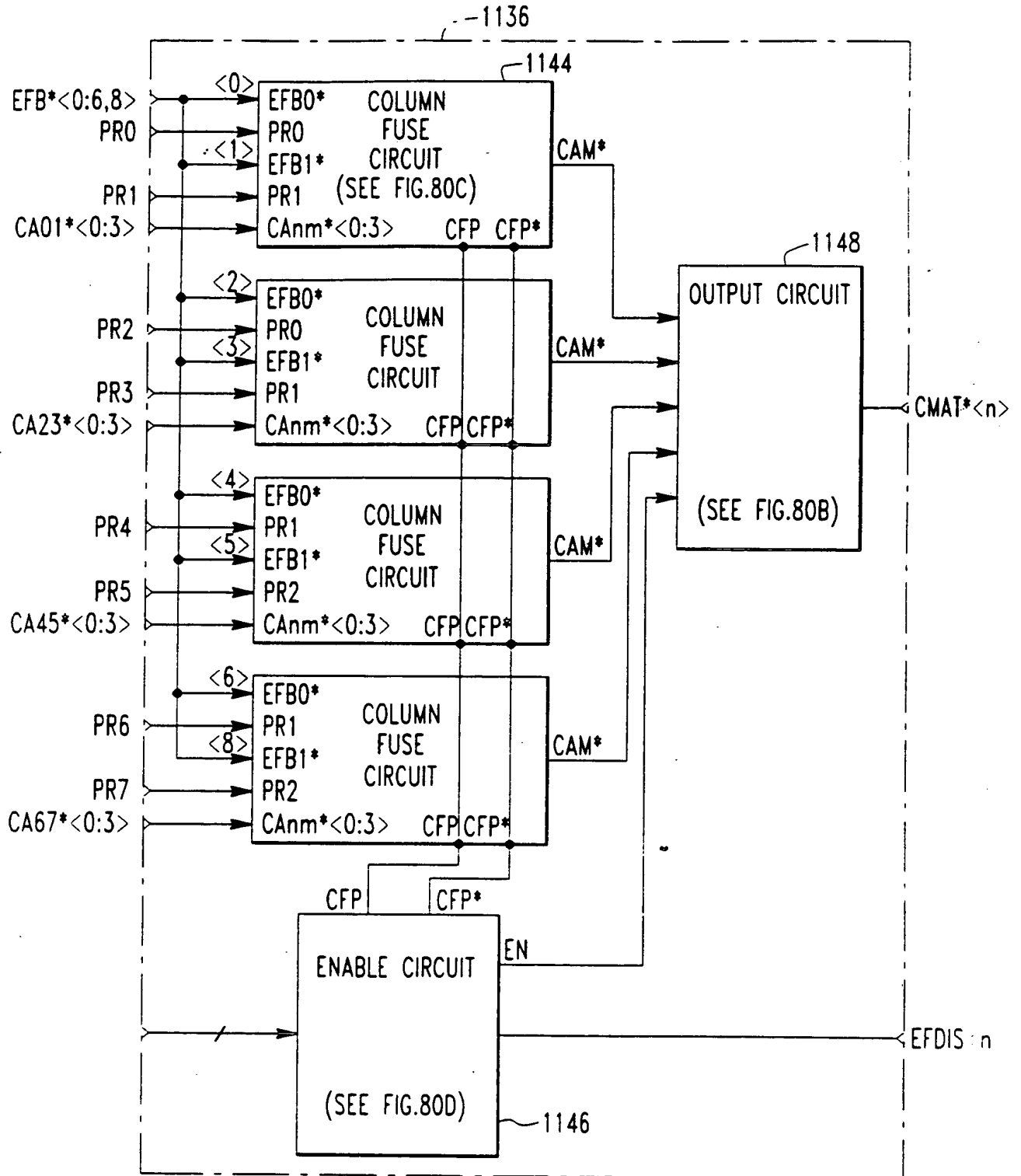
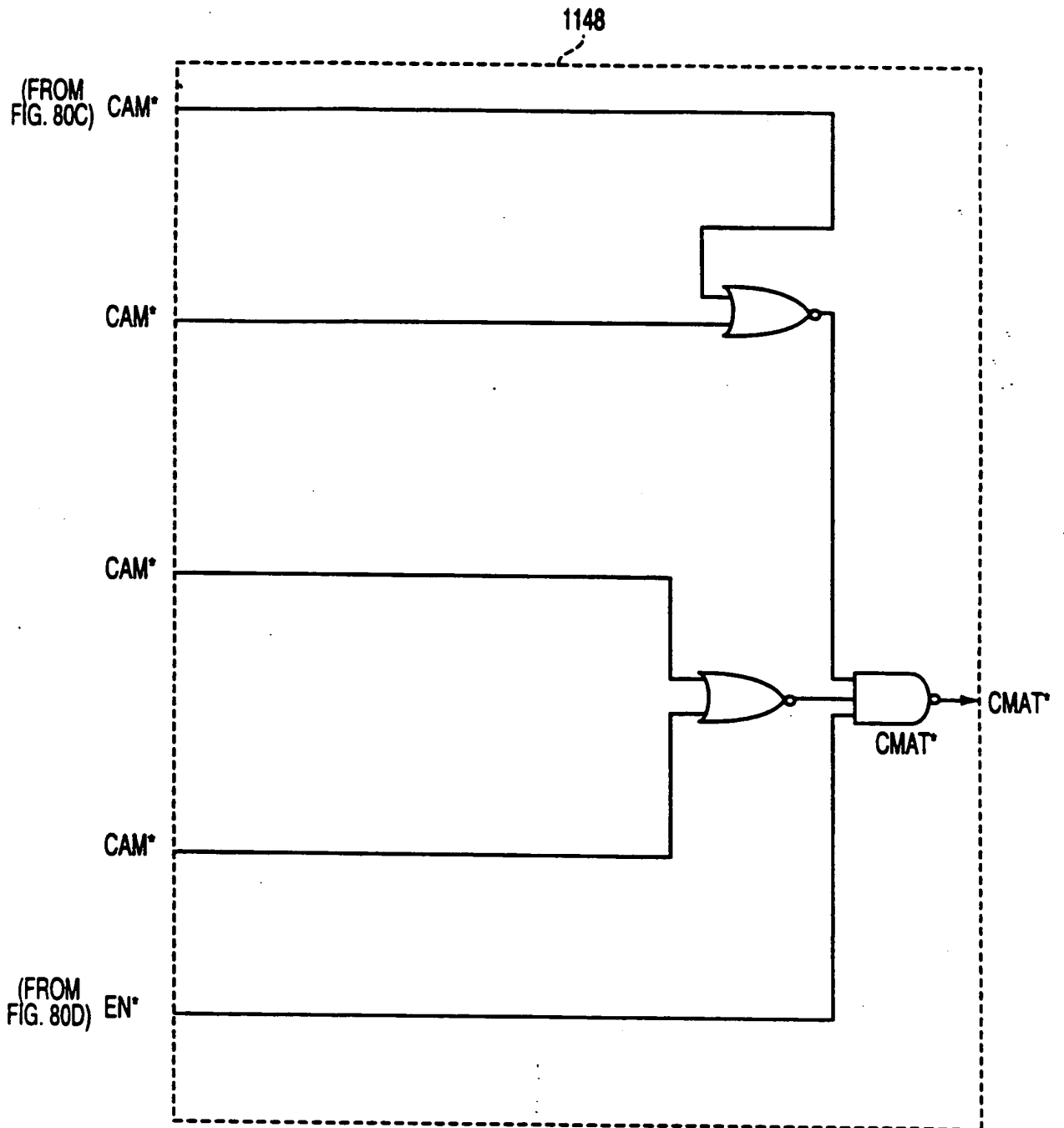


FIG. 80A

**FIG. 80B**

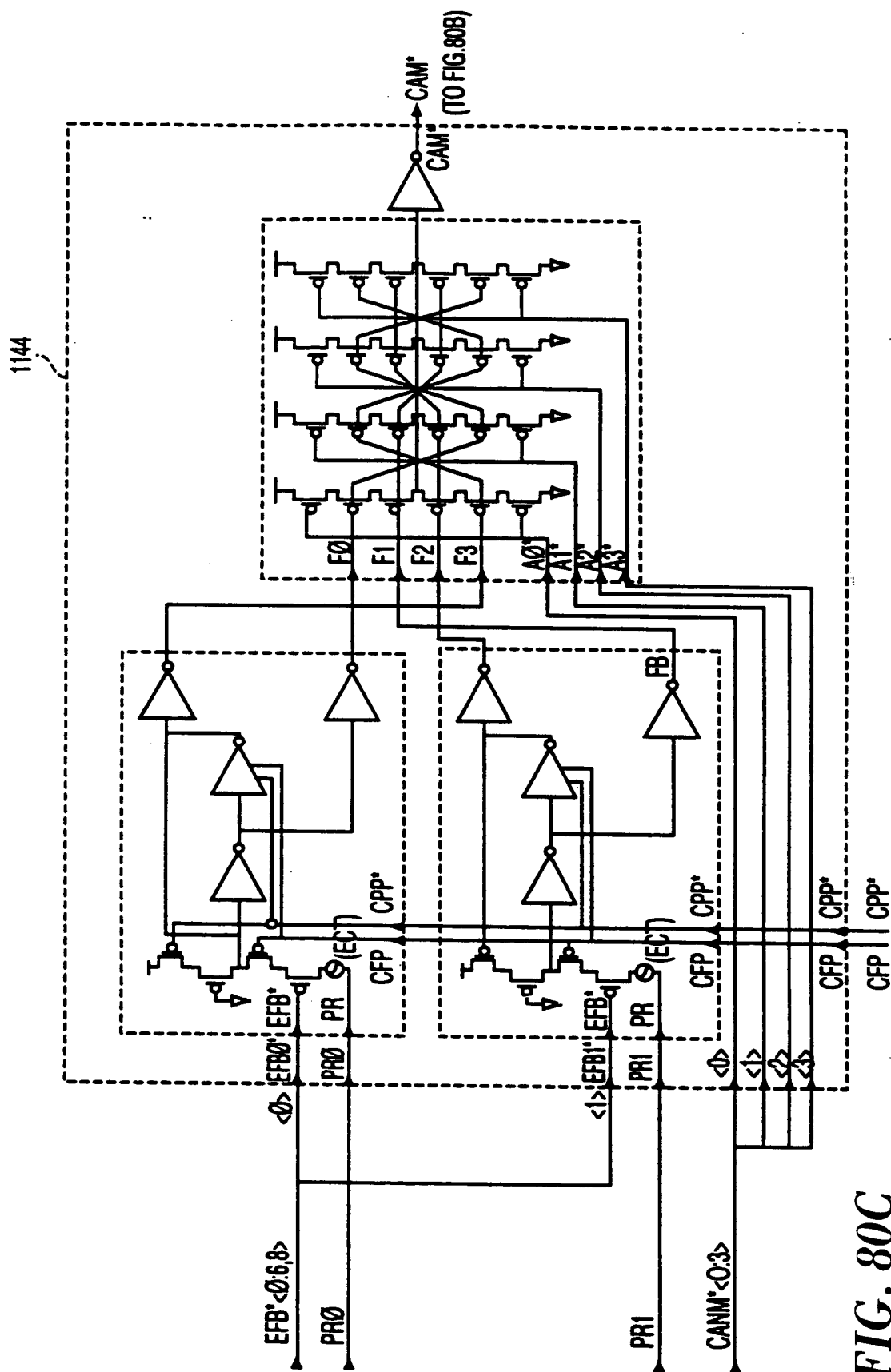


FIG. 80C

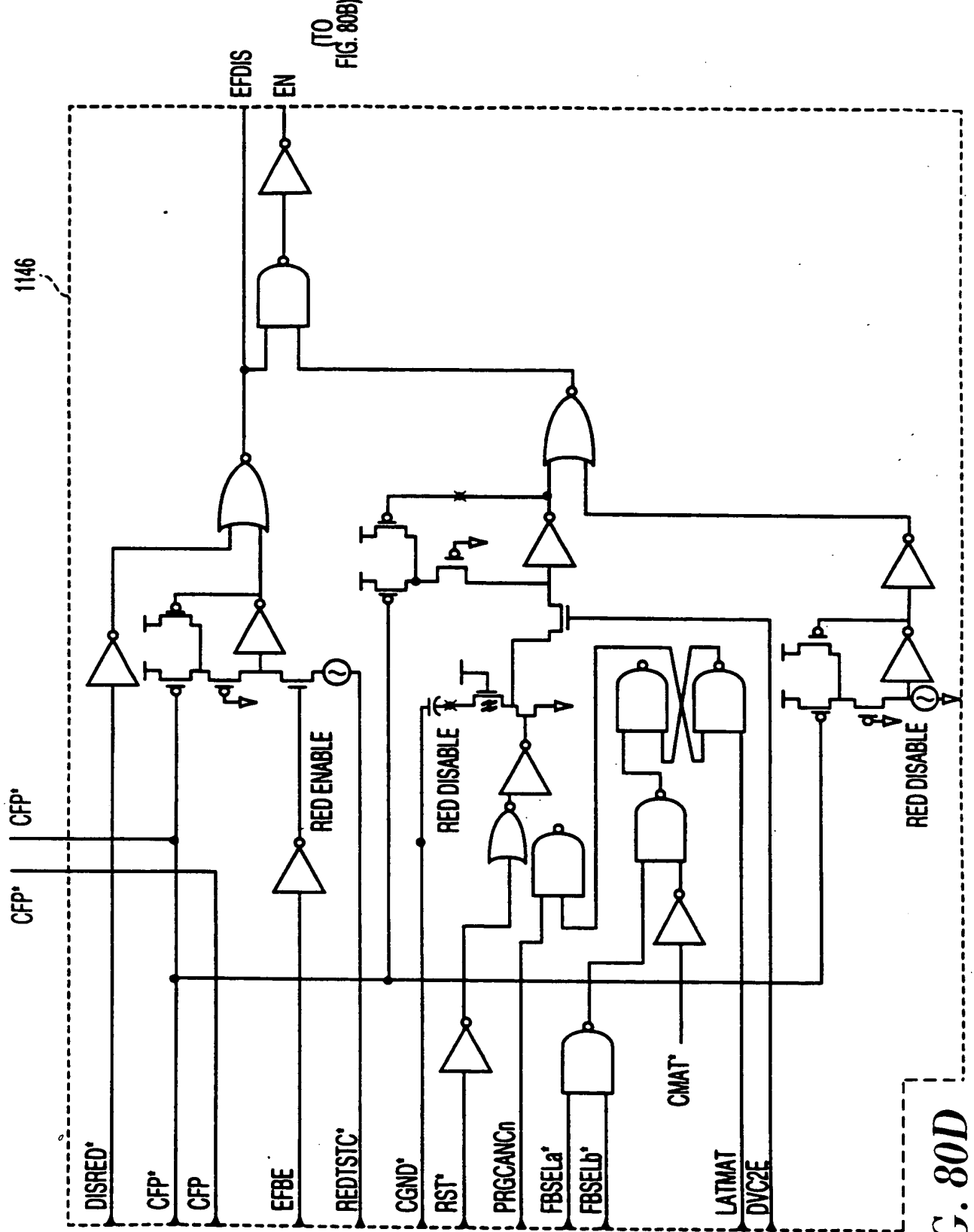


FIG. 80D

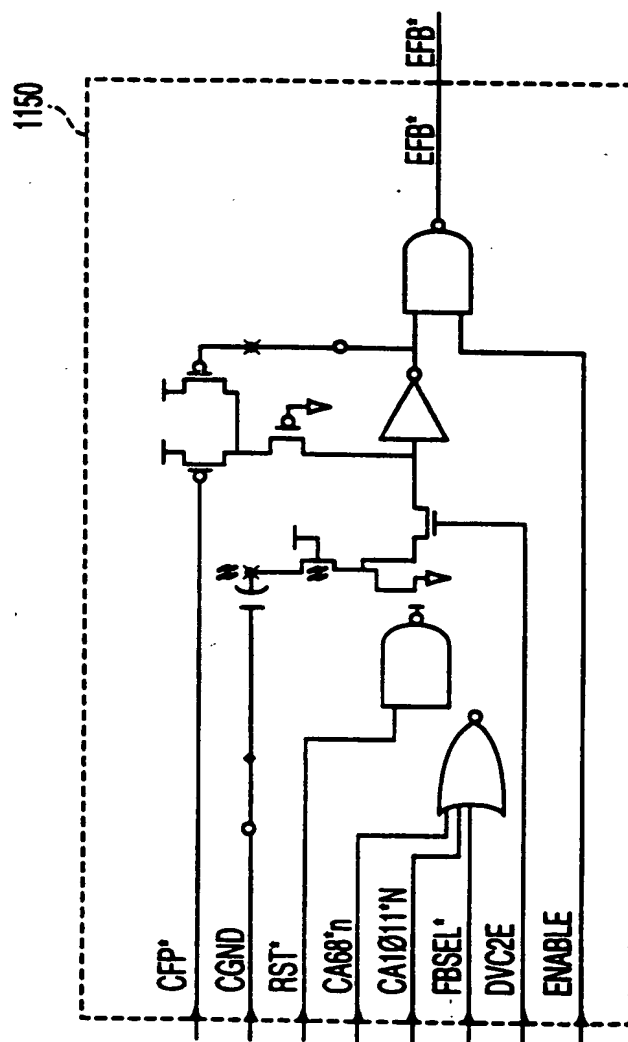


FIG. 81A

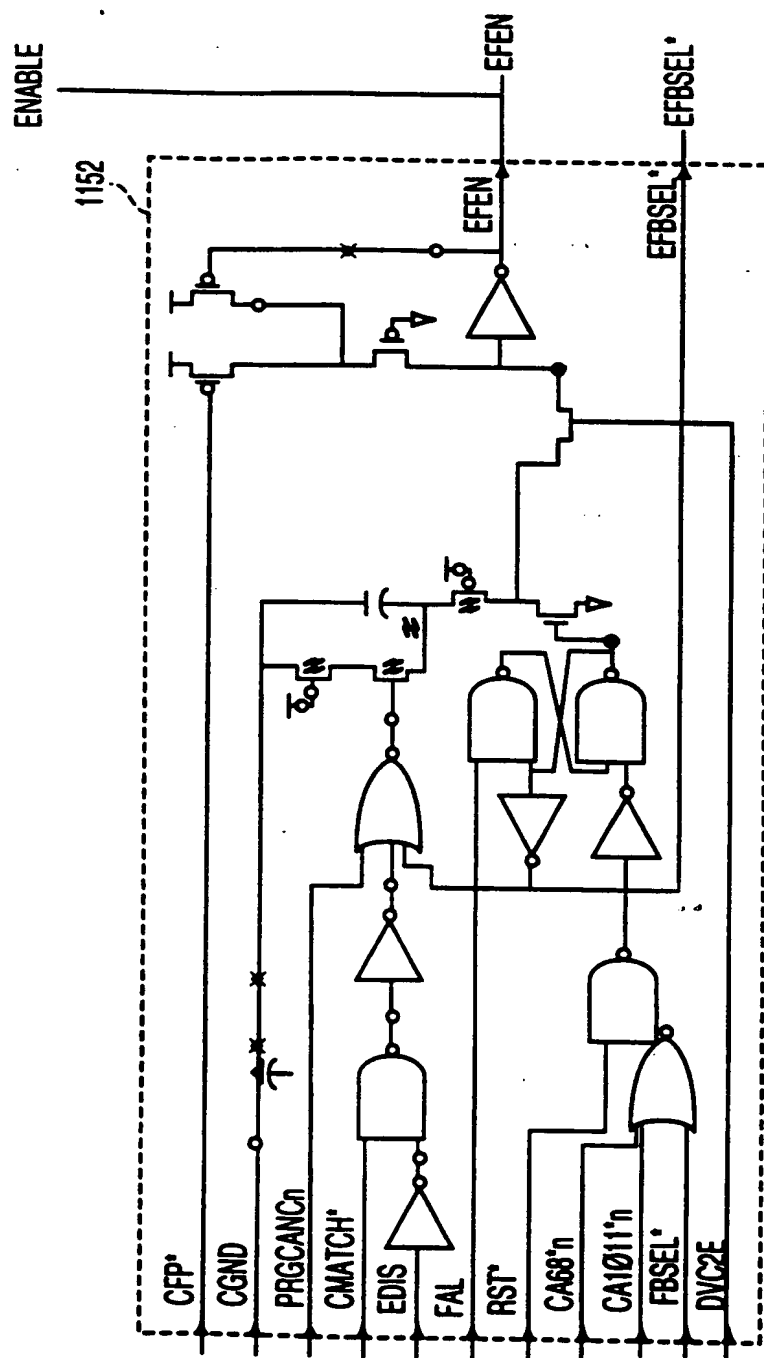


FIG. 81B

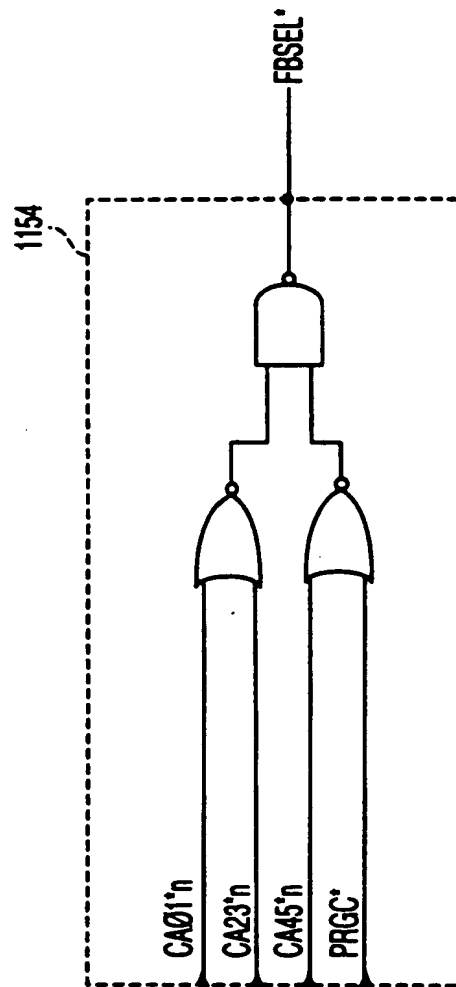


FIG. 81C

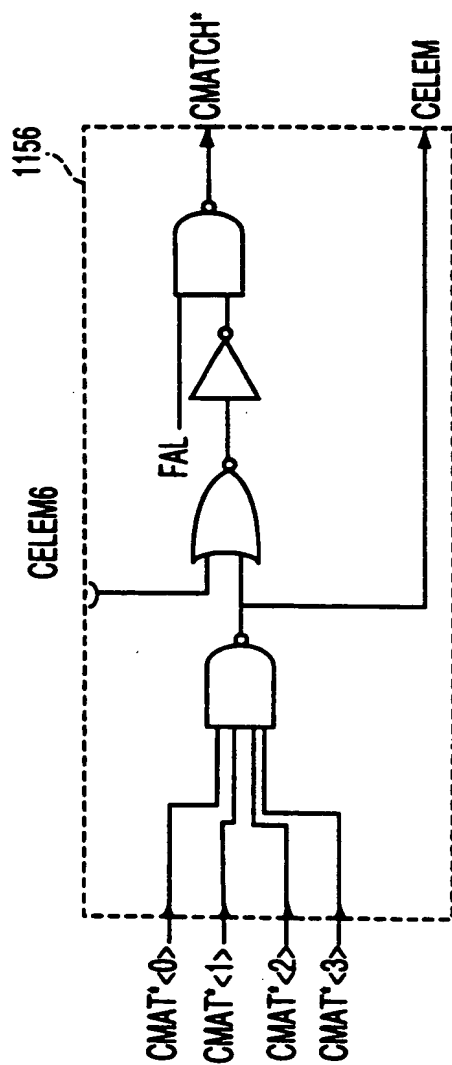
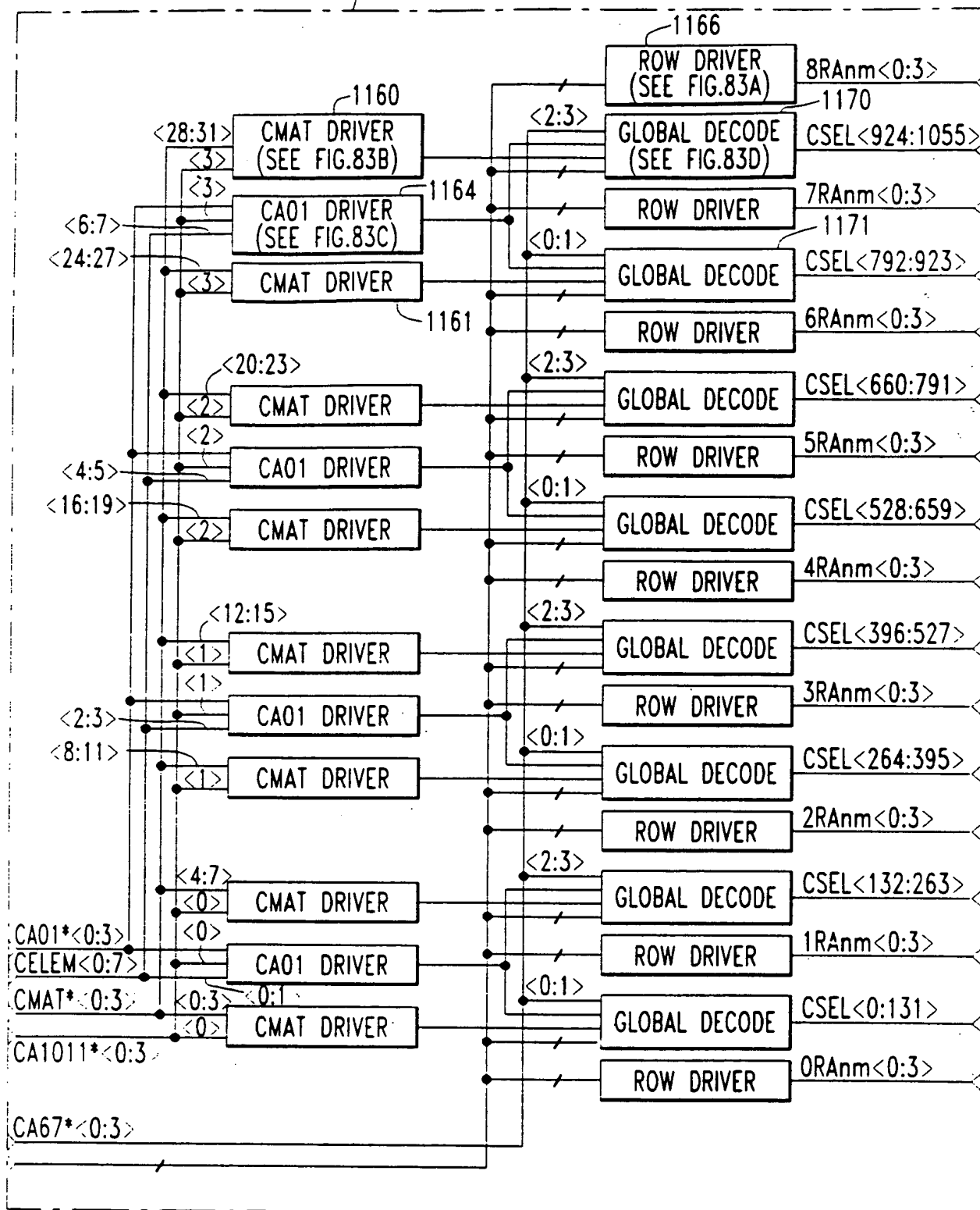


FIG. 81D



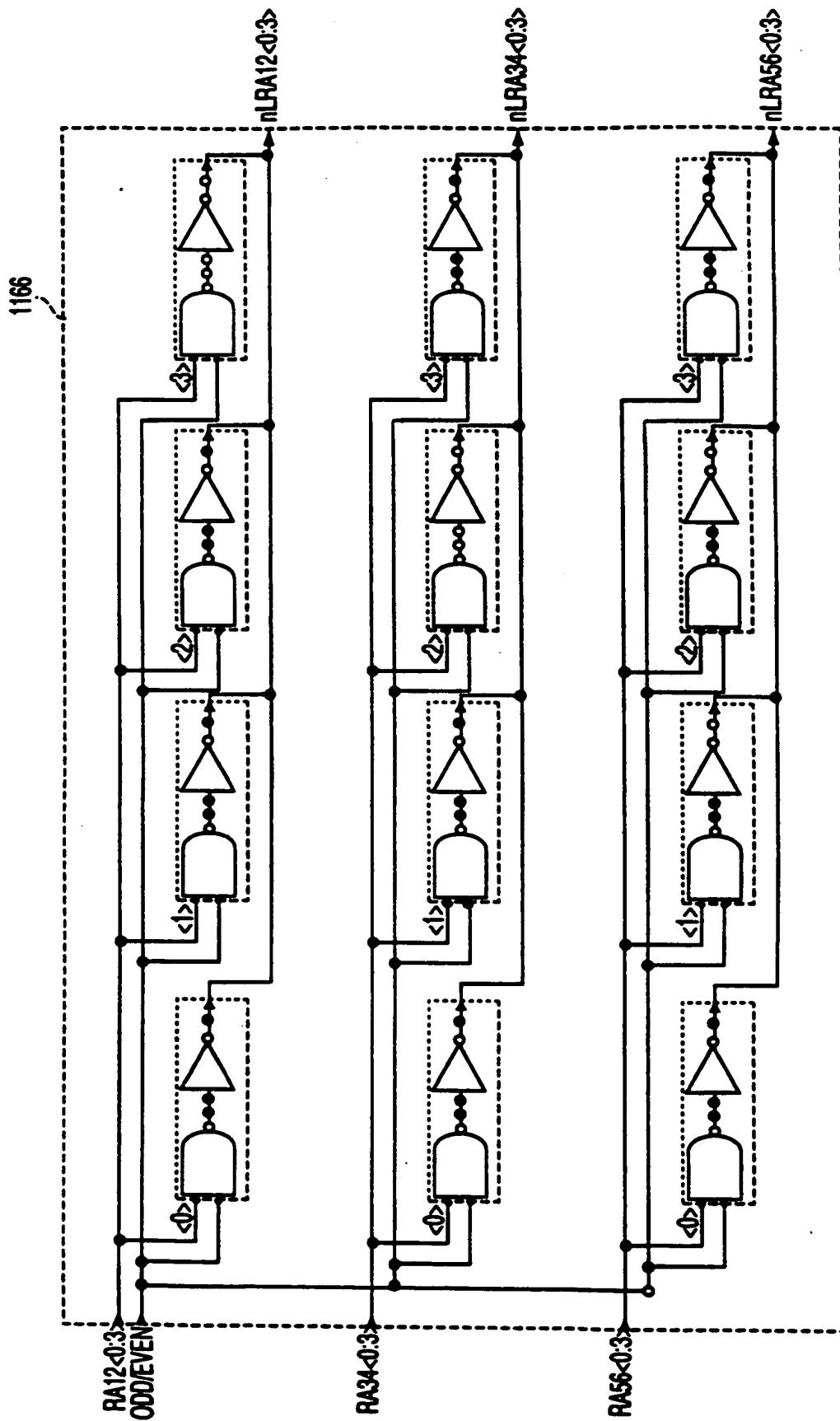


FIG. 83A

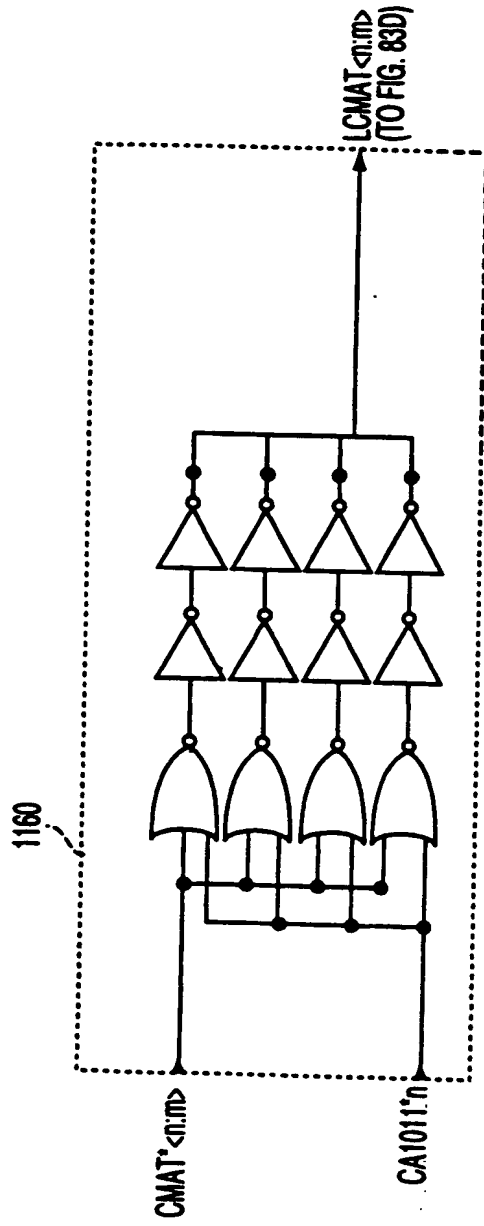


FIG. 83B

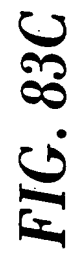
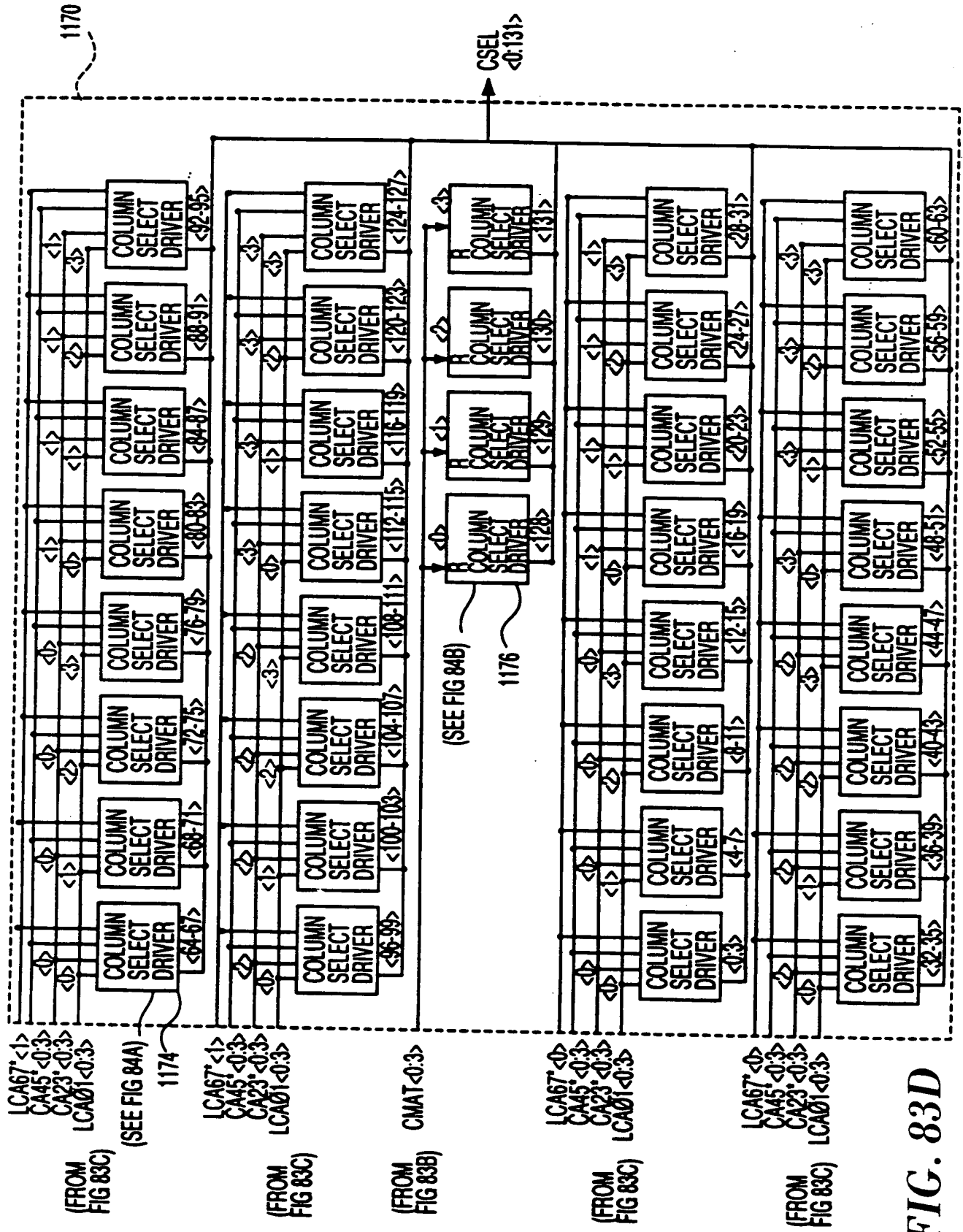


FIG. 83C



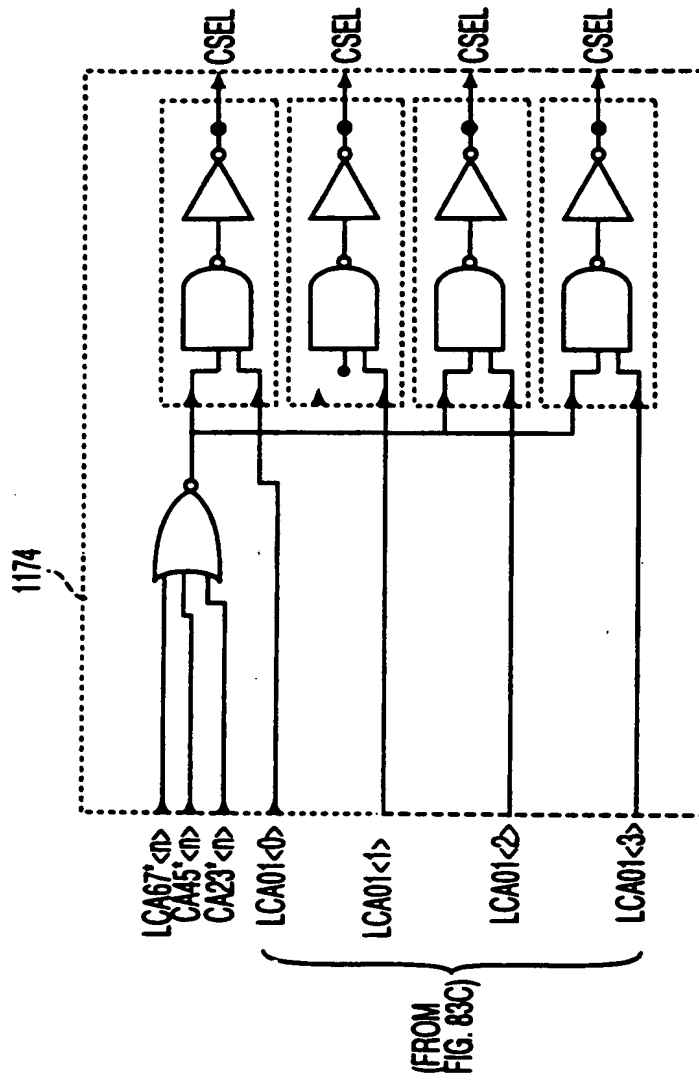
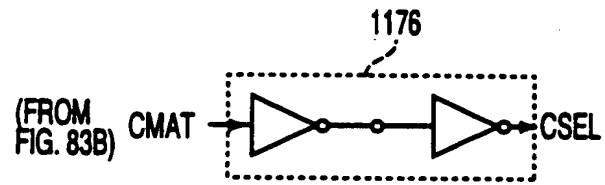


FIG. 84A

**FIG. 84B**

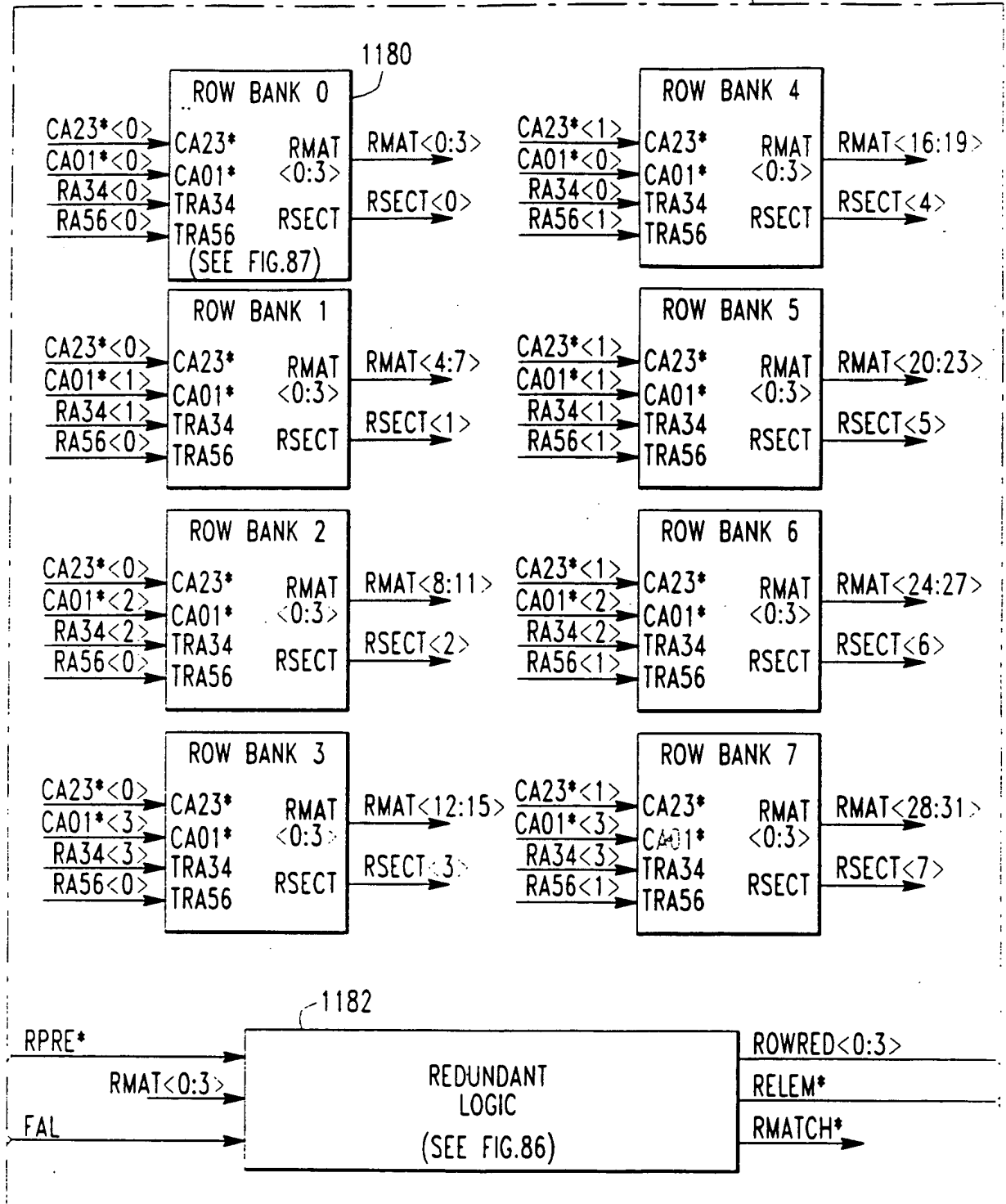


FIG. 85

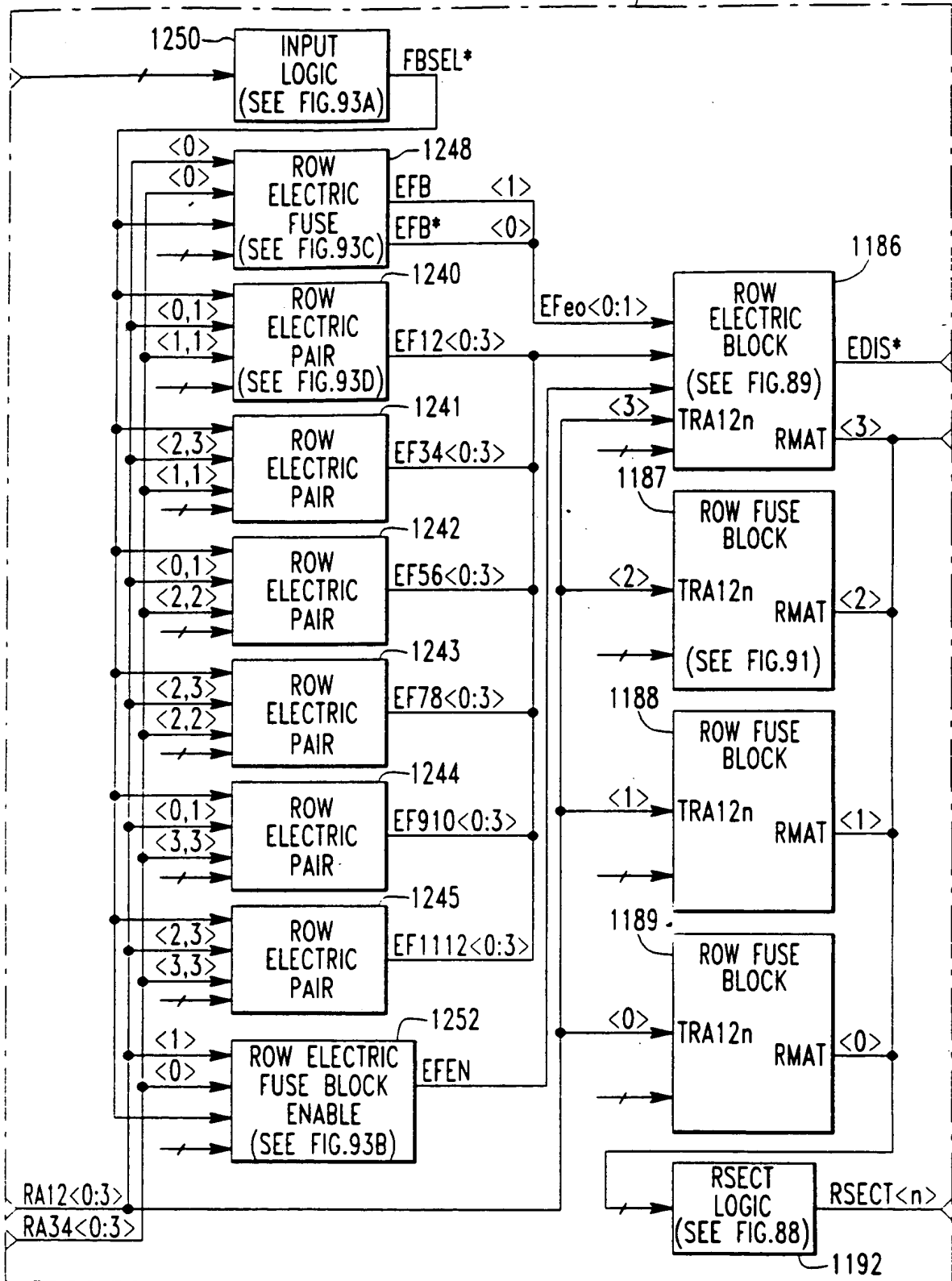
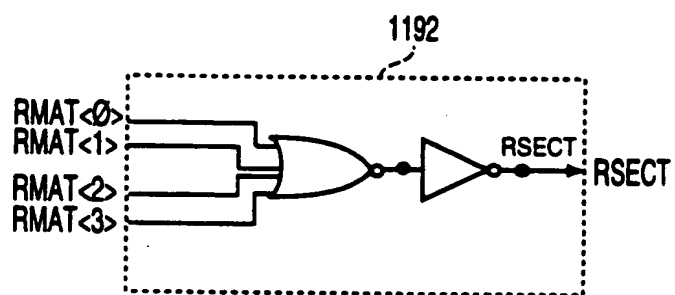
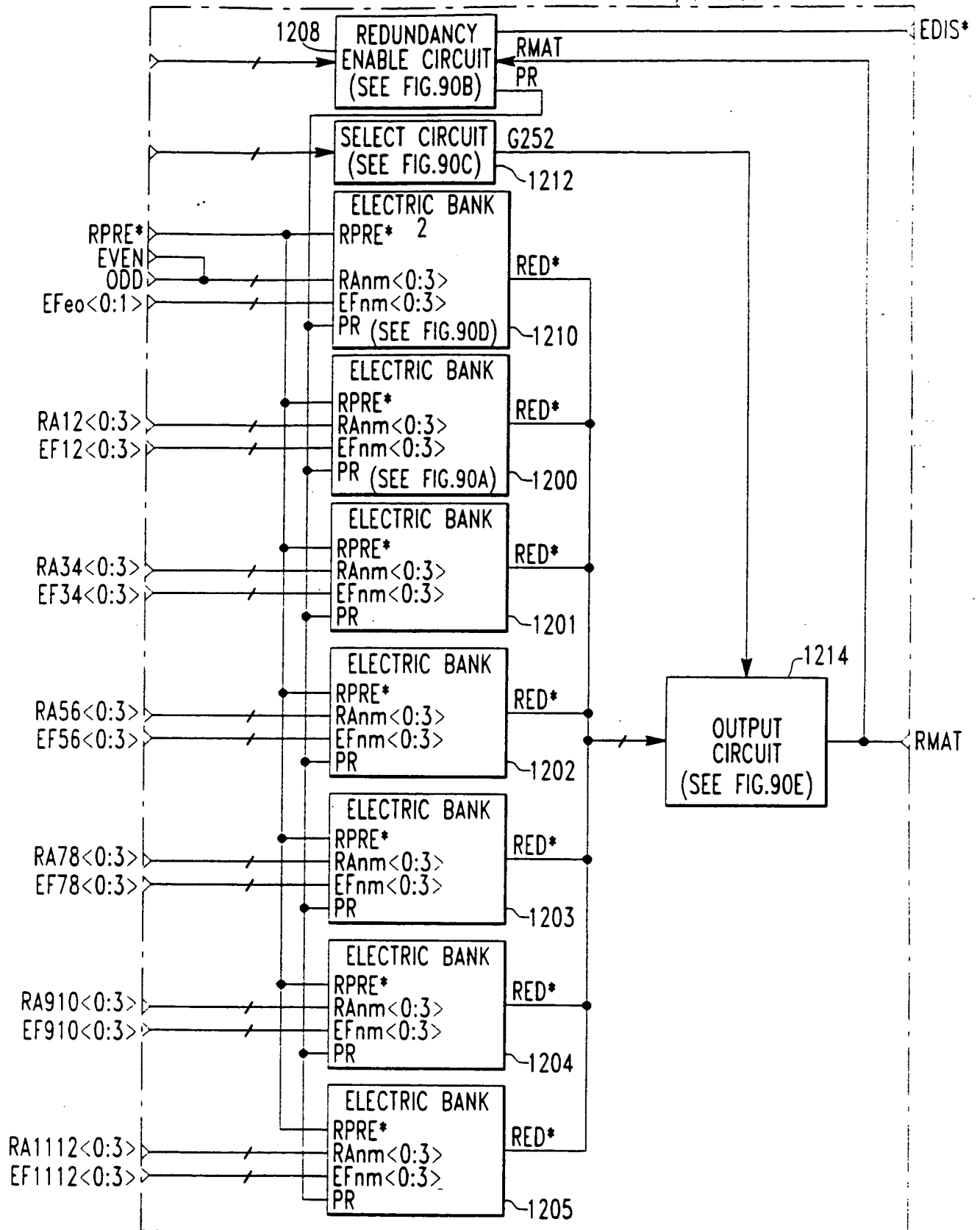


FIG. 87

**FIG. 88**



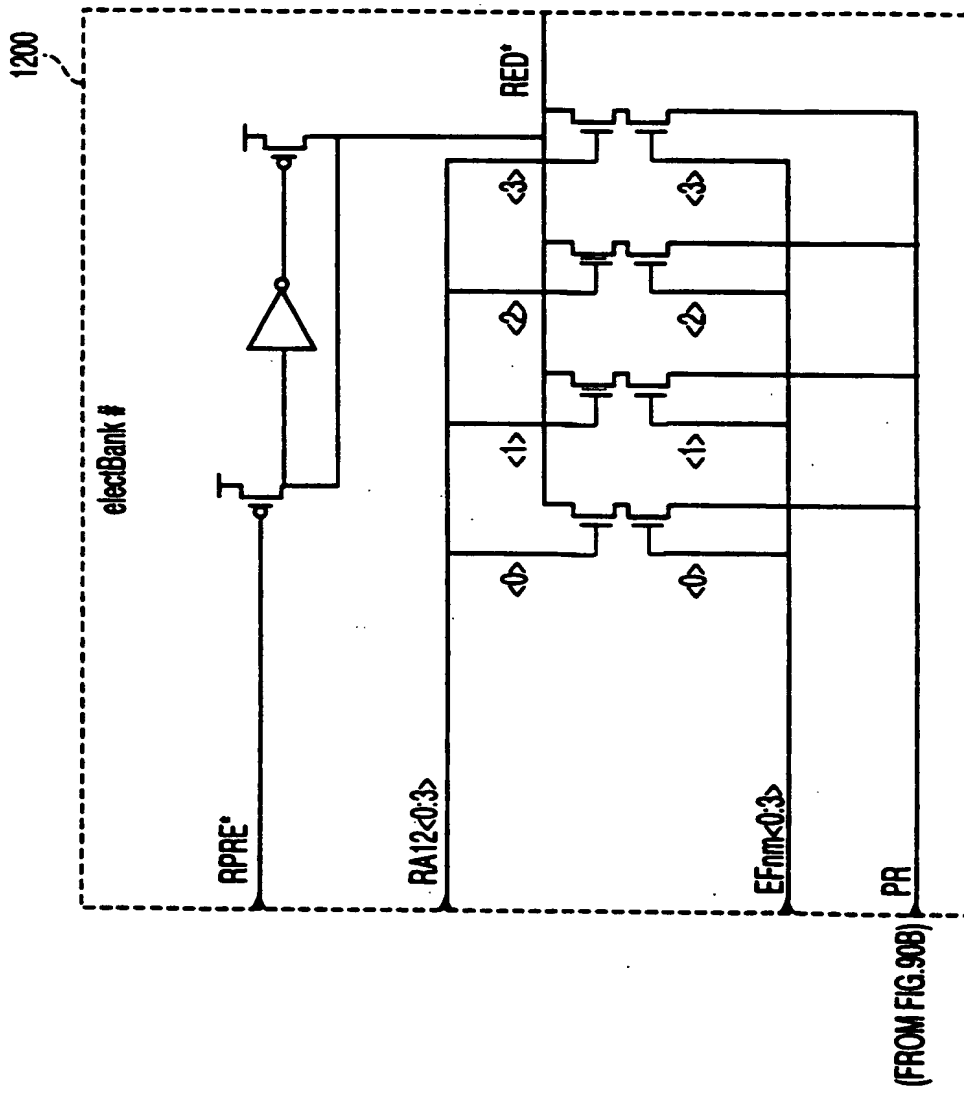


FIG. 90A

FIG. 90B

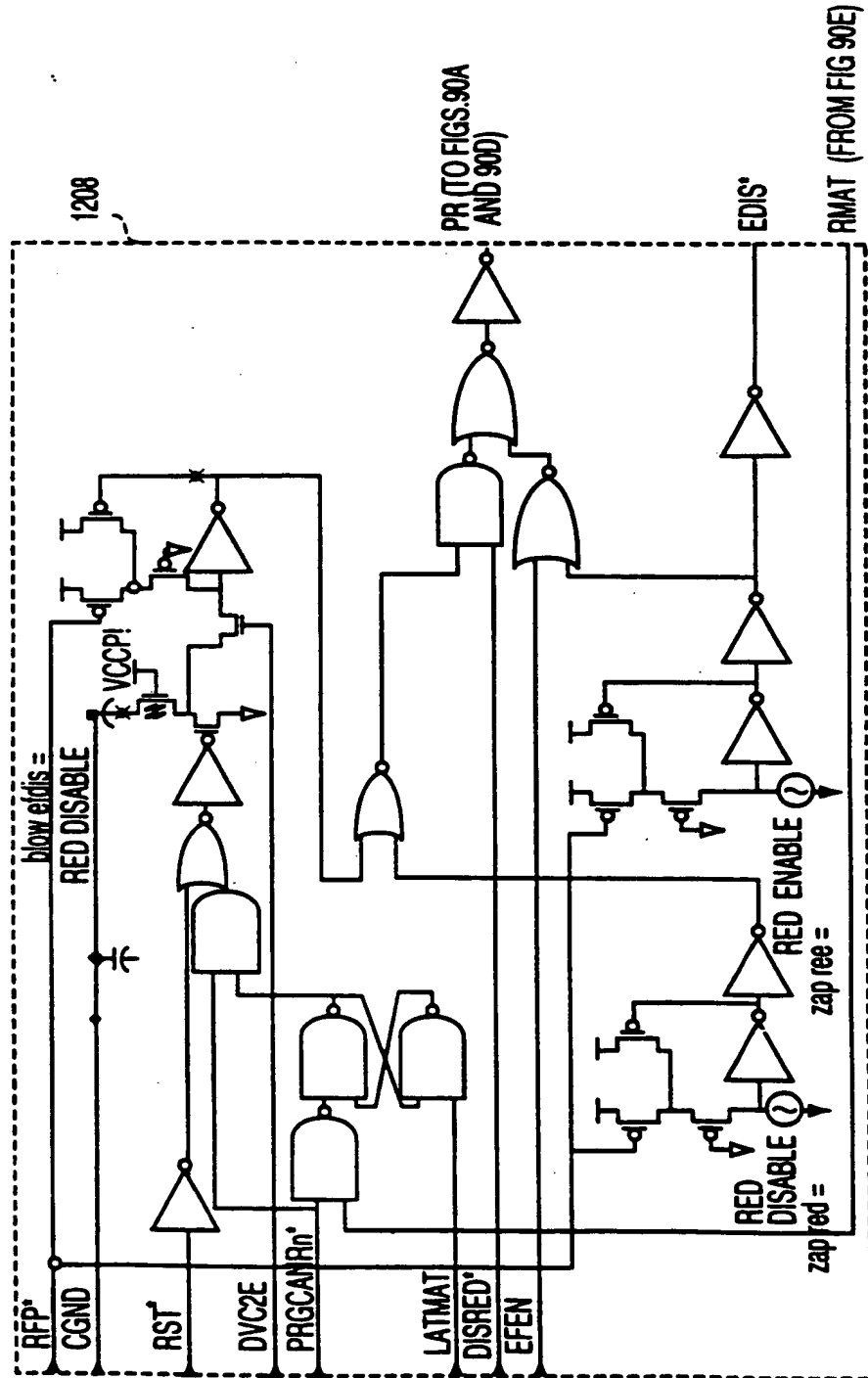


FIG. 90B

FIG. 90C

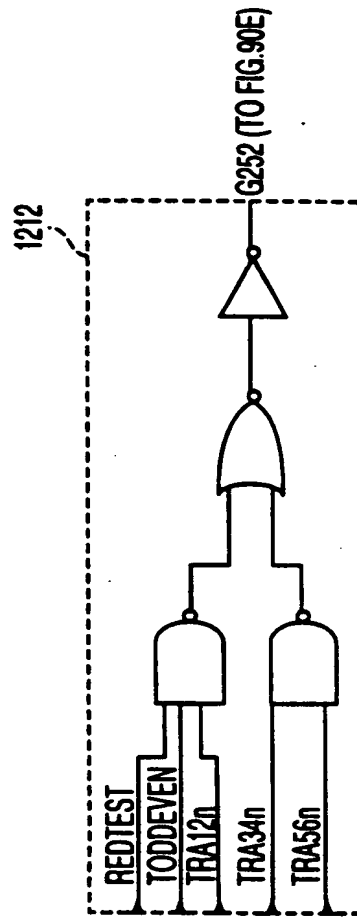


FIG. 90C

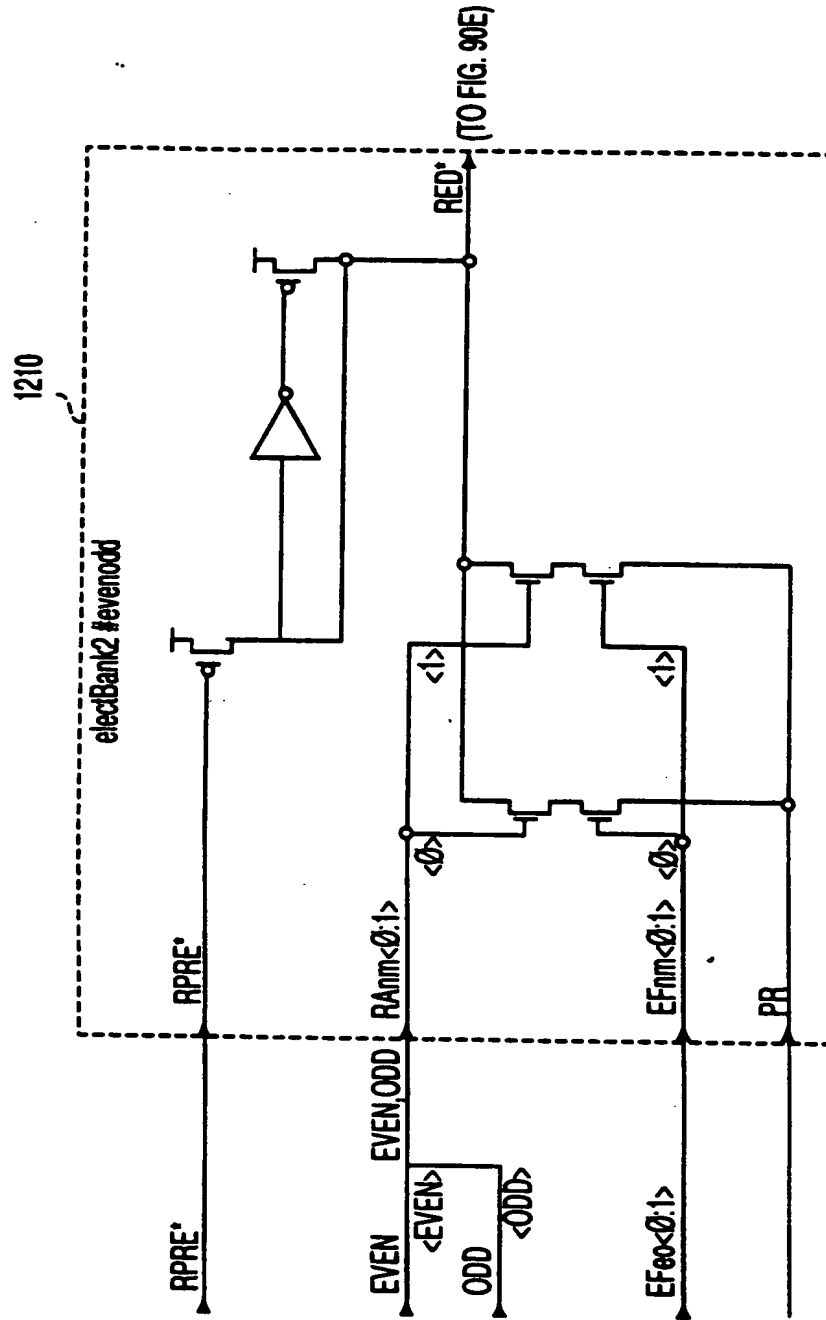


FIG. 90D

FIG. 90E

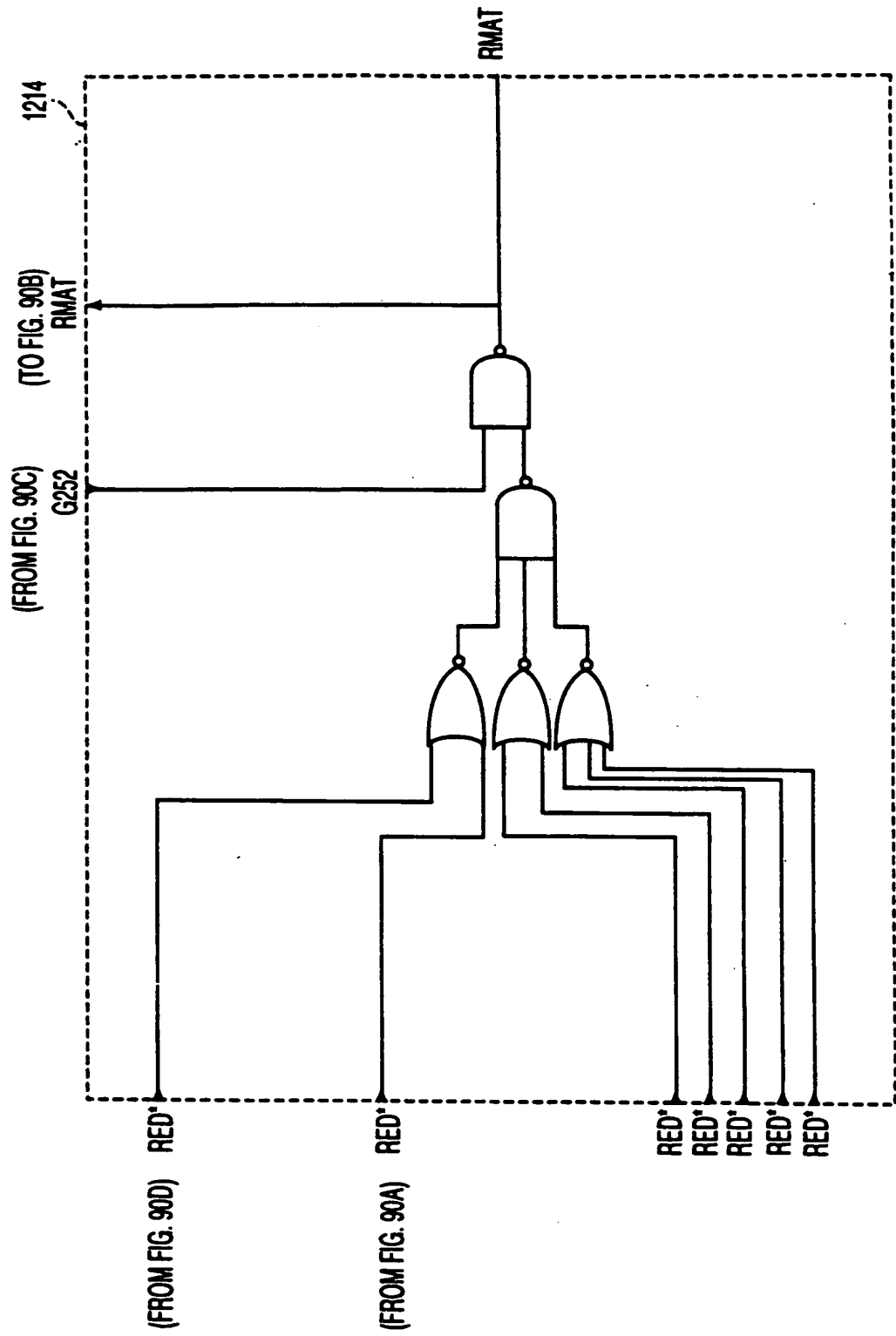


FIG. 90E

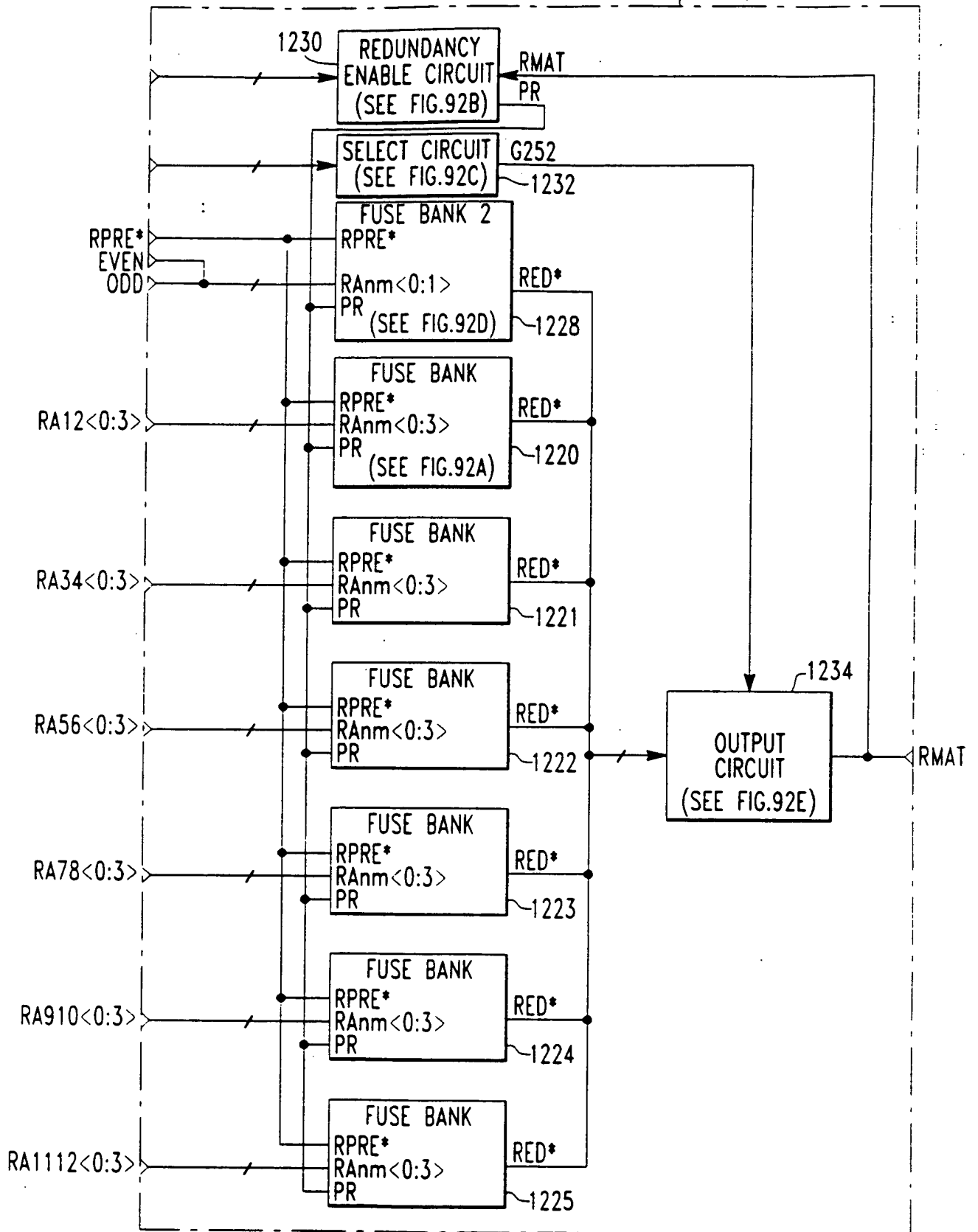


FIG. 91

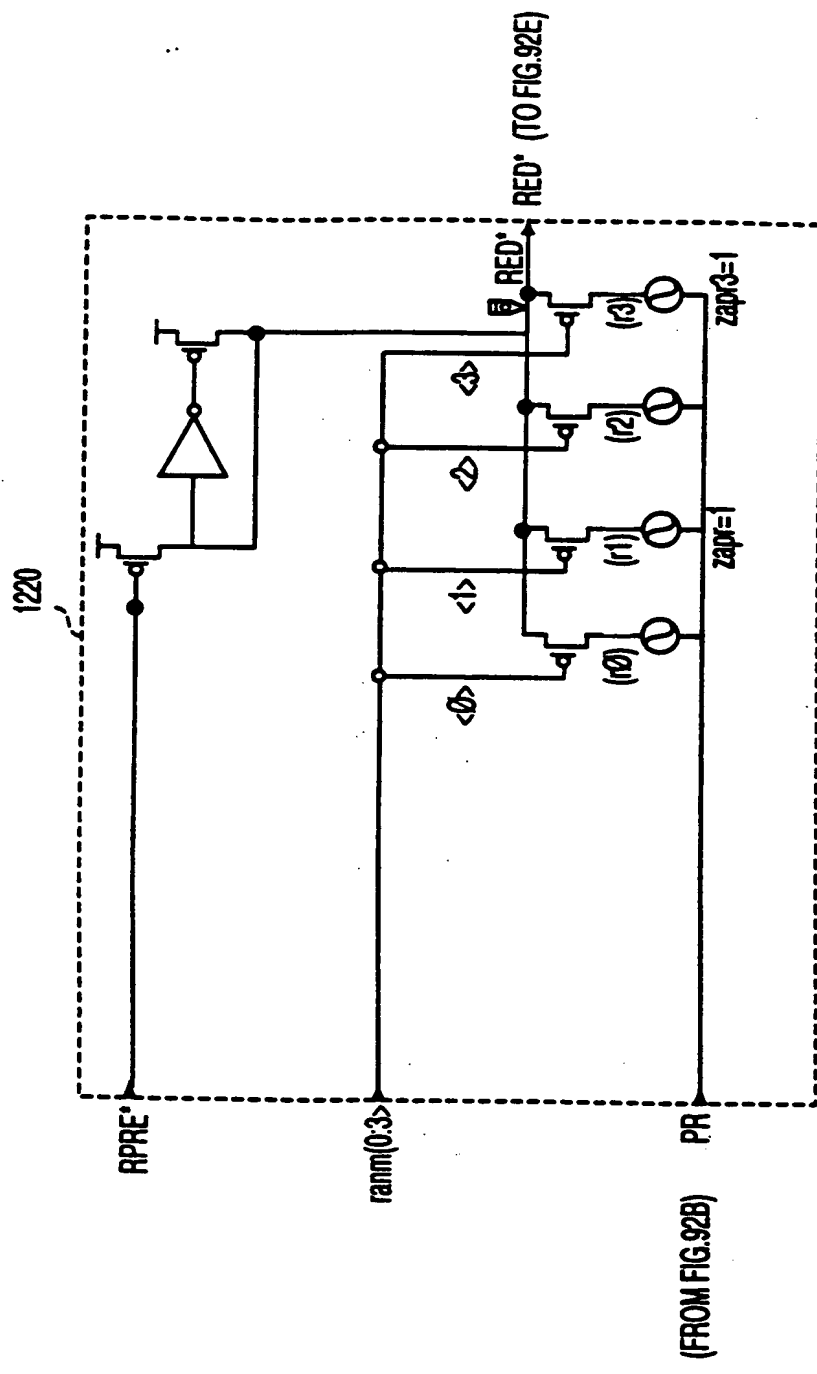


FIG. 92A

FIG. 92B

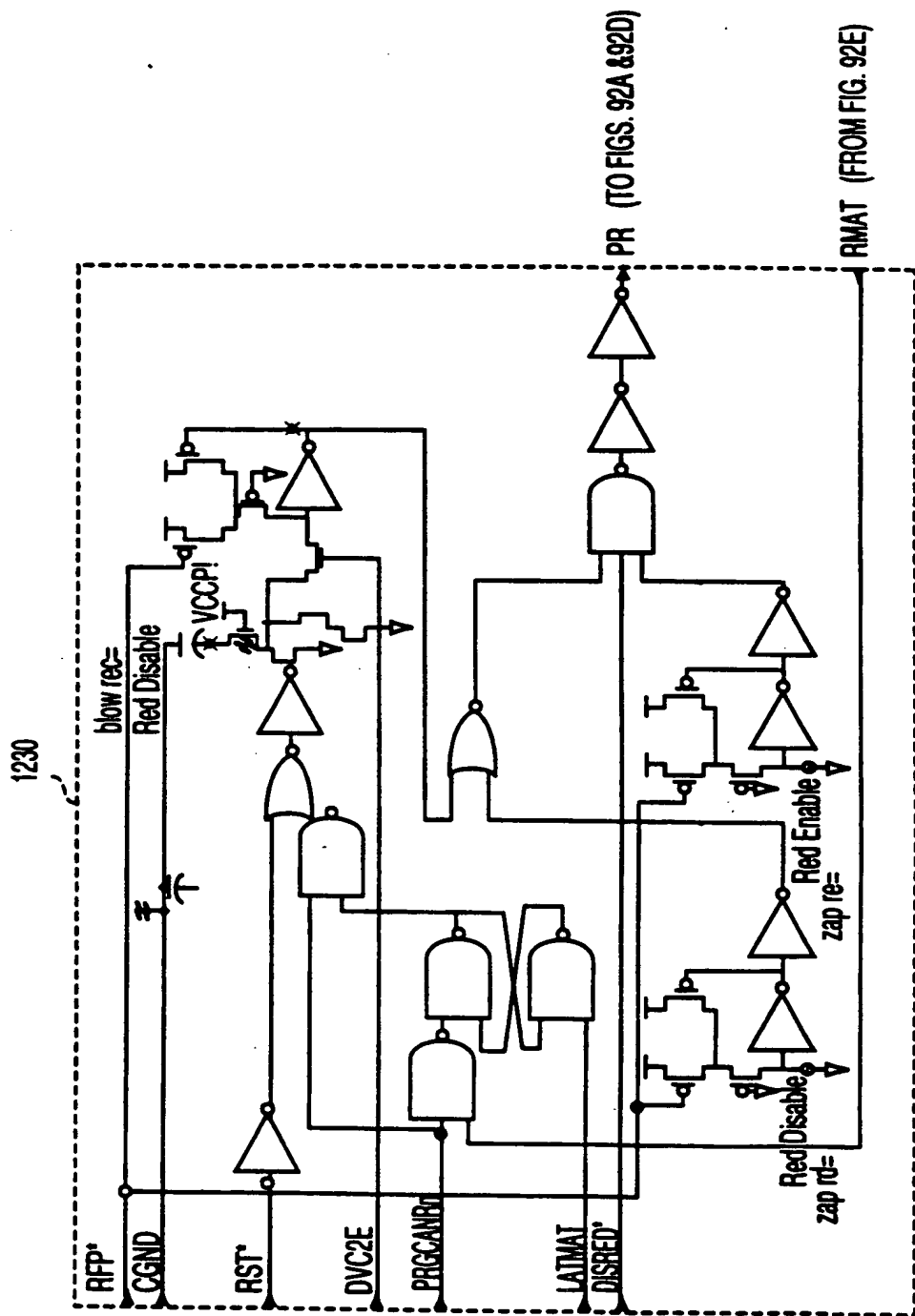


FIG. 92B

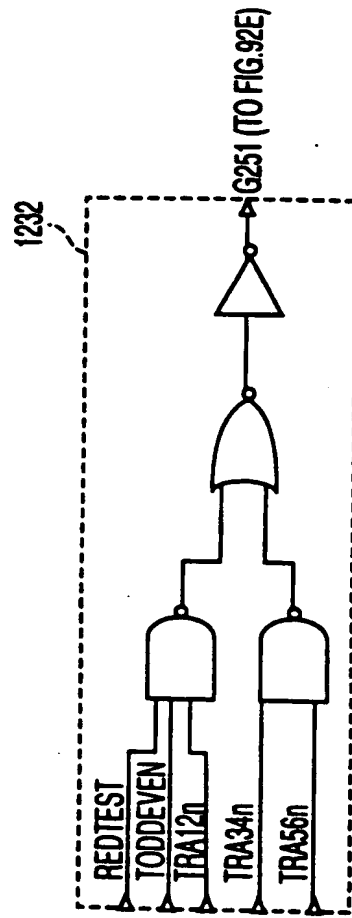


FIG. 92C

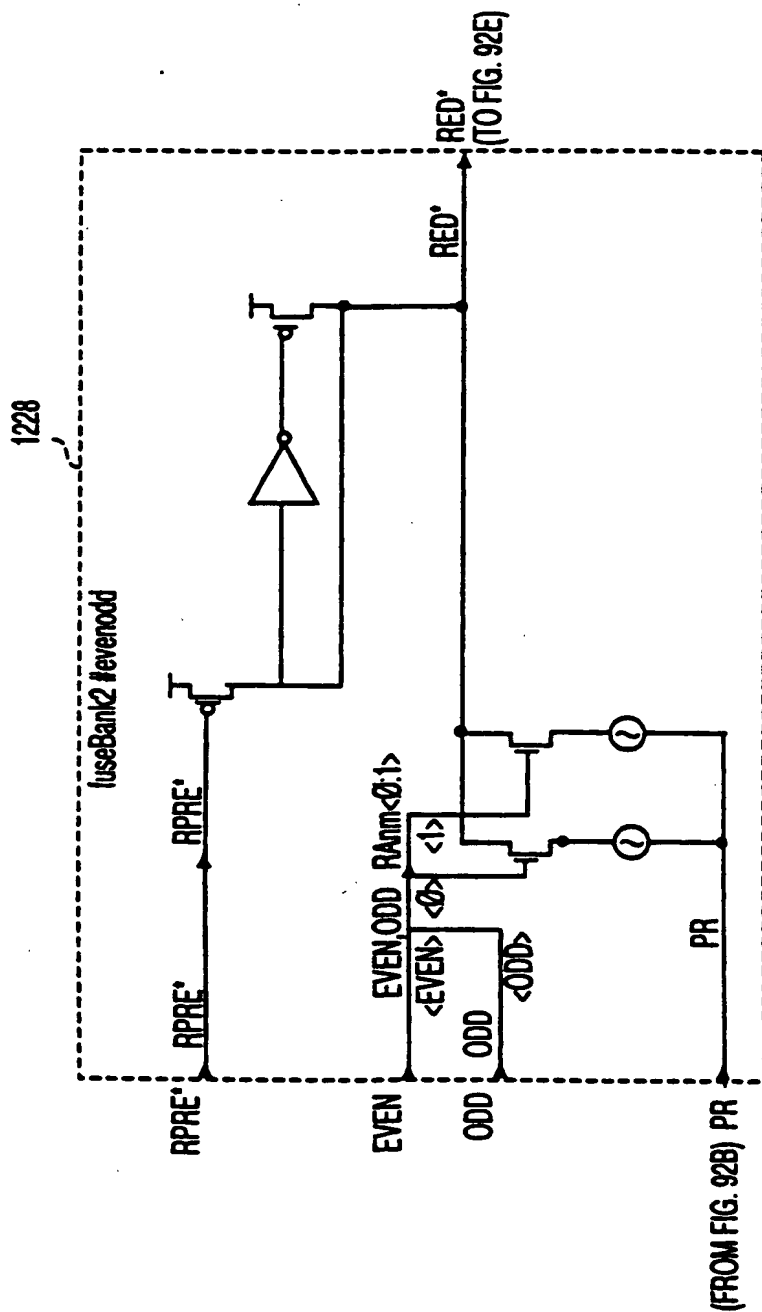


FIG. 92D



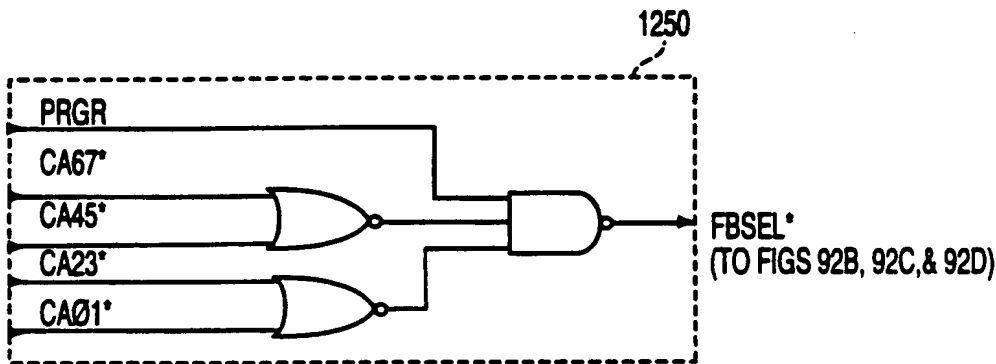
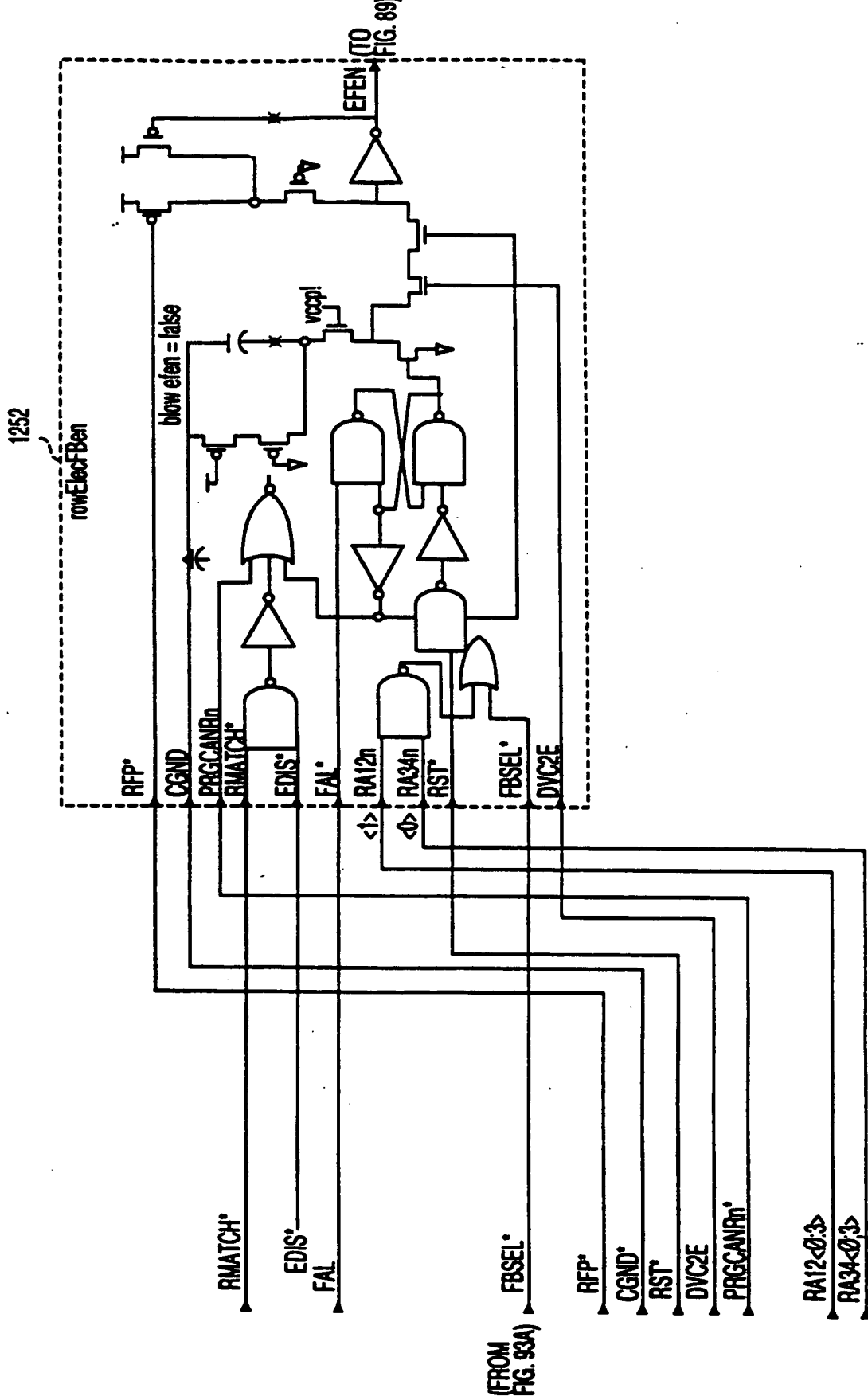
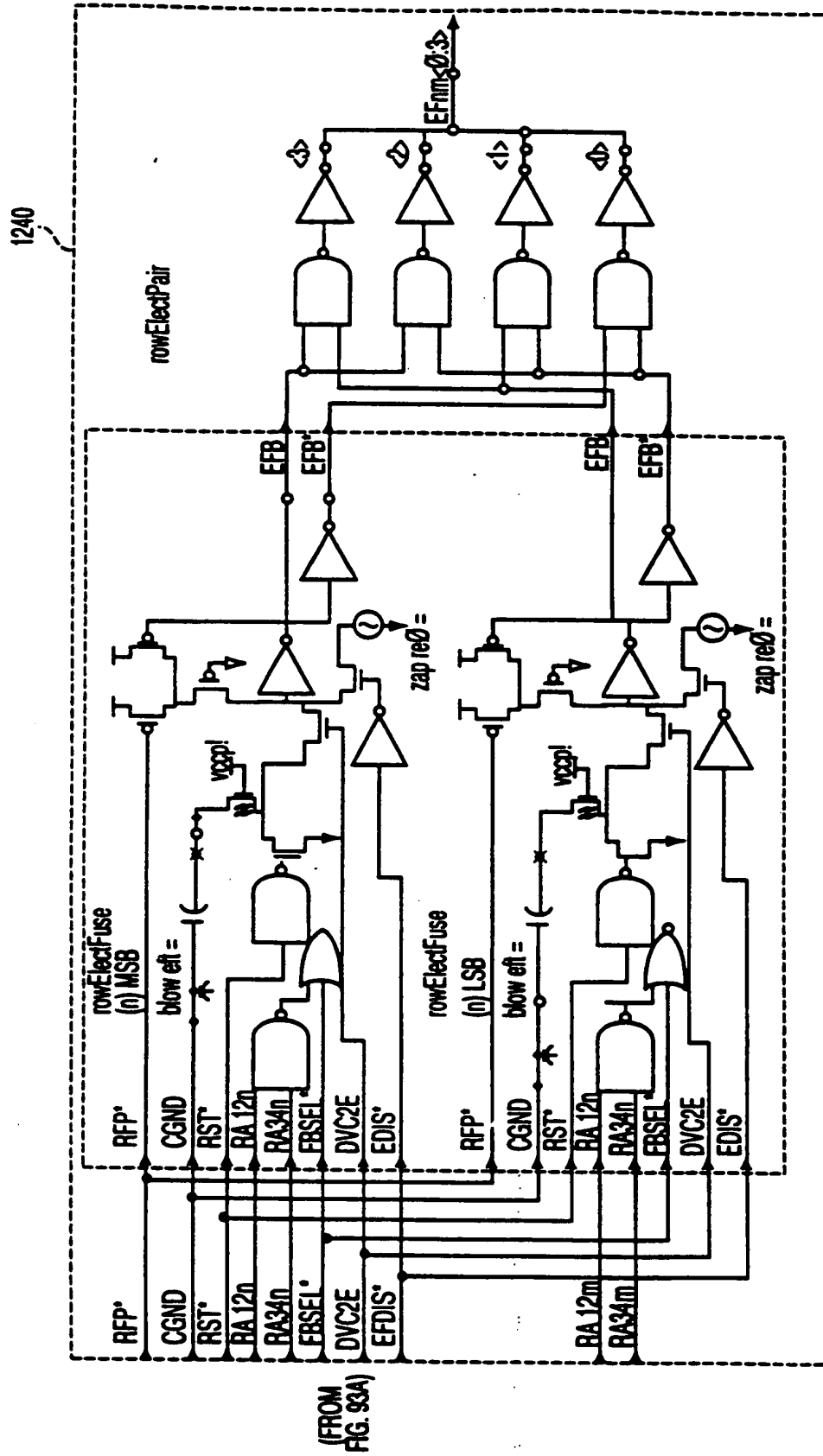
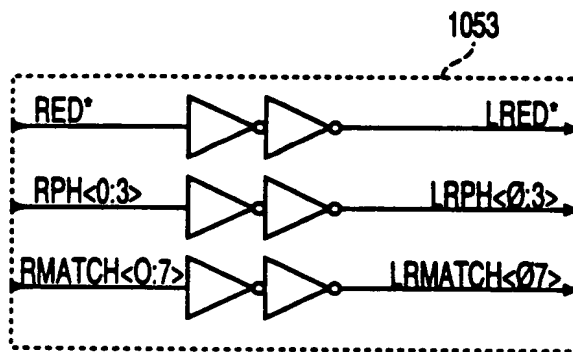


FIG. 93A





**FIG. 94**

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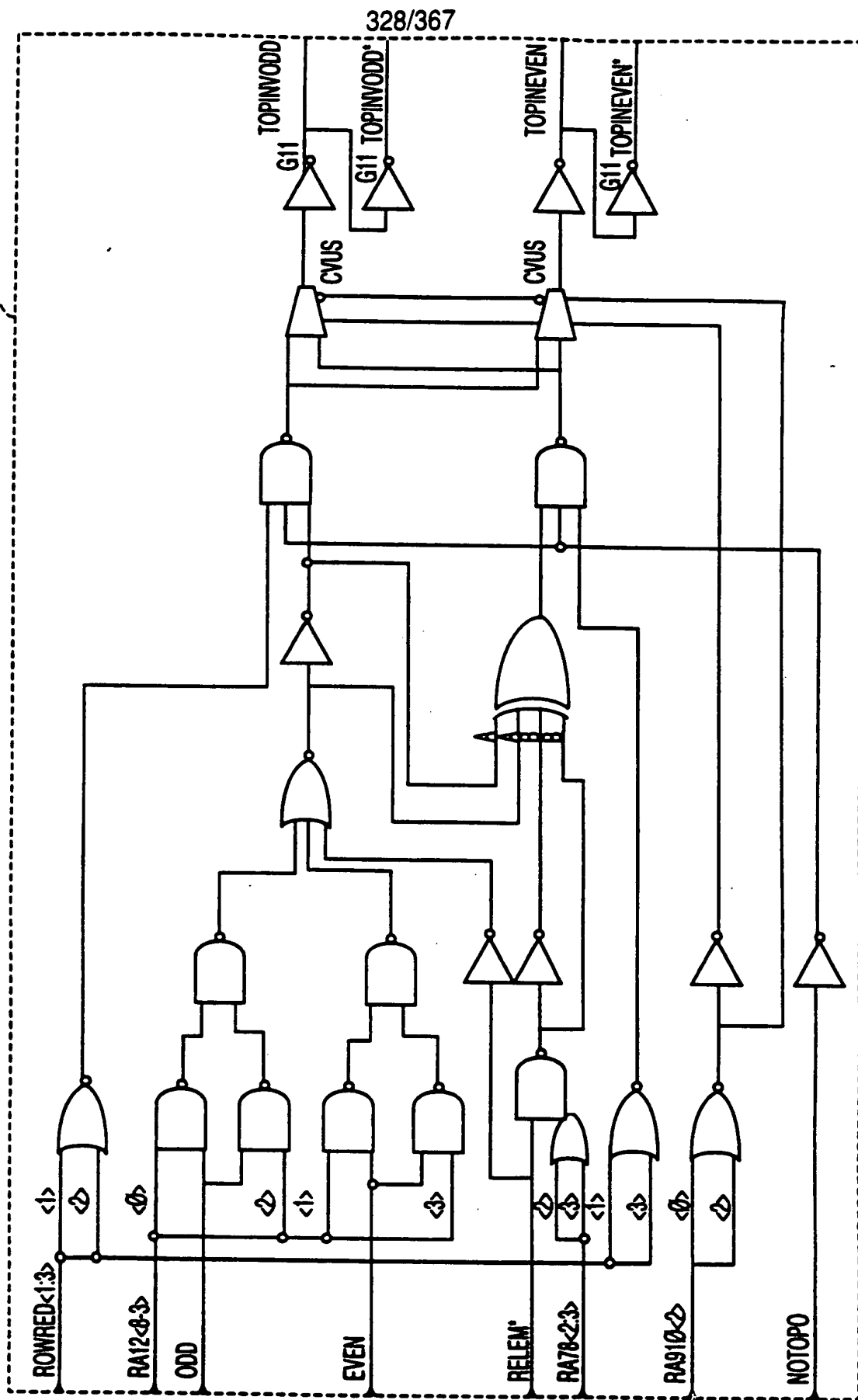


FIG. 95

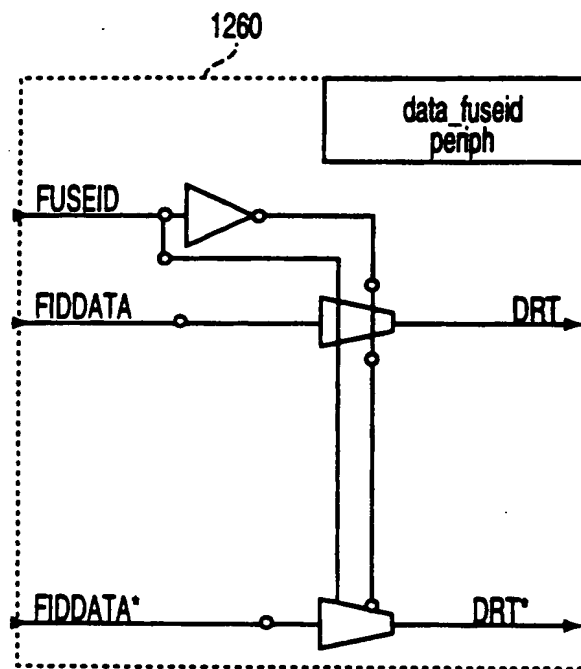
**FIG. 96**

FIG. 97-1

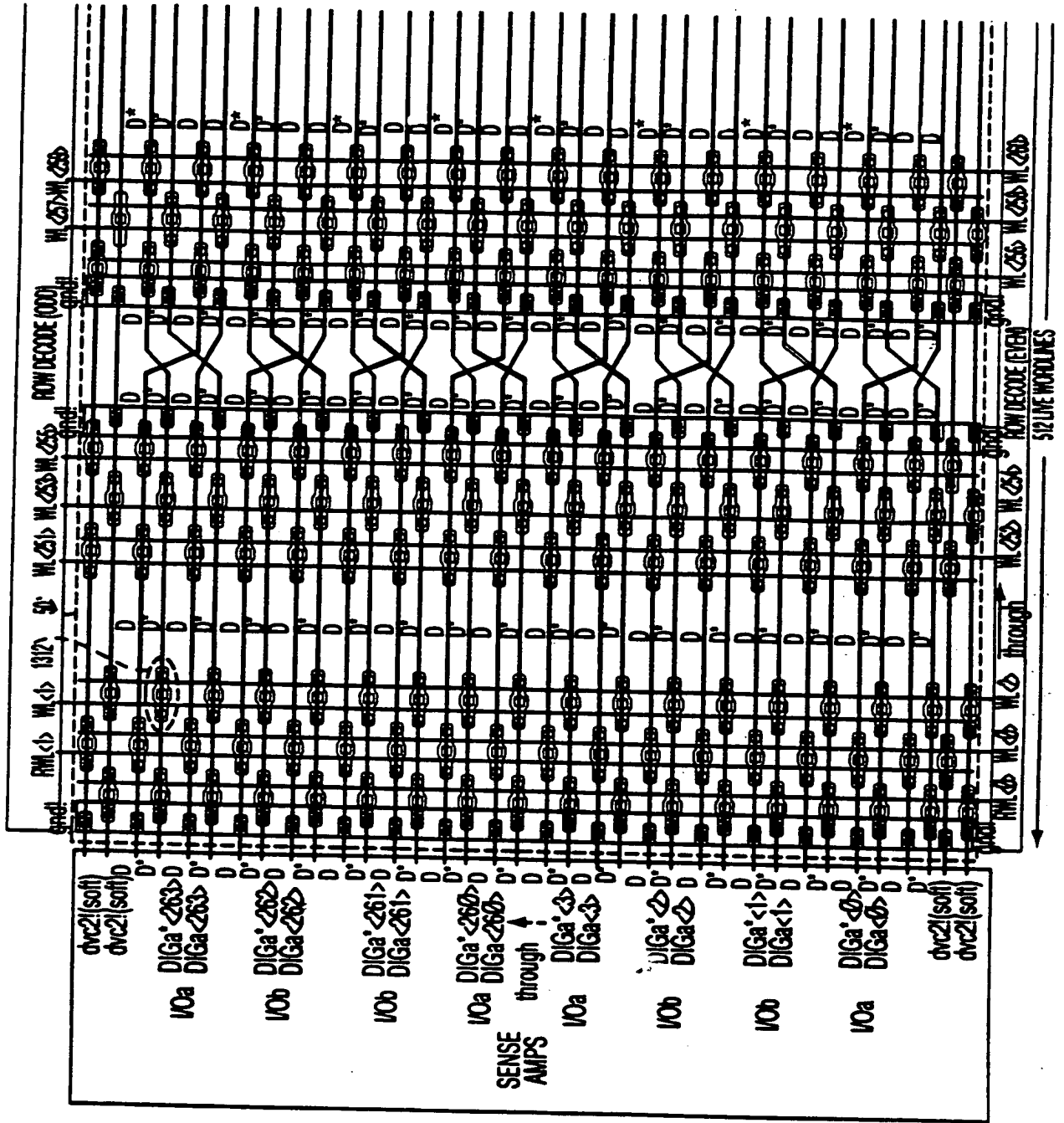


FIG. 97-1

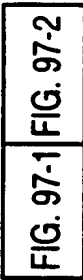
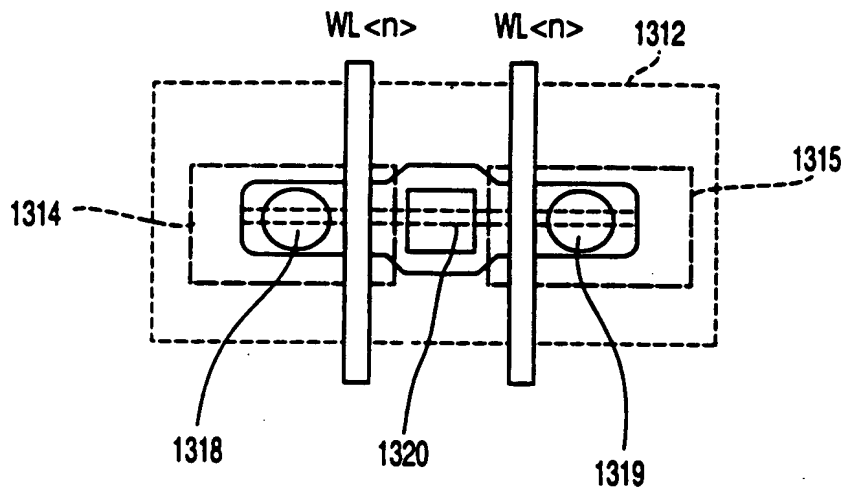


FIG. 97-2

**FIG. 98**

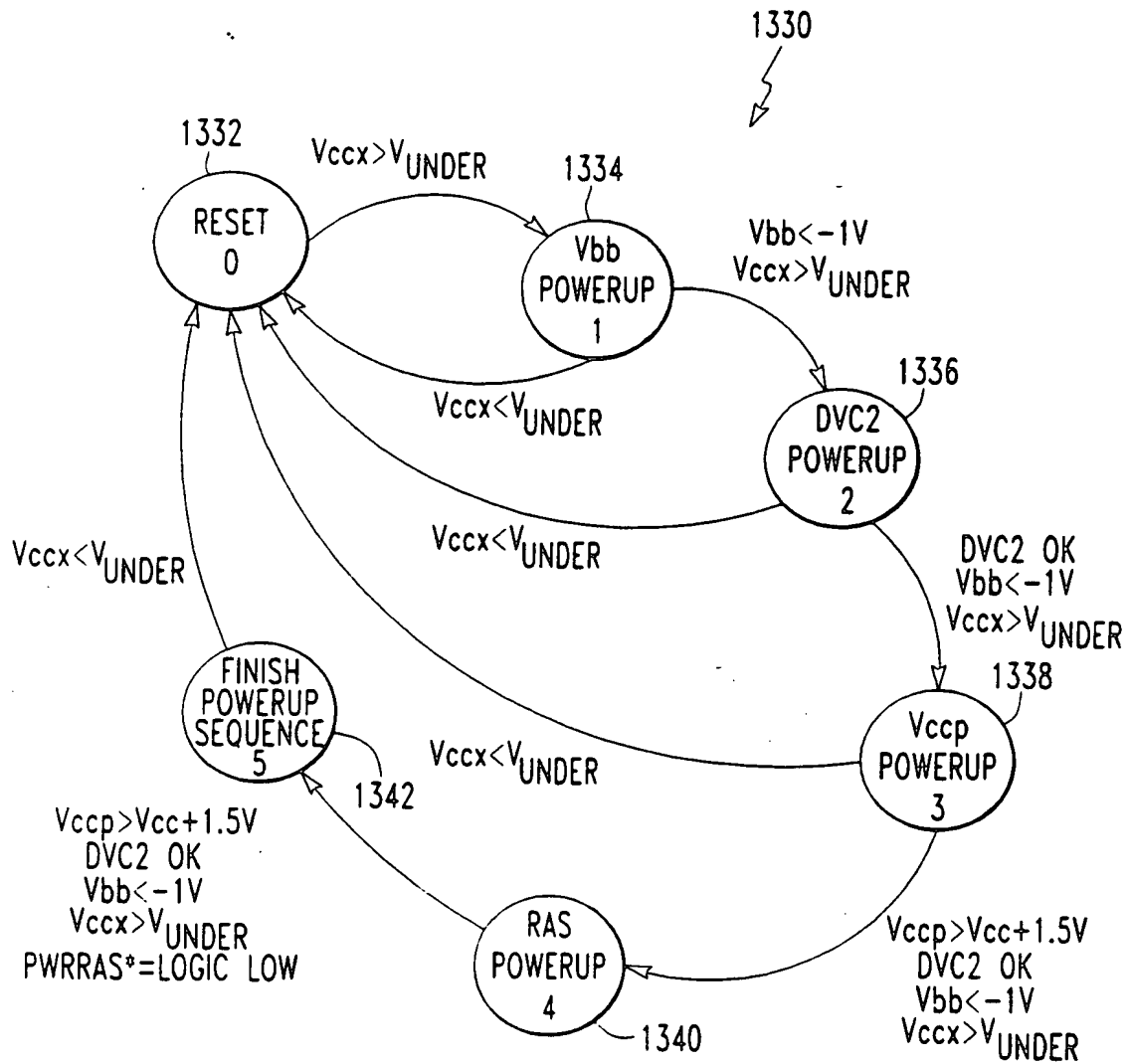


FIG. 99

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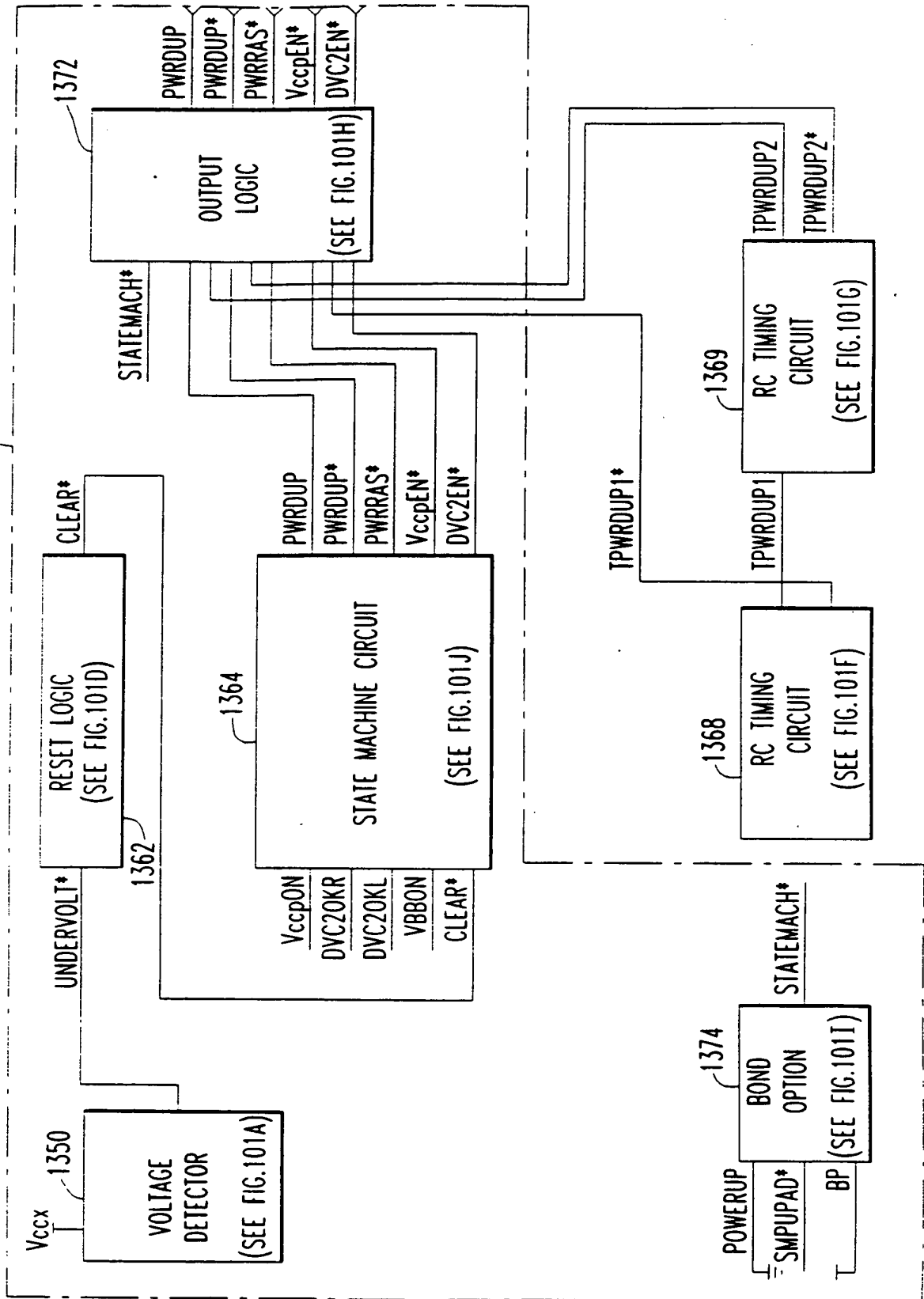


FIG. 100

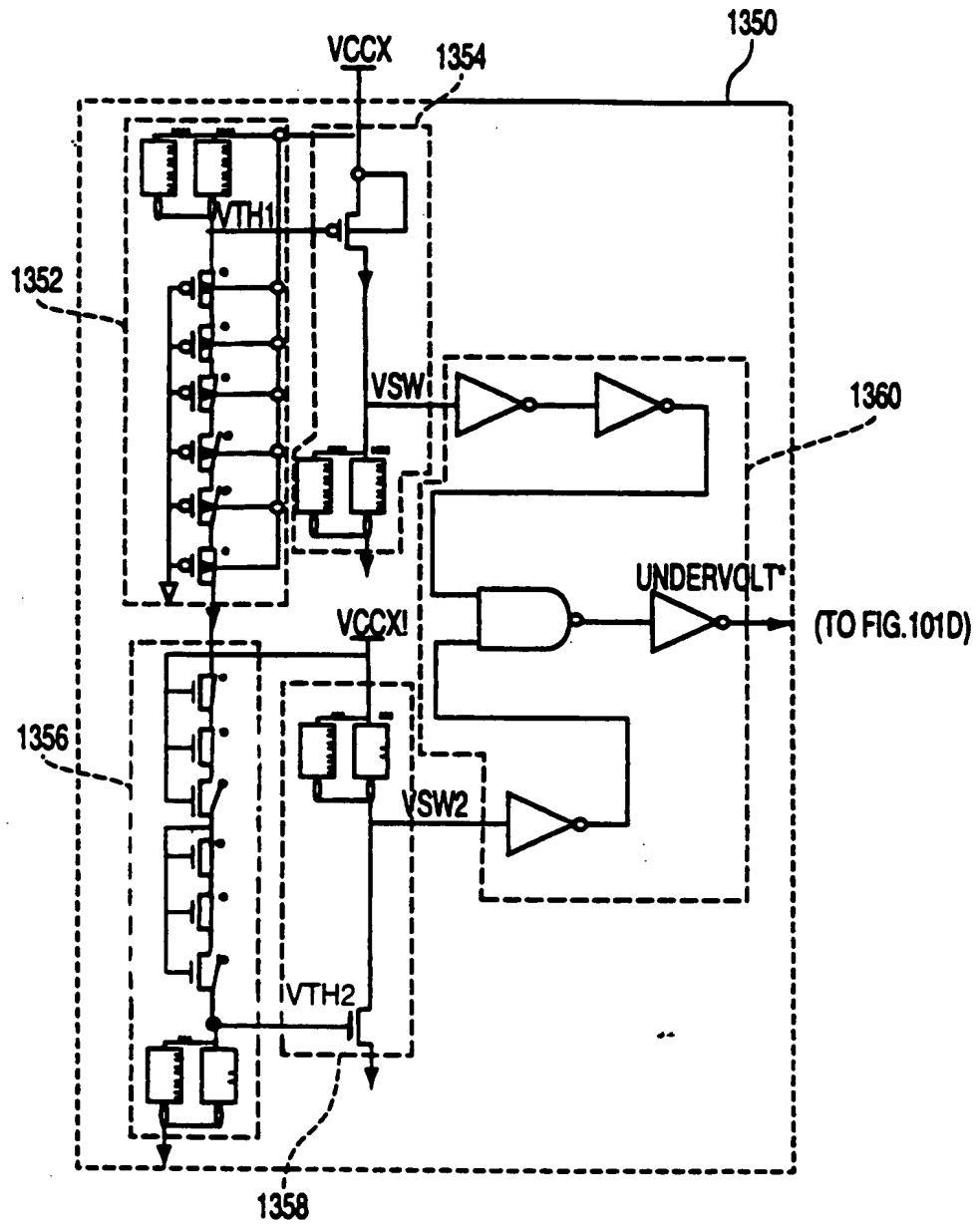
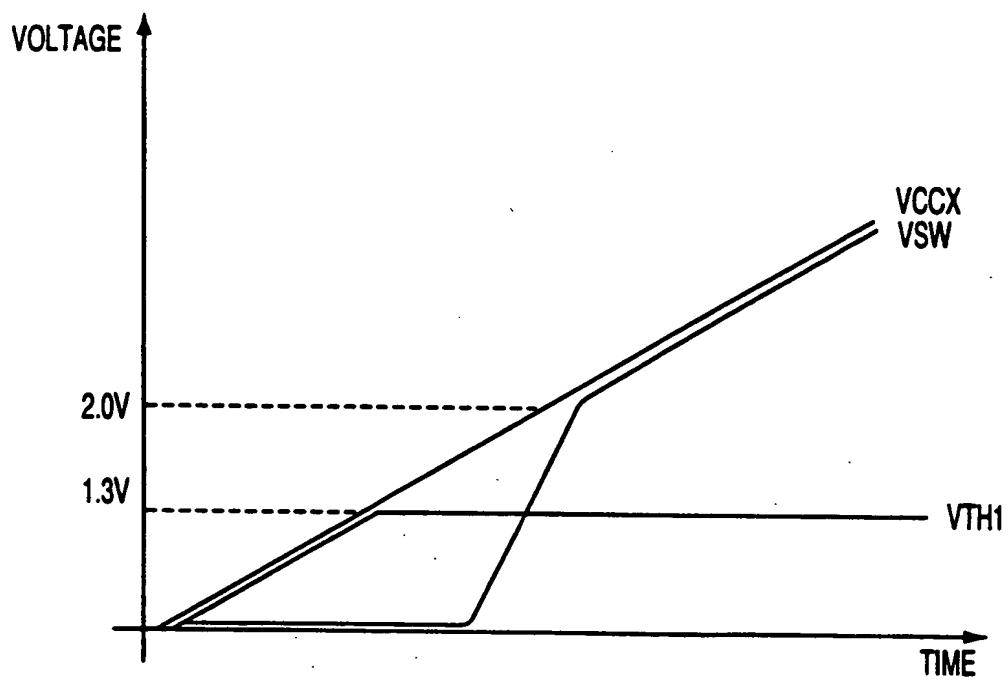
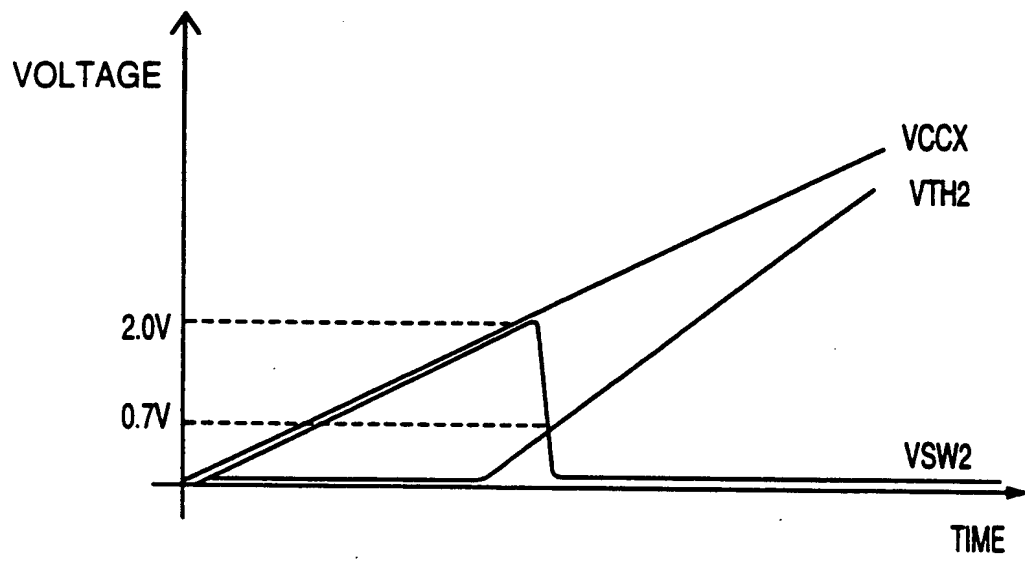


FIG. 101A

**FIG. 101B**

**FIG. 101C**

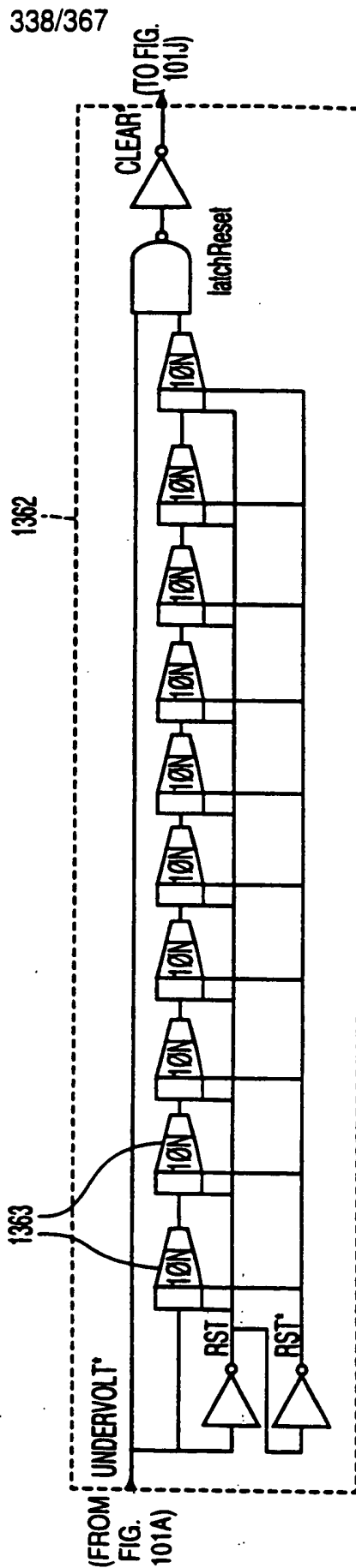


FIG. 101D

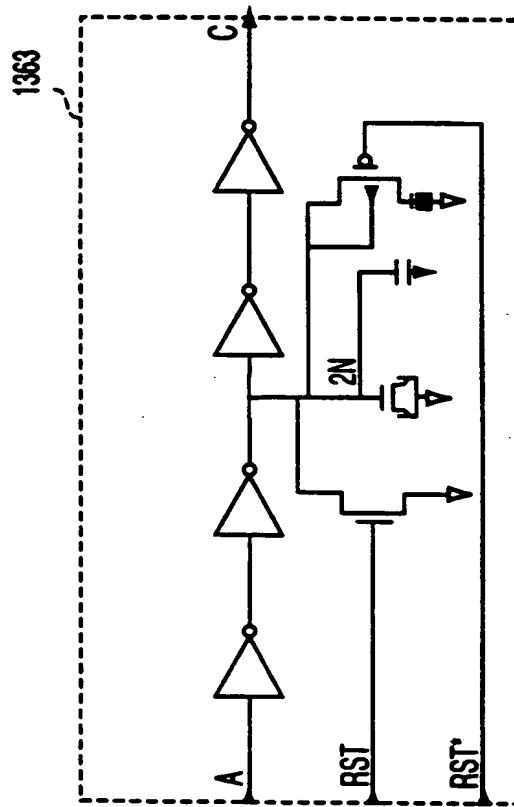


FIG. 101E

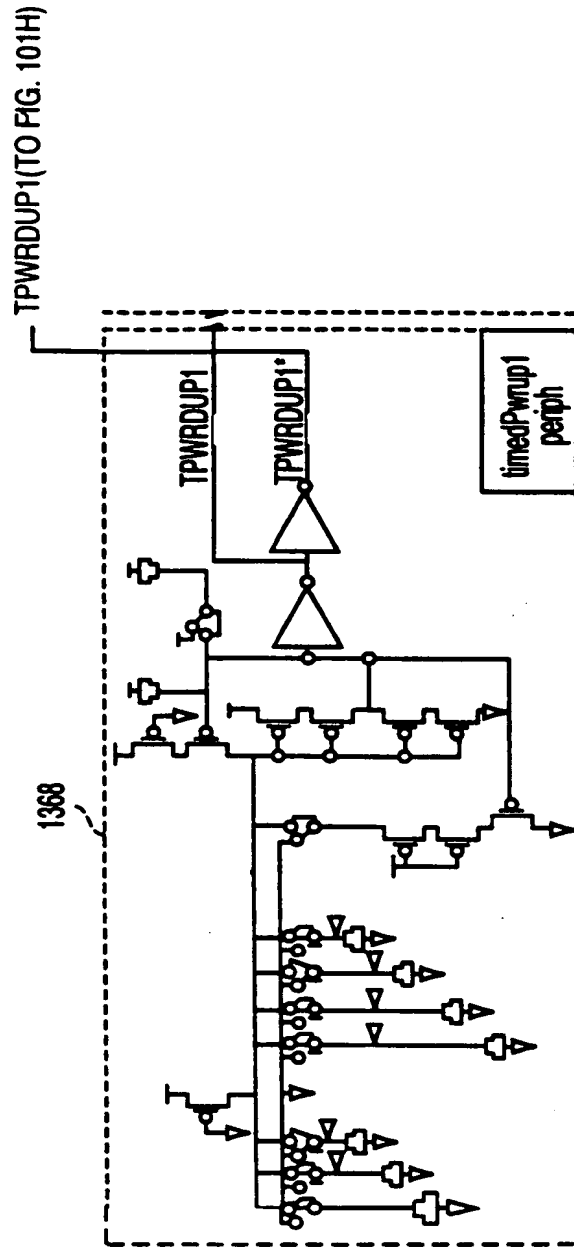


FIG. 101F

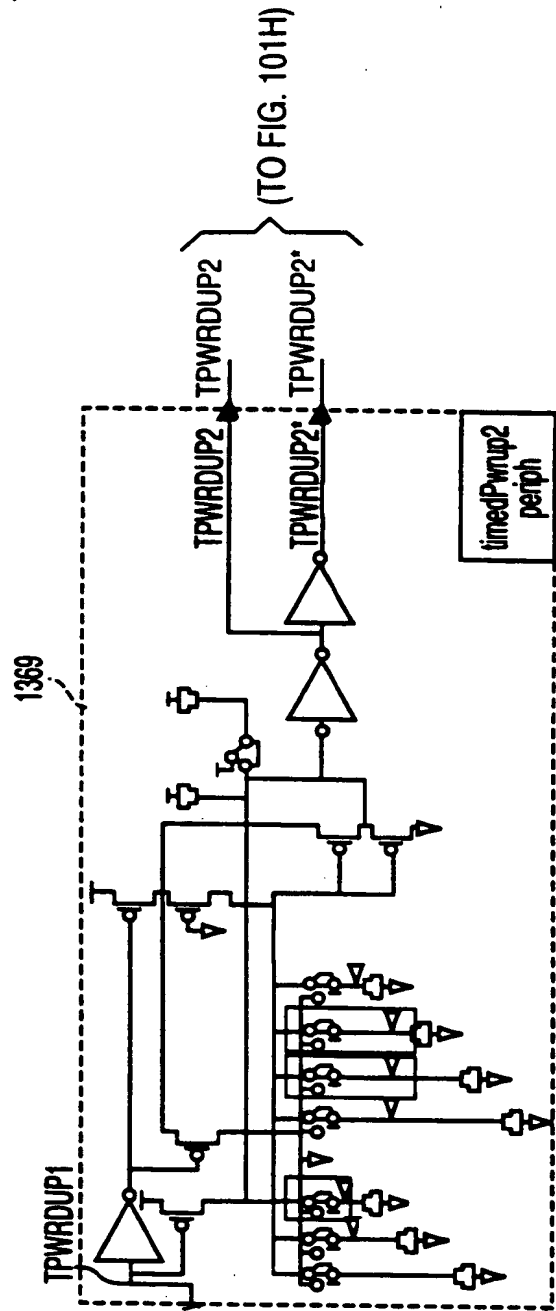
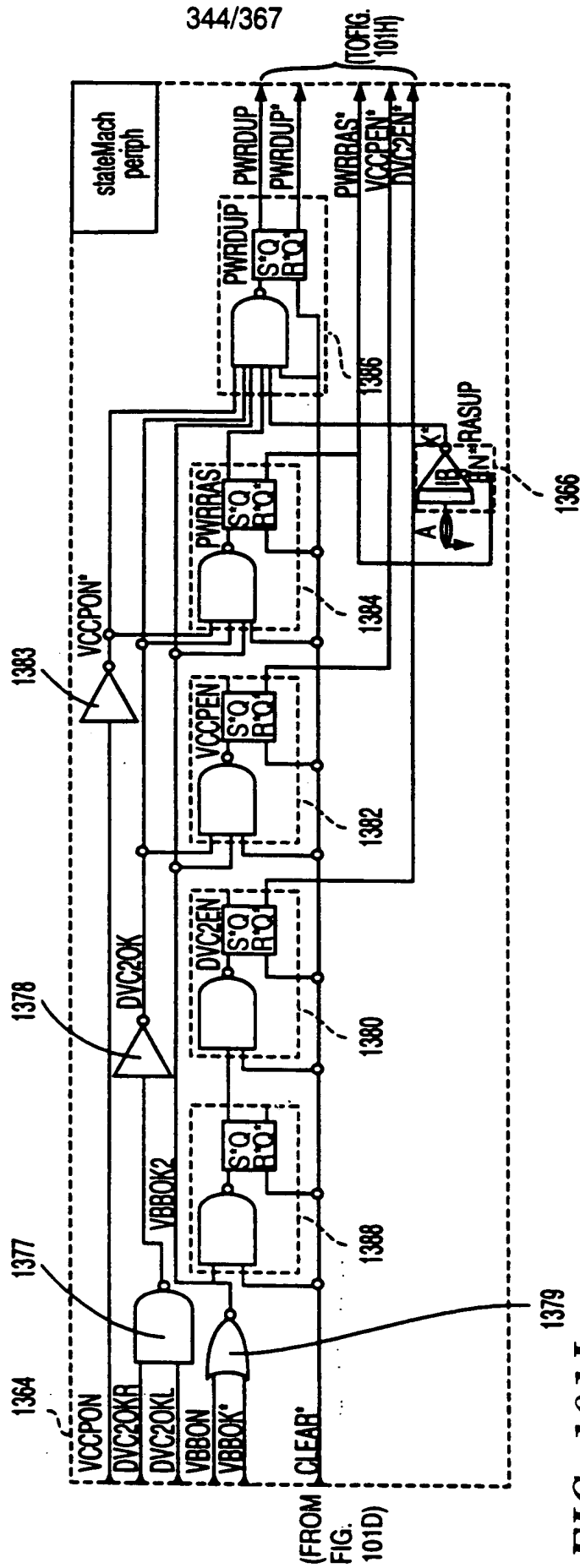
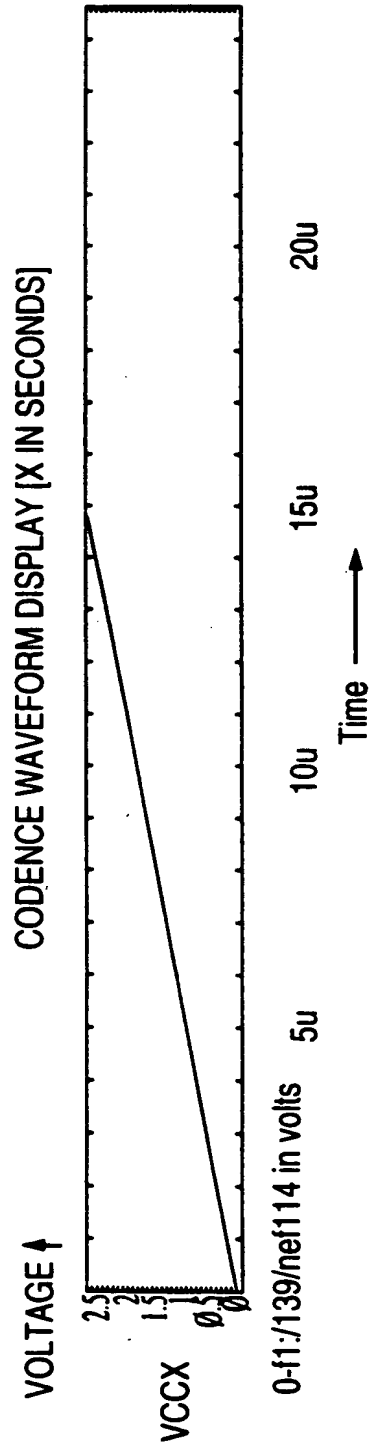


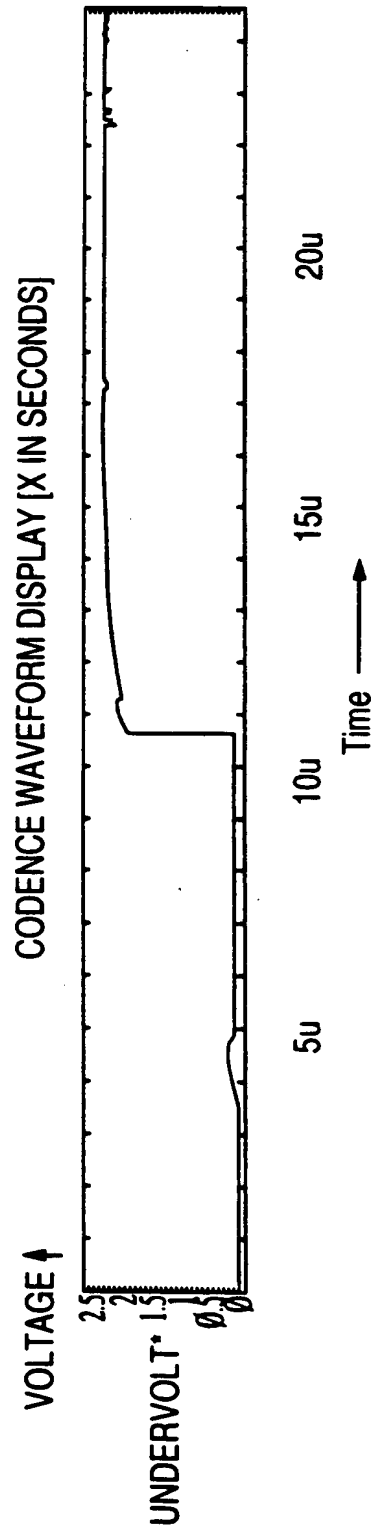
FIG. 101G





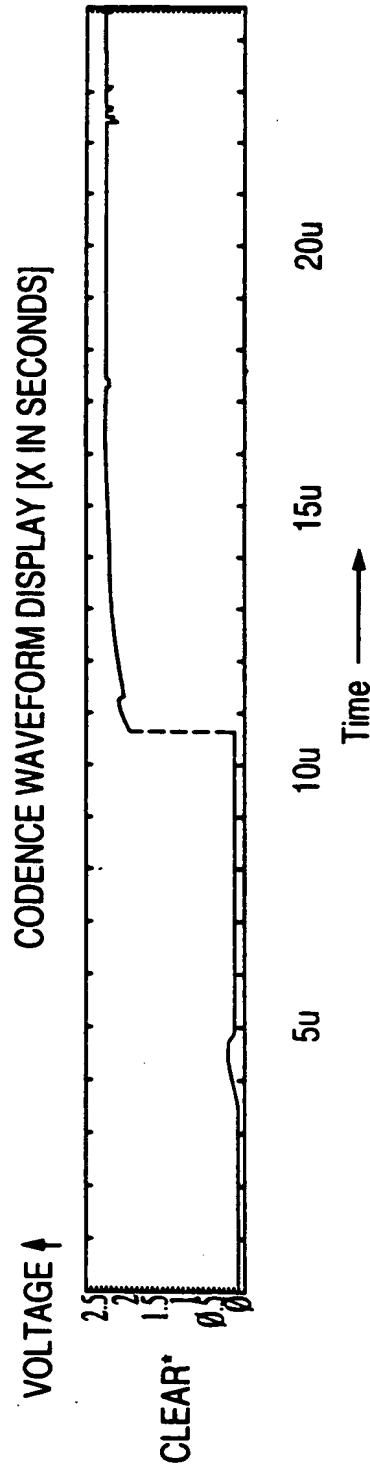
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102A



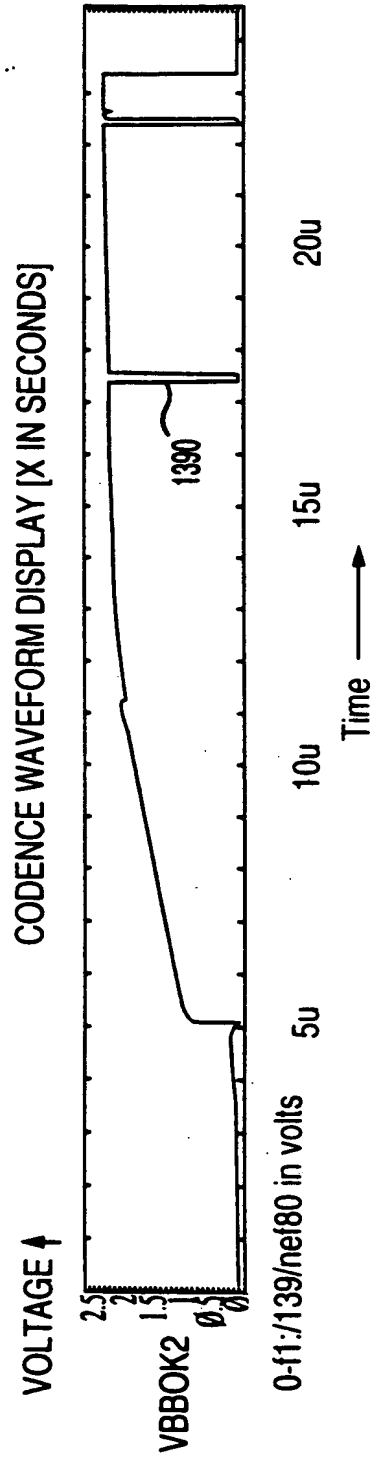
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102B



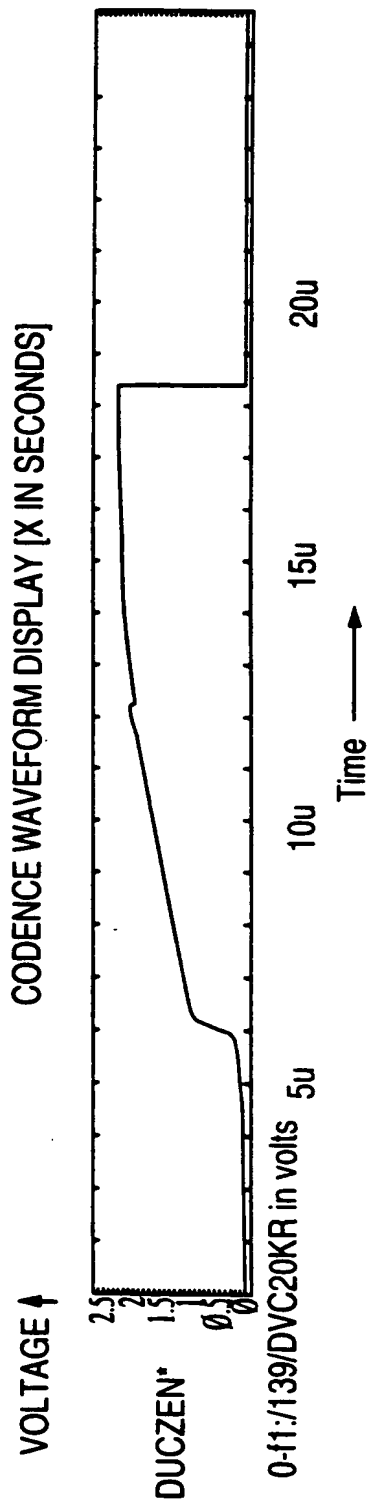
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102C



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102D



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102E

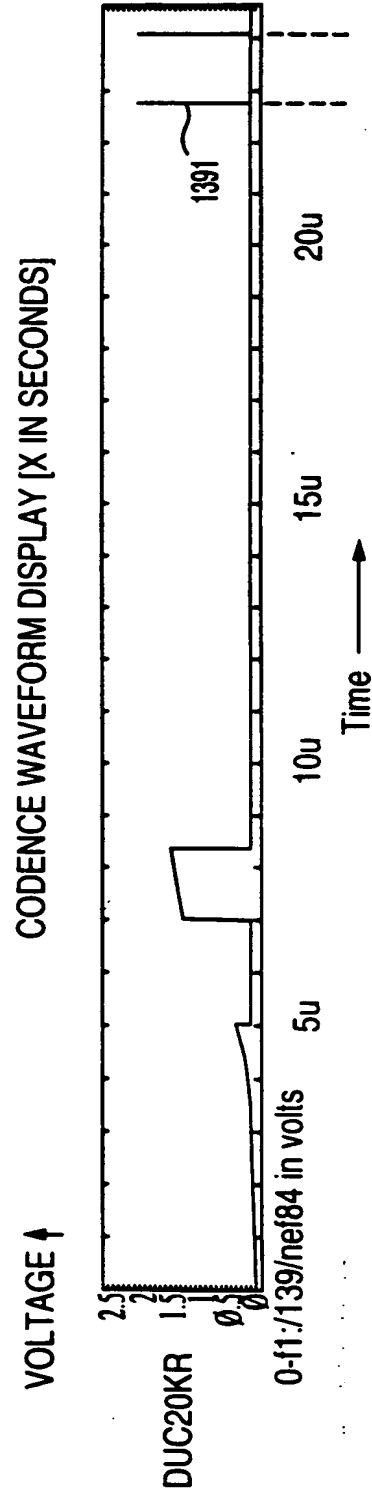
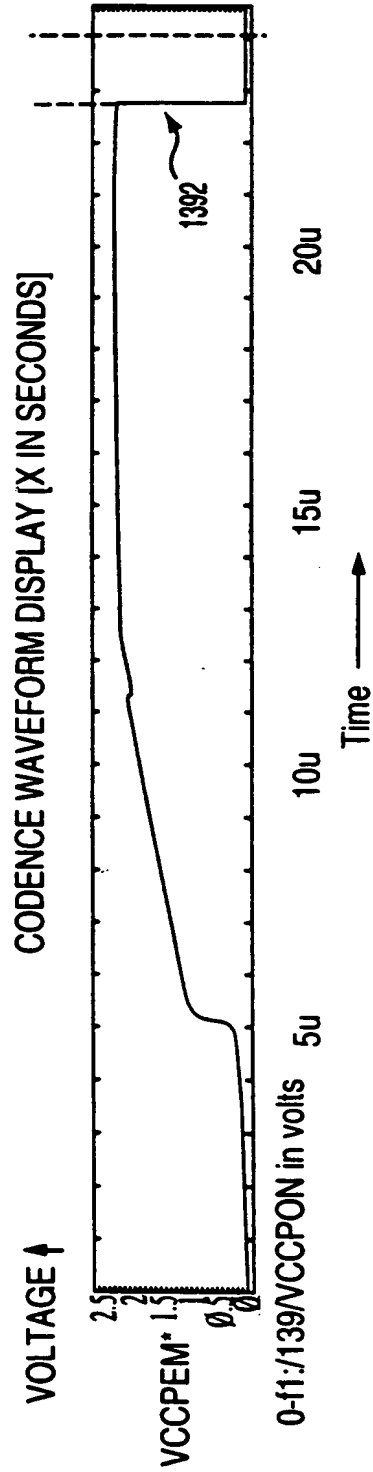


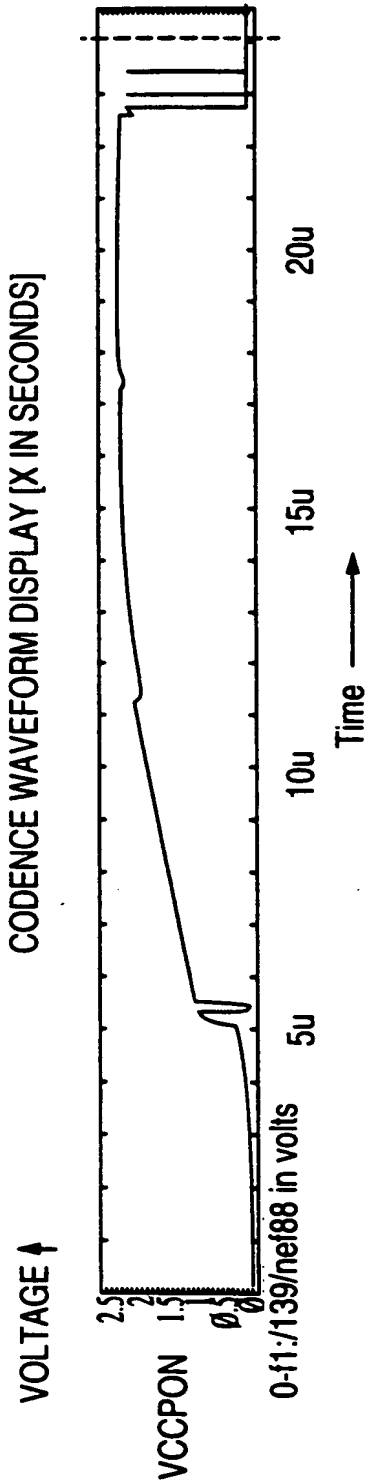
FIG. 102F

TOP SECRET



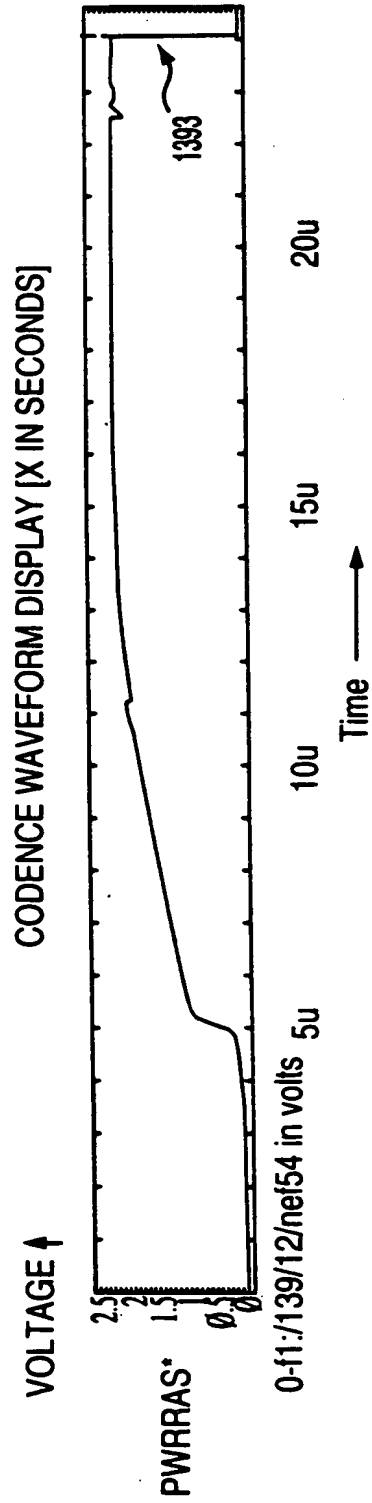
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102C



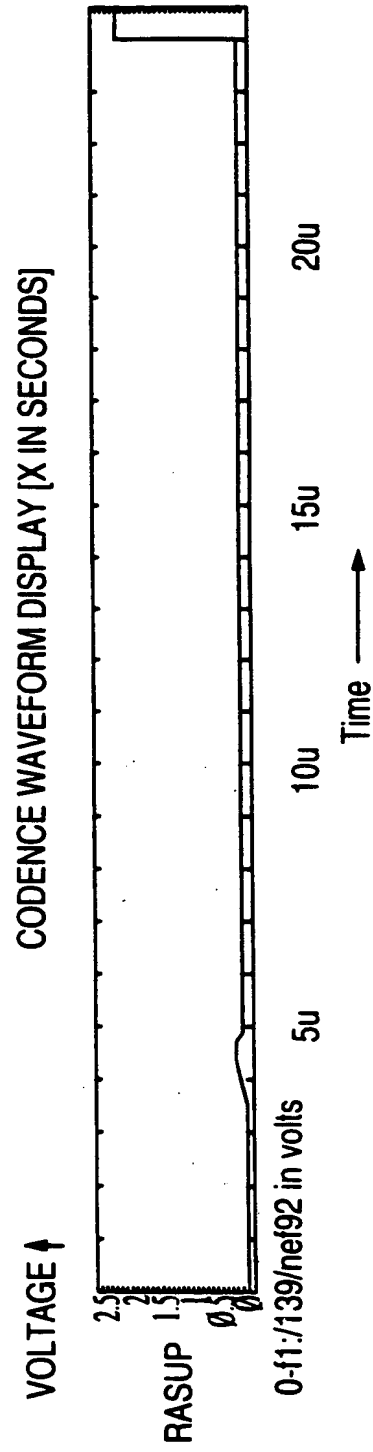
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102H



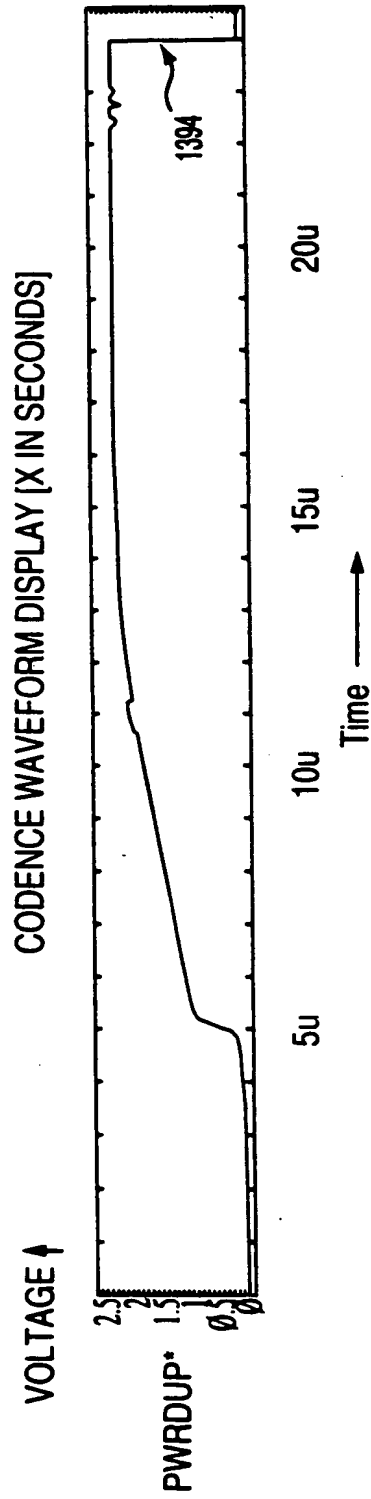
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102I



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102J



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102K

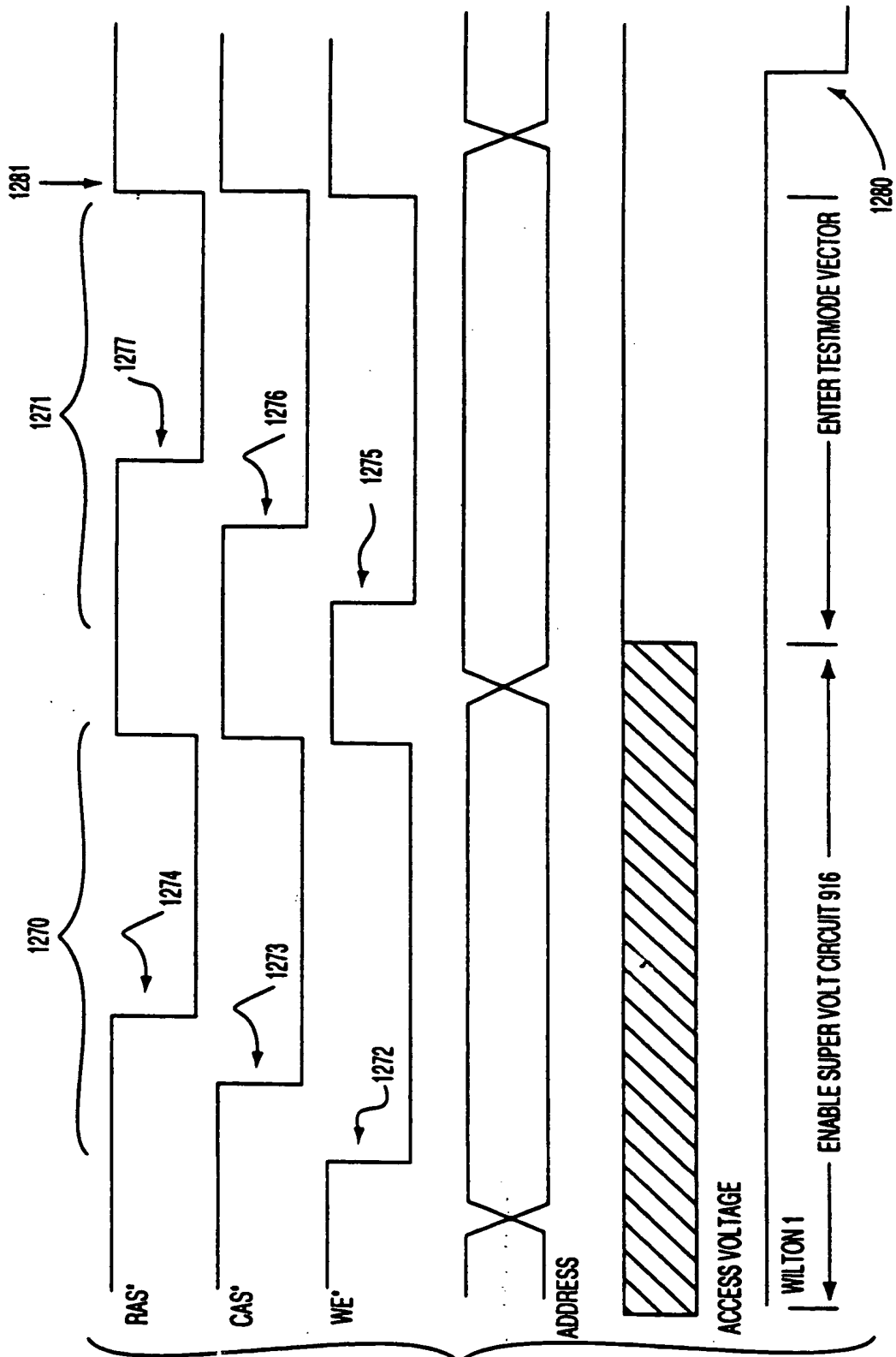
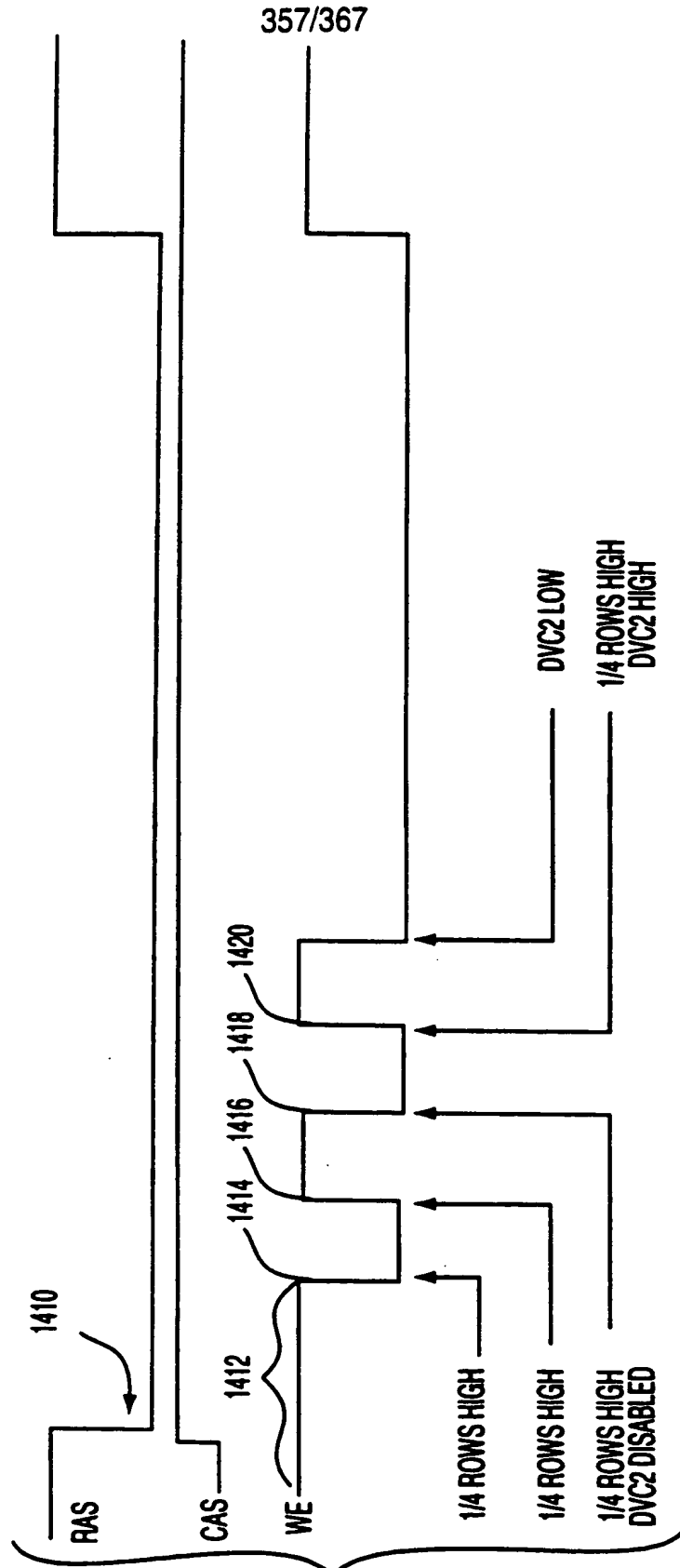


FIG.
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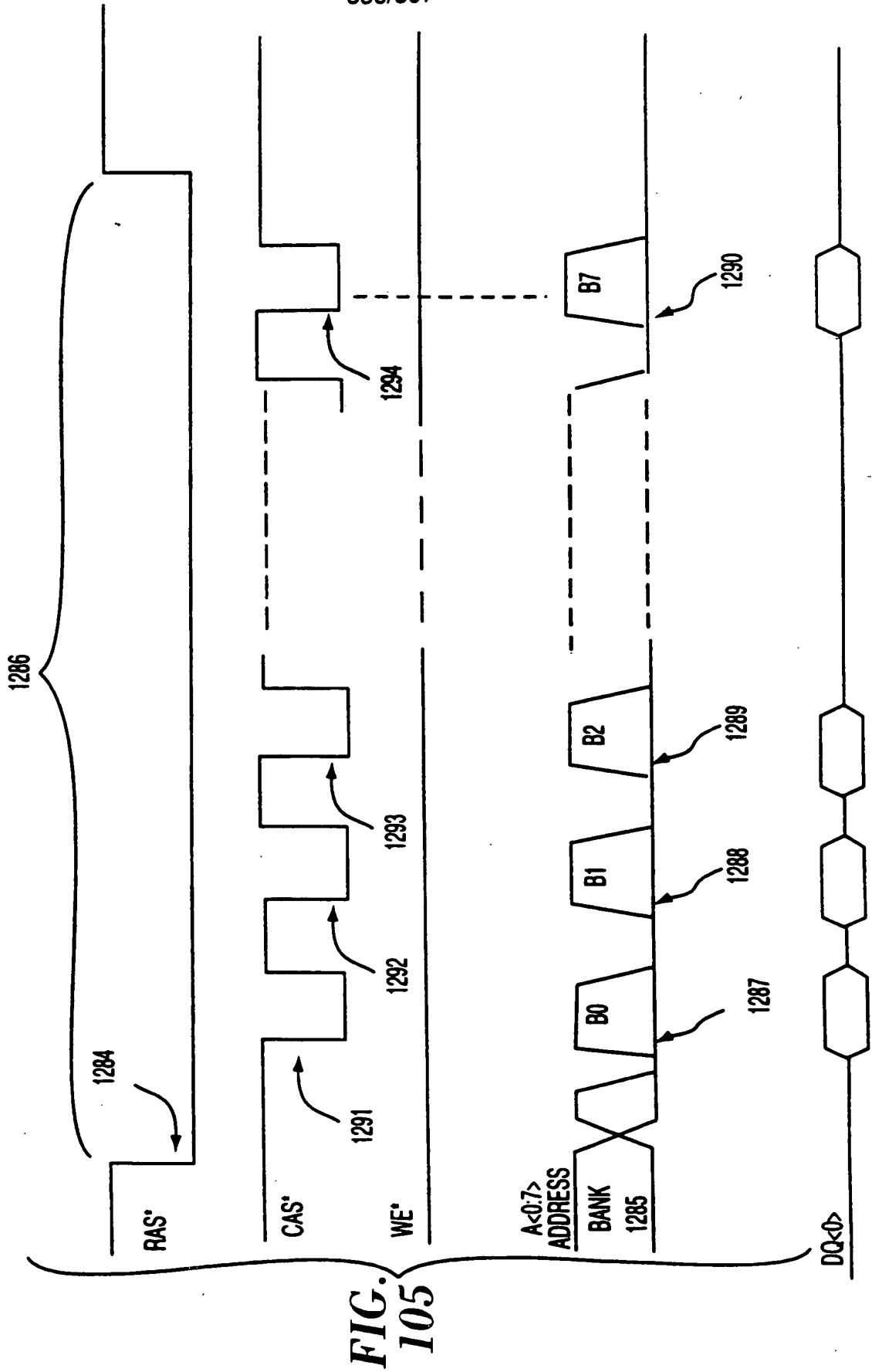


FIG. 106

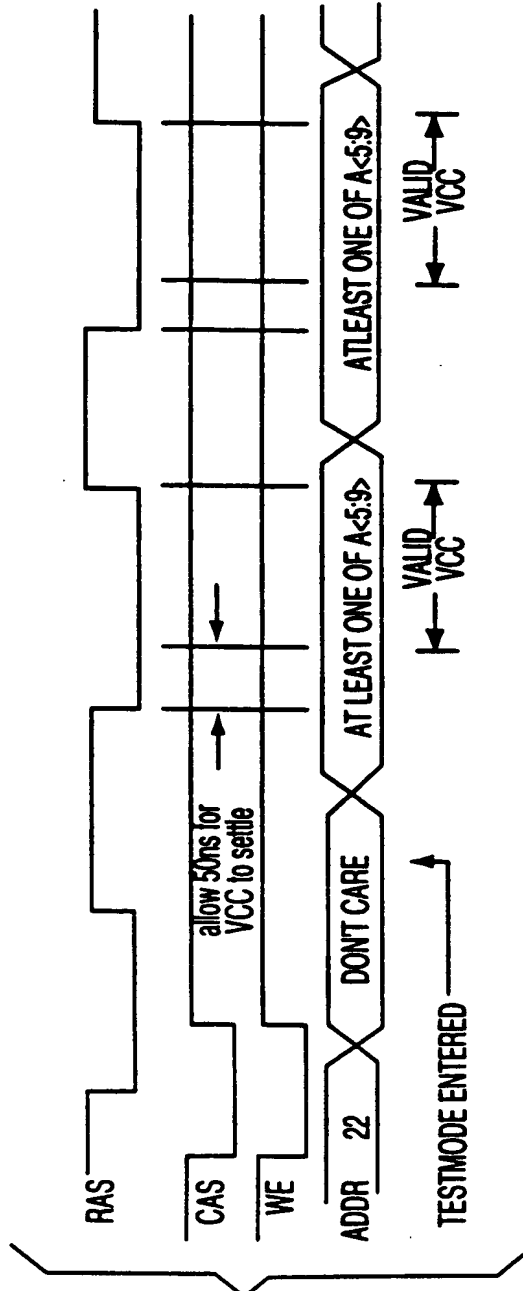


FIG. 106

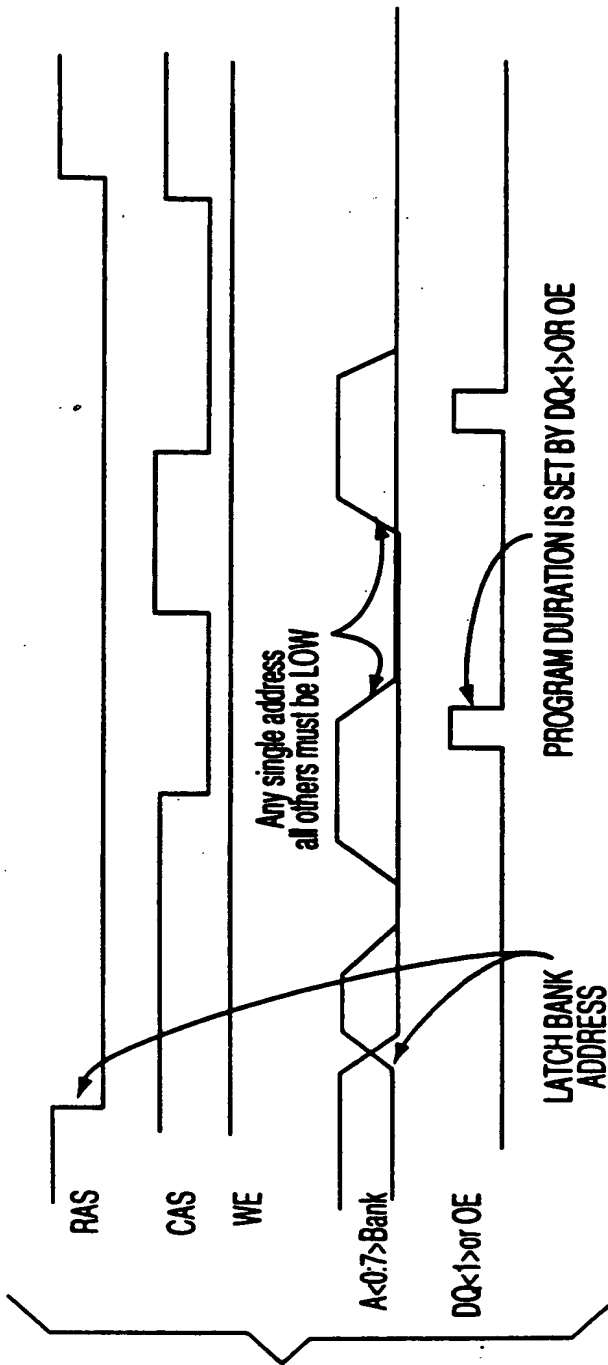


FIG. 107

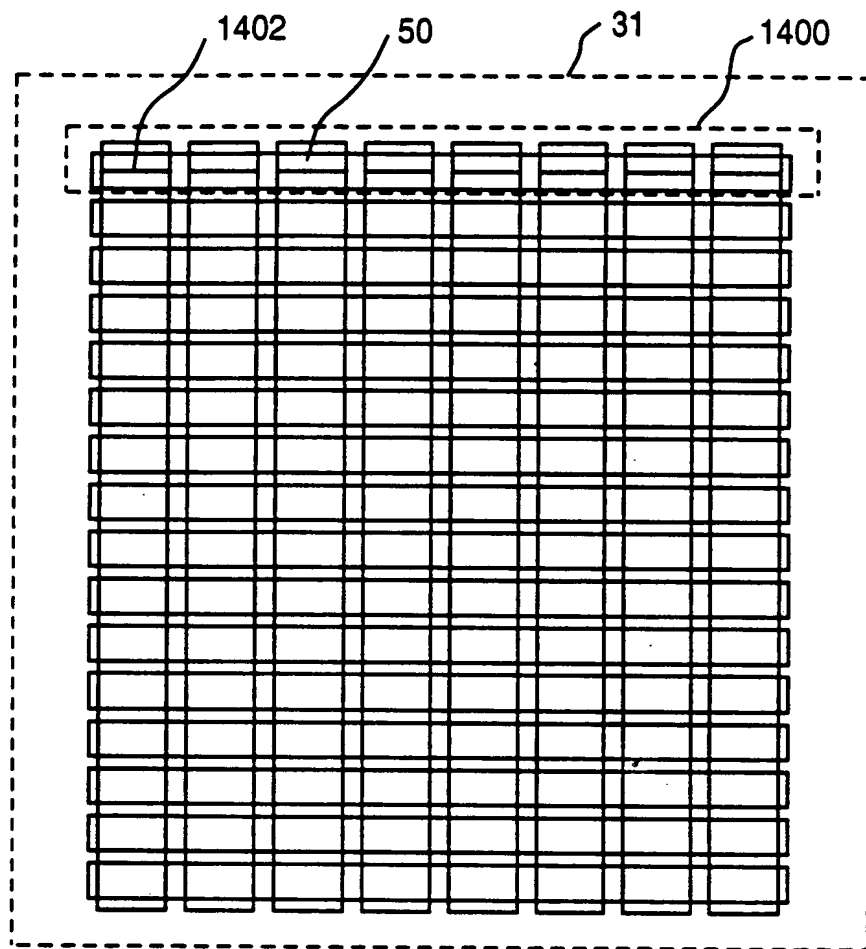
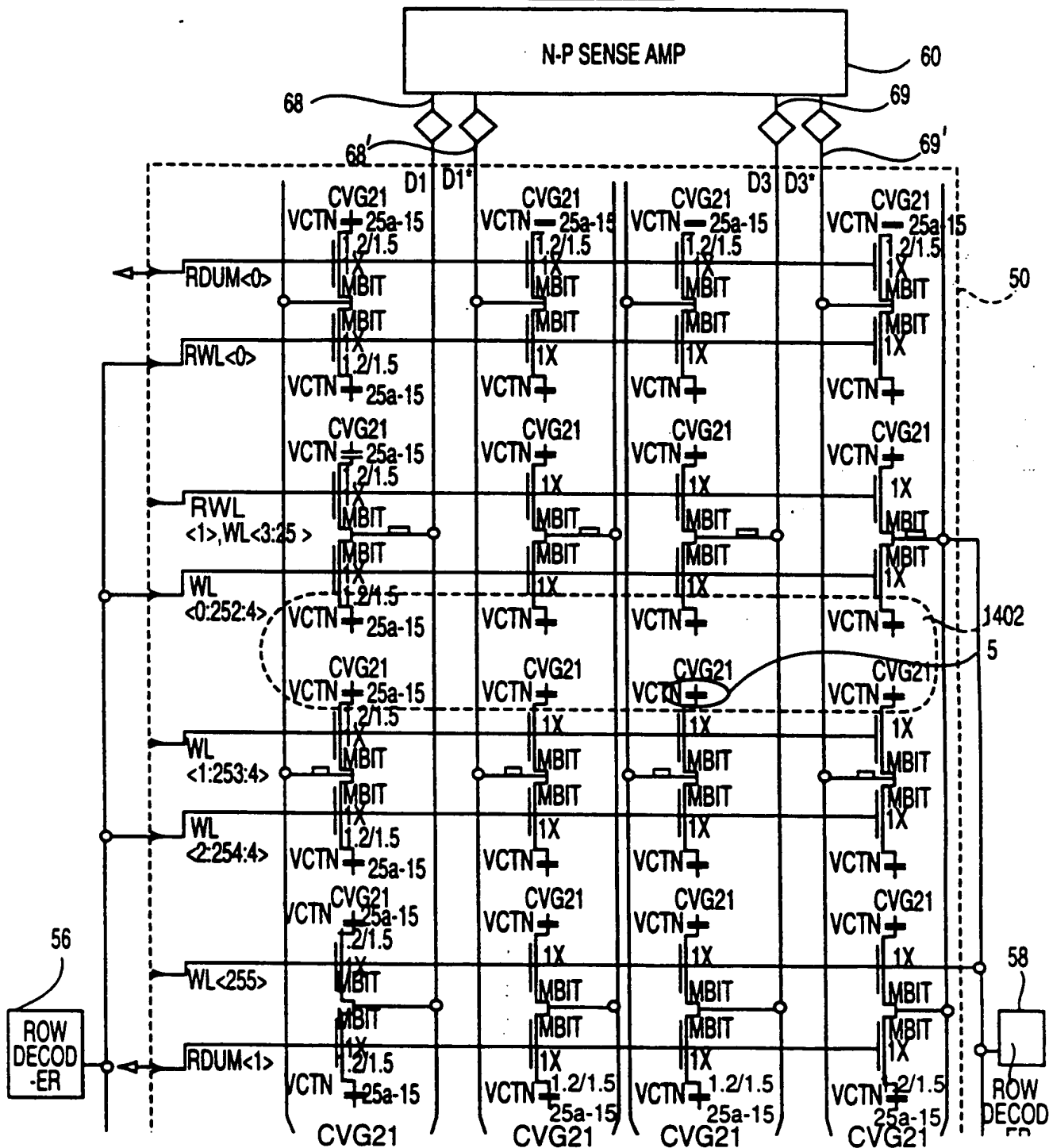


FIG. 108

FIG. 109-1

FIG. 109-1

FIG. 109-2



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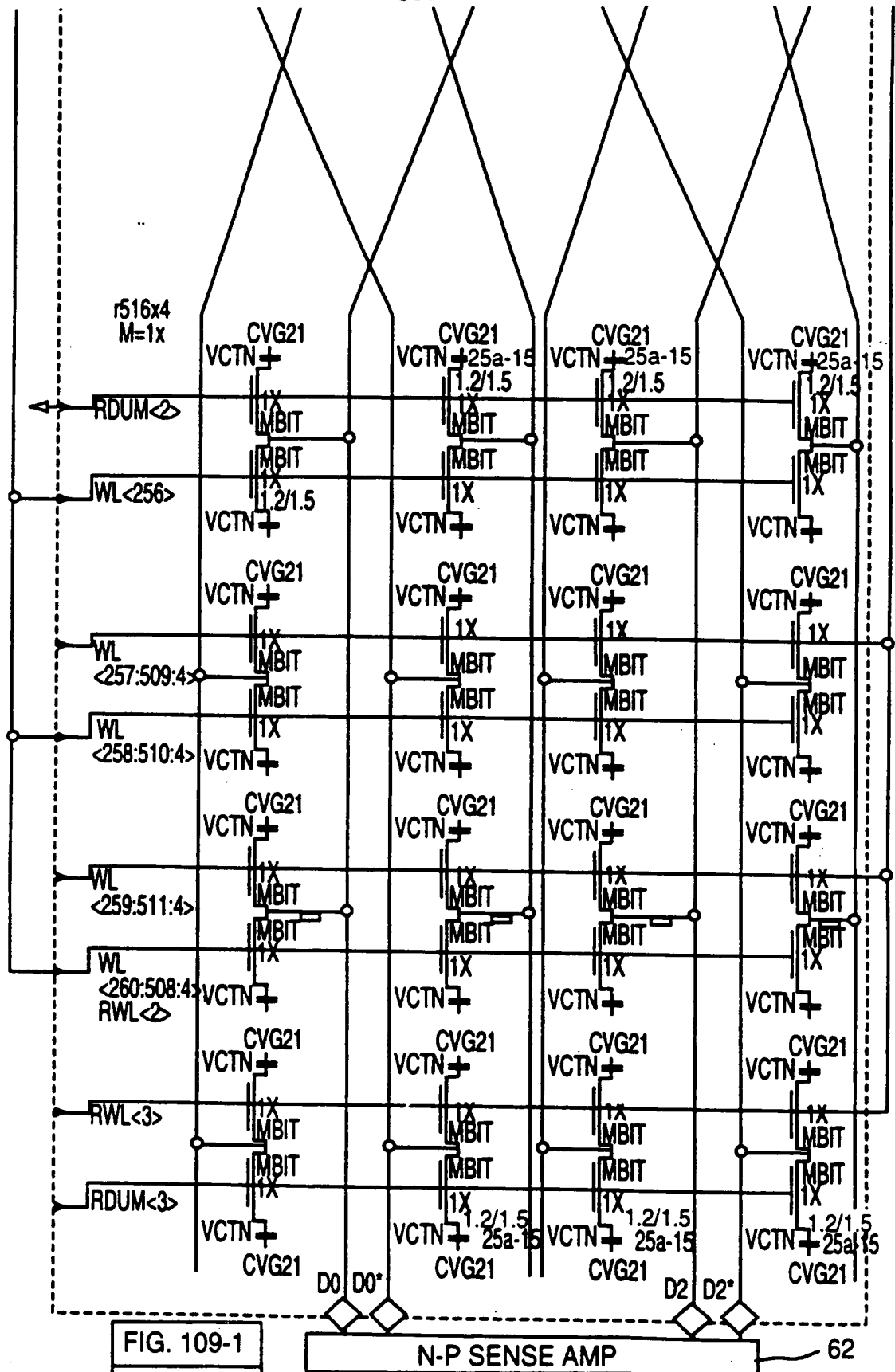


FIG. 109-2

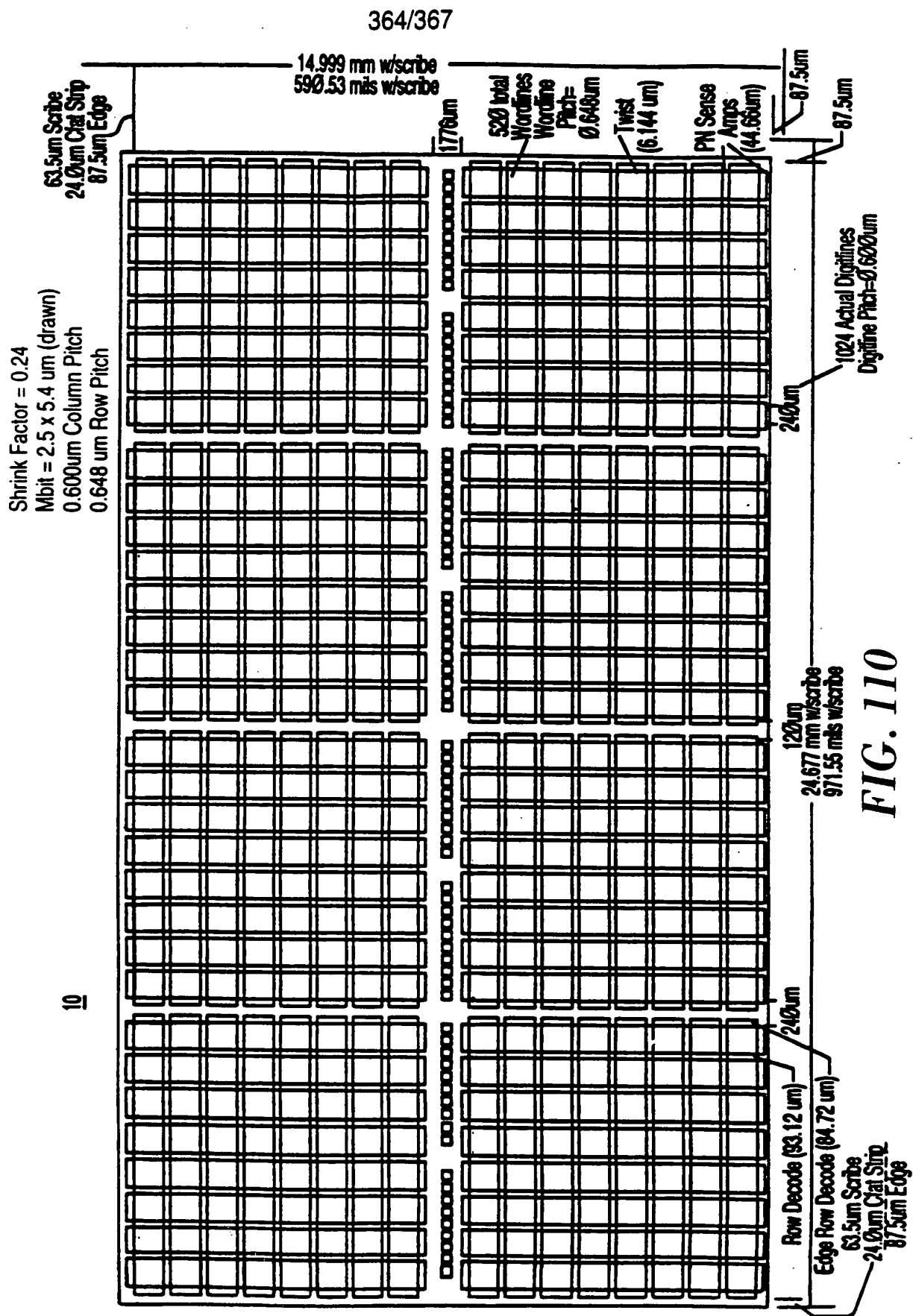


FIG. 110

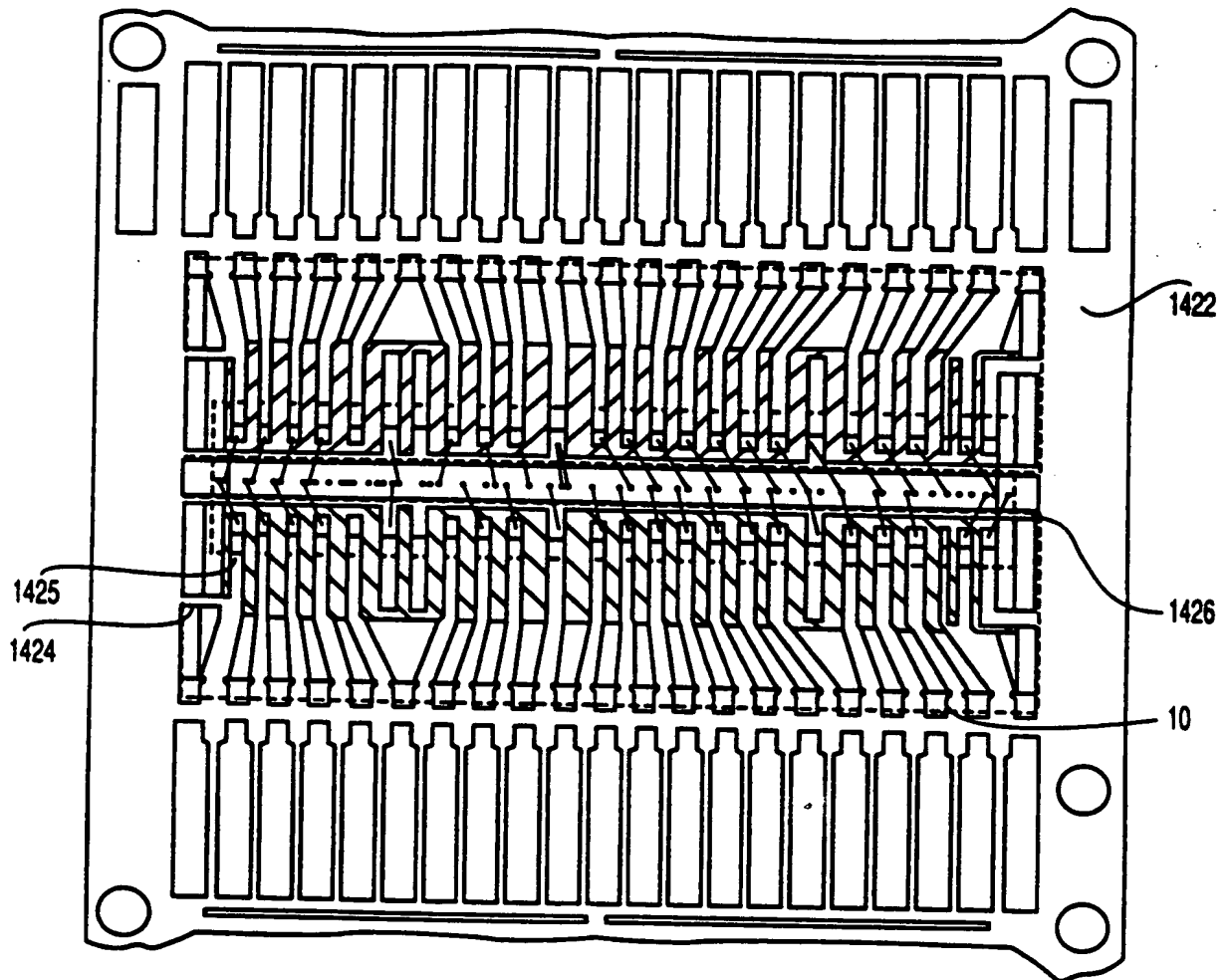


FIG. 111

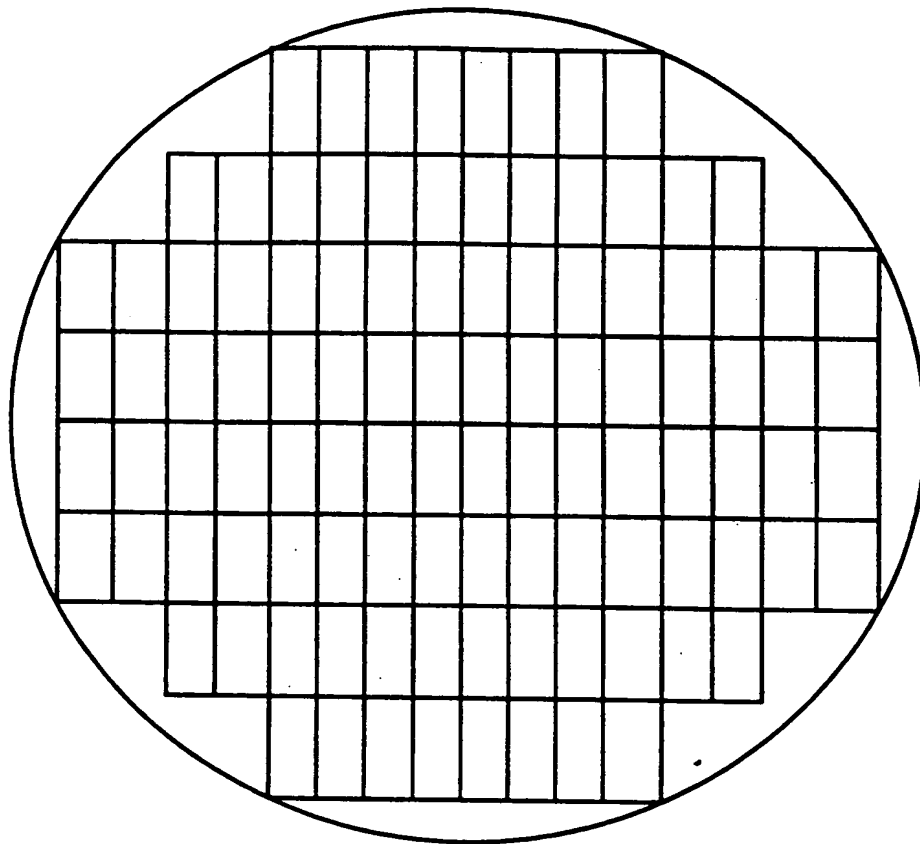
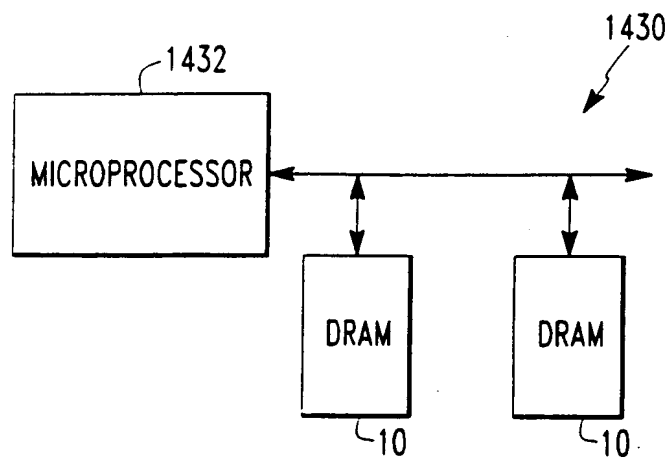


FIG. 112

402230 636680

*FIG. 113*